S8205A

Dual N-ch 20V Fast Switching MOSFETs

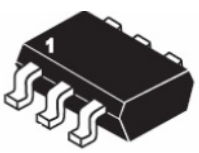
V_{DSS}	20	V
I_D	6	A
$R_{DS(ON)}$	20	mΩ

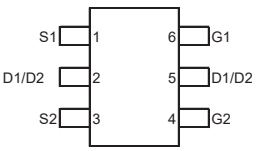
Features

Green Device Available
Super Low Gate Charge
Excellent Cdv/dt effect decline
Advanced high cell density Trench technology

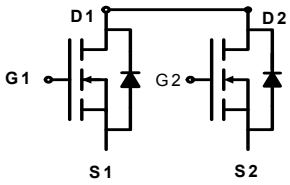
8205A

SOT 23 6L Pin Configurations





Equivalent Circuit



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings (@ T_a = 25°C unless otherwise specified)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	±12	V
Drain Current-Continuous	I_D	6	A
Drain Current-Pulsed (Note 1)	I_{DM}	25	A
Maximum Power Dissipation	P_D	1.25	W
Operating Junction and Storage Temperature Range	T_J,T_{STG}	-55 To 150	℃

Thermal Characteristic

Thermal Resistance,Junction-to-Ambient (Note 2)	$R_{\theta JA}$	100	℃/W

Electrical Characteristics (T_a=25℃unless otherwise noted)

Gate-Body Leakage Current	I_{GSS}	V_{GS}=±10V,V_{DS}=0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS},I_D=250μA	0.5	0.7	1.2	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	V_{GS}=4.5V, I_D=4A	-	20	25	mΩ
		V_{GS}=2.5V, I_D=3A	-	27	37	mΩ
Forward Transconductance	g_{FS}	V_{DS}=5V,I_D=4A	-	10	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C_{lss}	V_{DS}=8V,V_{GS}=0V, F=1.0MHz	-	600	-	PF
Output Capacitance	C_{oss}		-	330	-	PF
Reverse Transfer Capacitance	C_{rss}		-	140	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	V_{DD}=10V,I_D=1A V_{GS}=4V,R_{GEN}=10Ω	-	18	-	nS
Turn-on Rise Time	t_r		-	5	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	43	-	nS
Turn-Off Fall Time	t_f		-	20	-	nS
Total Gate Charge	Q_g	V_{DS}=10V,I_D=4A, V_{GS}=4.5V	-	11	-	nC
Gate-Source Charge	Q_{gs}		-	2.3	-	nC
Gate-Drain Charge	Q_{gd}		-	2.5	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	V_{GS}=0V,I_S=2A	-	0.8	1.2	V
Diode Forward Current (Note 2)	I_S		-	-	2	A

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production

RATING AND CHARACTERISTIC CURVES

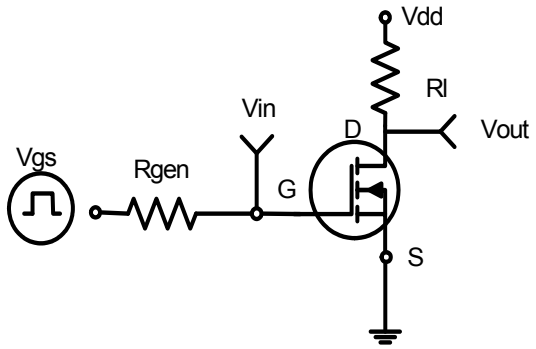


Figure 1:Switching Test Circuit

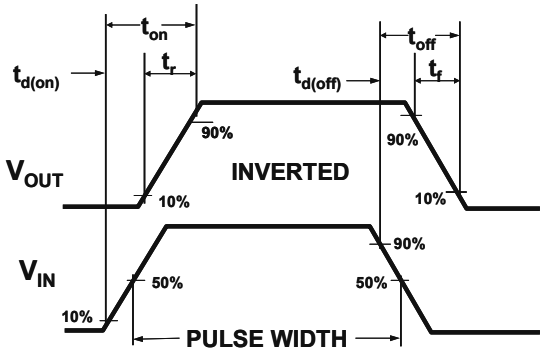


Figure 2:Switching Waveforms

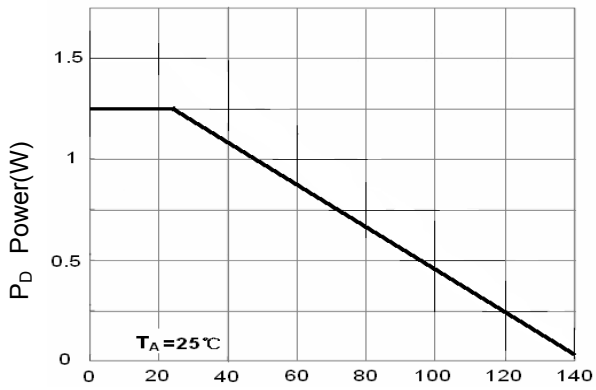


Figure 3 Power Dissipation

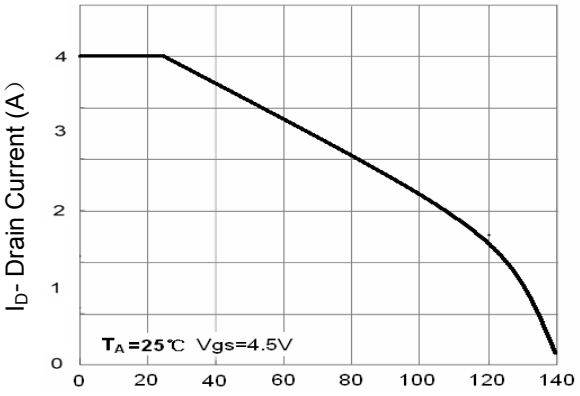


Figure 4 Drain Current

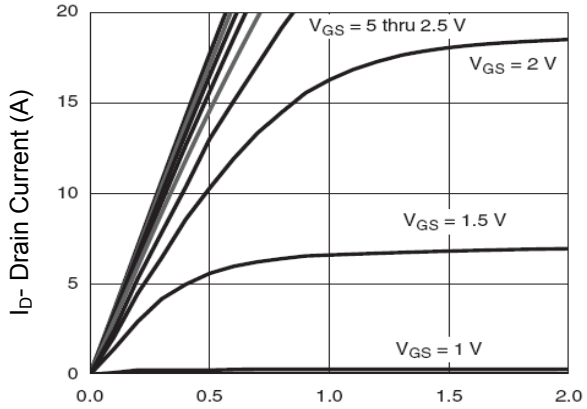


Figure 5 Output Characteristics

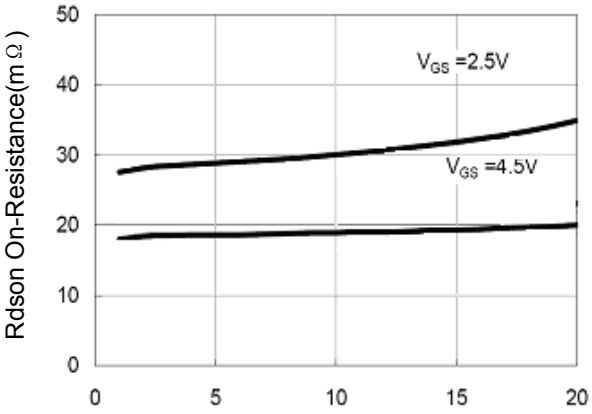


Figure 6 Drain-Source On-Resistance

RATING AND CHARACTERISTIC CURVES

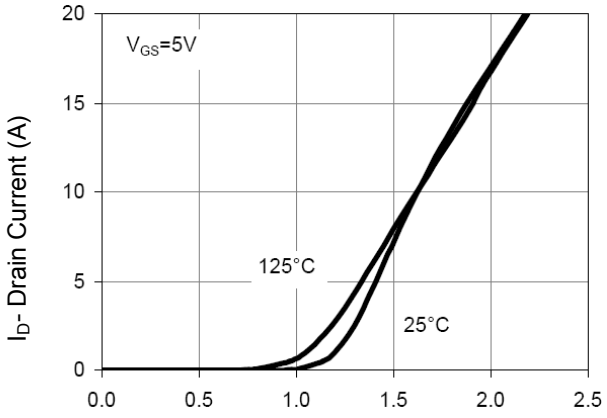


Figure 7 Transfer Characteristics

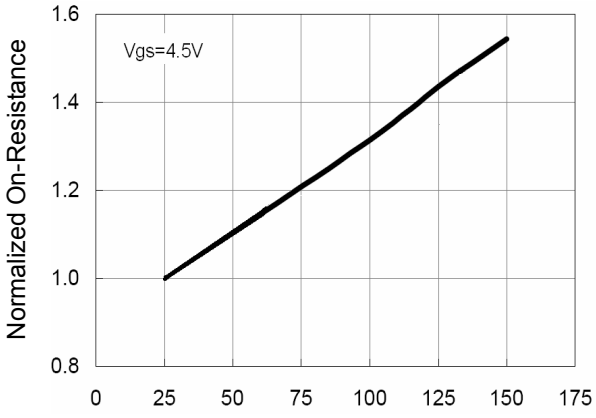


Figure 8 Drain-Source On-Resistance

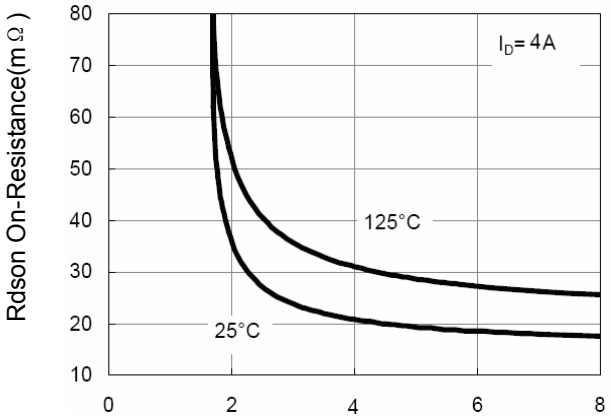


Figure 9 Rdson vs Vgs

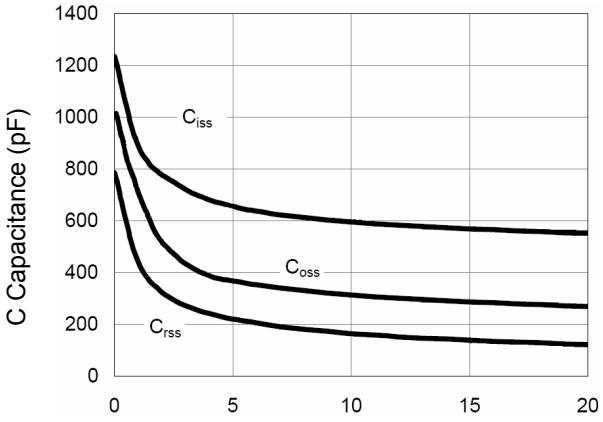


Figure 10 Capacitance vs Vds

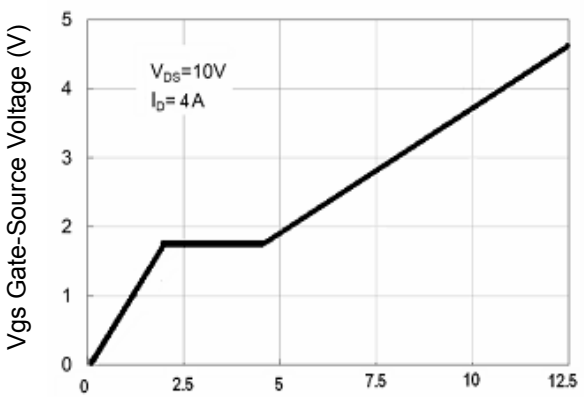


Figure 11 Gate Charge

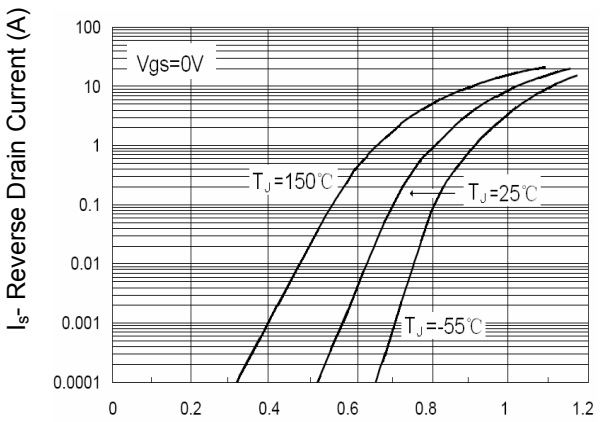


Figure 12 Source- Drain Diode Forward

RATING AND CHARACTERISTIC CURVES

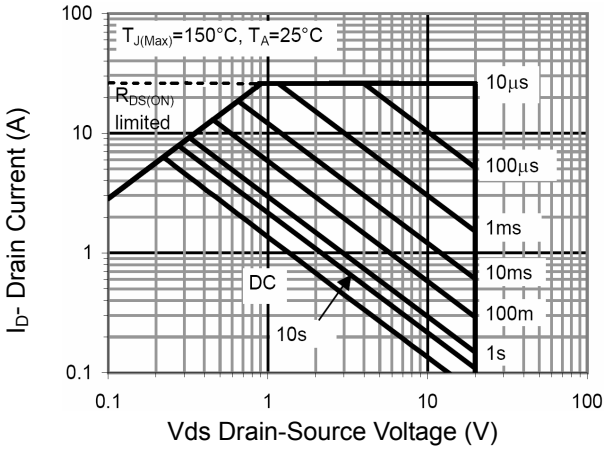


Figure 13 Safe Operation Area

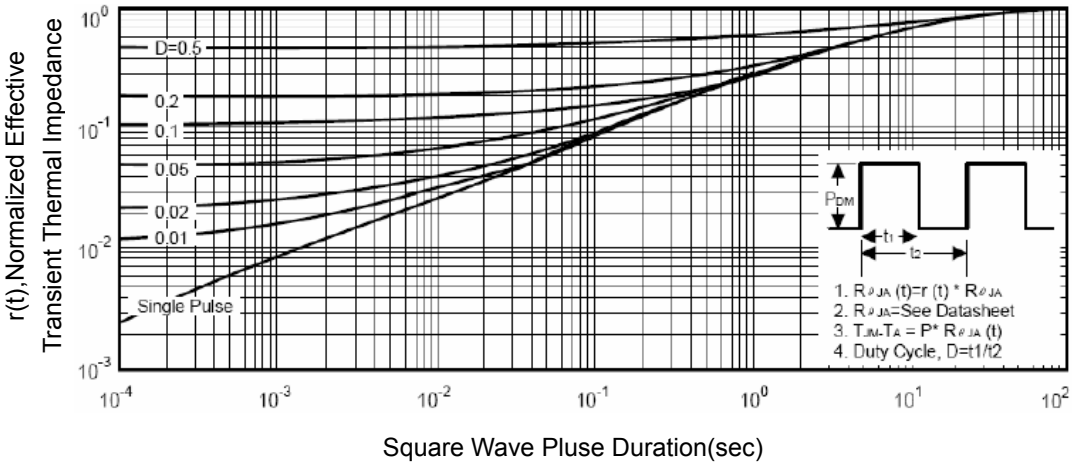
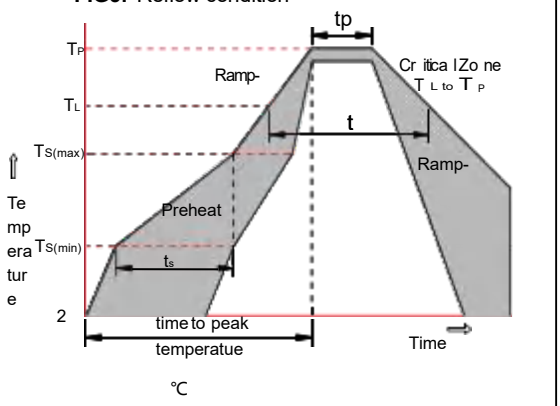


Figure 14 Normalized Maximum Transient Thermal Impedance

Soldering parameters

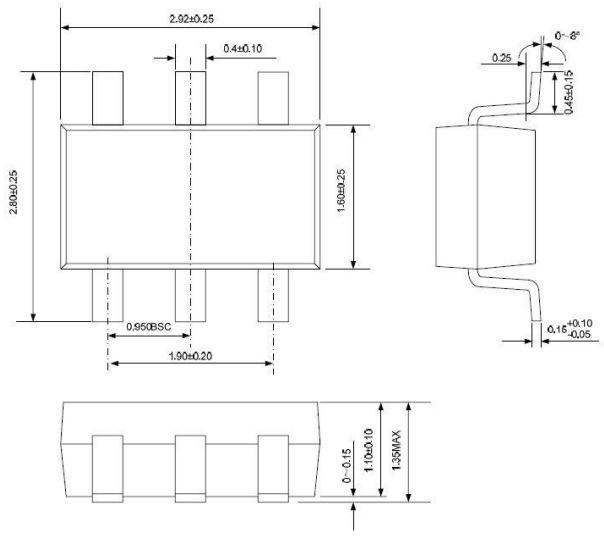
Reflow Condition		Pb-Free assembly (see as bellow)
Pre Heat	-Temperature Min ($T_{s(min)}$)	+150℃
	-Temperature Max($T_{s(max)}$)	+200℃
	-Time (Min to Max) (ts)	60-180 secs.
Average ramp up rate (Liquid us Temp (T_L) to peak)		3℃/sec. Max
$T_{s(max)}$ to T_L - Ramp-up Rate		3℃/sec. Max
Reflow	-Temperature(T_L)(Liquid us)	+217℃
	-Temperature(t_L)	60-150 secs.
Peak Temp (T_p)		+260(+0/-5)℃
Time within 5℃ of actual Peak Temp (t_p)		30 secs. Max
Ramp-down Rate		6℃/sec. Max
Time 25℃ to Peak Temp (T_P)		8 min. Max
Do not exceed		+260℃

FIG5: Reflow condition



Package Dimensions & Suggested Pad Layout

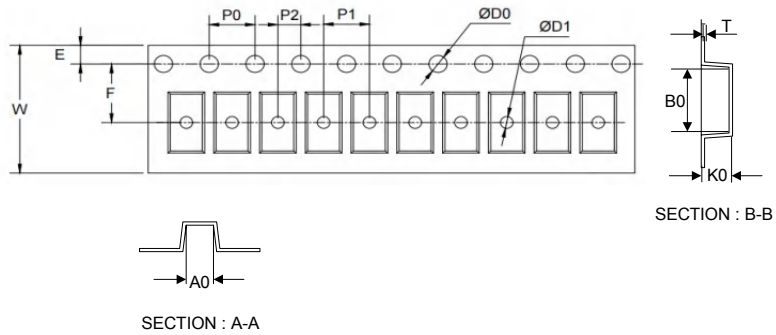
SOT23-6L



Dimensions in millimeters

Tape & rell specification

Tape

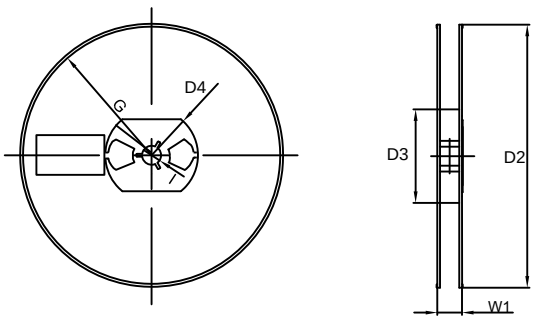


SECTION : A-A

SECTION : B-B

Symbol	Dimension (mm)
P0	4.00±0.20
P1	4.00±0.20
P2	2.00±0.20
D0	1.55±0.20
D1	1.20±0.20
E	1.55±0.20
F	3.60±0.20
W	8.00±0.20
A0	3.80±0.20
B0	3.50±0.20
K0	1.55±0.20
T	0.25±0.15

7” Reel



D2	178.0±5.0
D3	55Min.
D4	R24.0±3.0
G	R82.0±3.0
I	13.0±2.0
W1	11.0±3.0

Quantity: 3000PCS

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