2SK1835

Silicon N Channel MOS FET

REJ03G0978-0400
Rev.4.00
Jun 04, 2008

Application

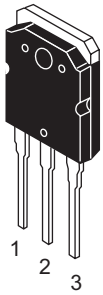
High speed power switching

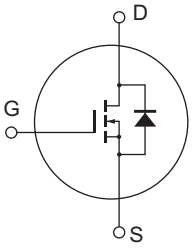
Features

- High breakdown voltage (V_{DSS} = 1500 V)
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator

Outline

RENESAS Package code: PRSS0004ZE-A
(Package name: TO-3P)





1. Gate
2. Drain
   (Flange)
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	1500	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I_D	4	A
Drain peak current	$I_{D(pulse)}$ Note1	10	A
Body to drain diode reverse drain current	I_{DR}	4	A
Channel dissipation	Pch Note2	125	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes: 1. PW ≤ 10 µs, duty cycle ≤ 1 %
2. Value at Tc = 25°C

Electrical Characteristics

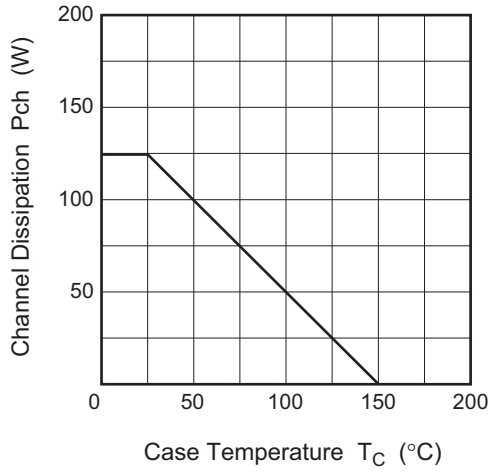
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	1500	—	—	V	I_D = 10 mA, V_{GS} = 0
Gate to source leak current	I_{GSS}	—	—	±1	µA	V_{GS} = ±20 V, V_{DS} = 0
Zero gate voltage drain current	I_{DSS}	—	—	500	µA	V_{DS} = 1200 V, V_{GS} = 0
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	—	4.0	V	I_D = 1 mA, V_{DS} = 10 V
Static drain to source on state resistance	$R_{DS(on)}$	—	4.6	7.0	Ω	I_D = 2 A, V_{GS} = 15 V Note 3
Forward transfer admittance	$\|y_{fs}\|$	0.9	1.4	—	S	I_D = 2 A, V_{DS} = 20 V Note 3
Input capacitance	Ciss	—	1700	—	pF	V_{DS} = 10 V, V_{GS} = 0, f = 1 MHz
Output capacitance	Coss	—	230	—	pF	
Reverse transfer capacitance	Crss	—	100	—	pF	
Turn-on delay time	$t_{d(on)}$	—	25	—	ns	I_D = 2A, V_{GS} = 10 V, R_L = 15 Ω
Rise time	t_r	—	80	—	ns	
Turn-off delay time	$t_{d(off)}$	—	230	—	ns	
Fall time	t_f	—	80	—	ns	
Body to drain diode forward voltage	V_{DF}	—	0.85	—	V	I_F = 4 A, V_{GS} = 0
Body to drain diode reverse recovery time	t_{rr}	—	2500	—	ns	I_F = 4 A, V_{GS} = 0, di_F/dt = 100 A/µs

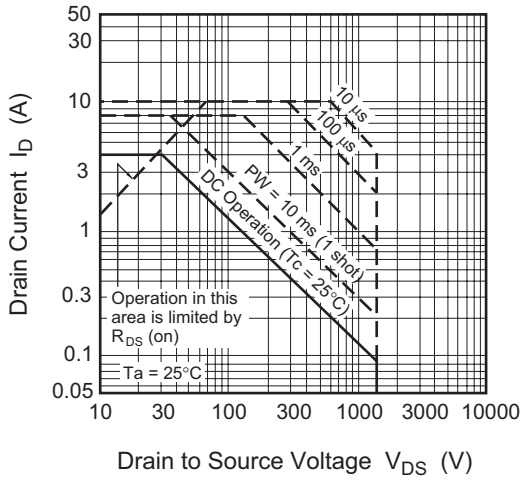
Note: 3. Pulse Test

Main Characteristics

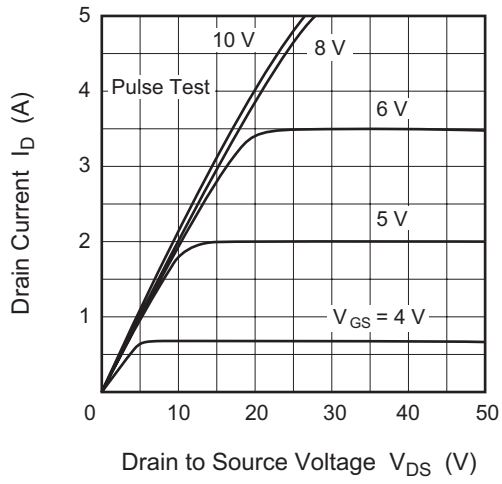
Power vs. Temperature Derating



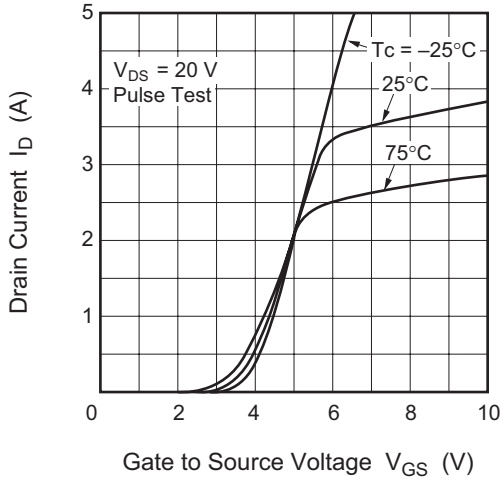
Maximum Safe Operation Area



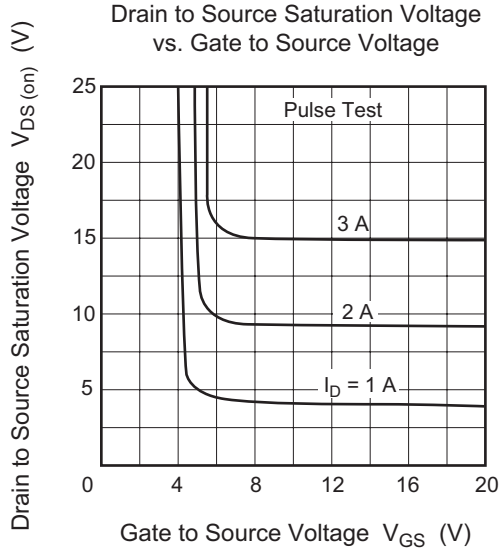
Typical Output Characteristics



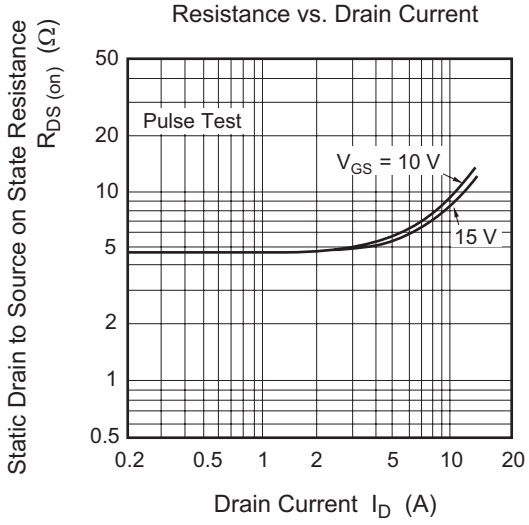
Typical Transfer Characteristics



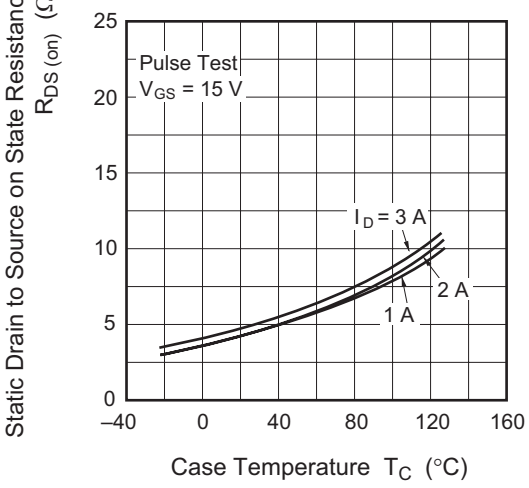
Drain to Source Saturation Voltage vs. Gate to Source Voltage



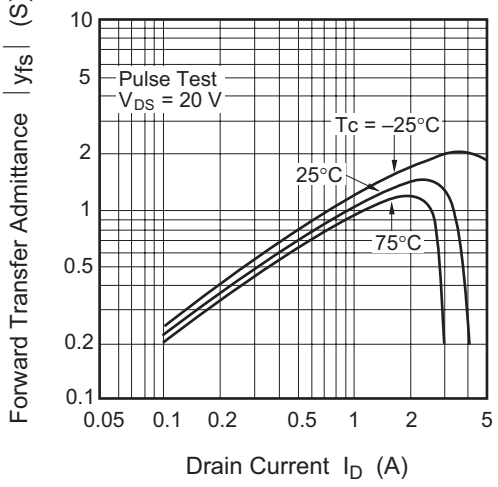
Static Drain to Source on State Resistance vs. Drain Current



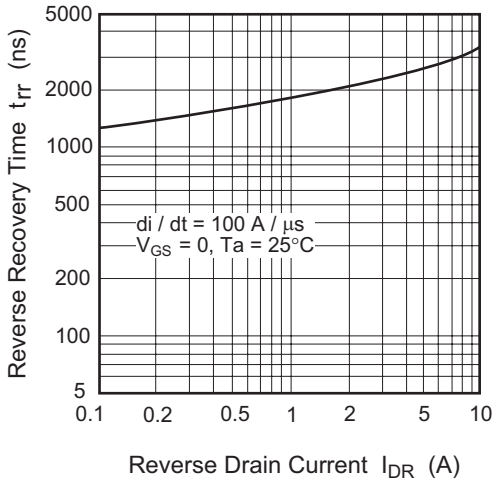
Static Drain to Source on State Resistance vs. Temperature



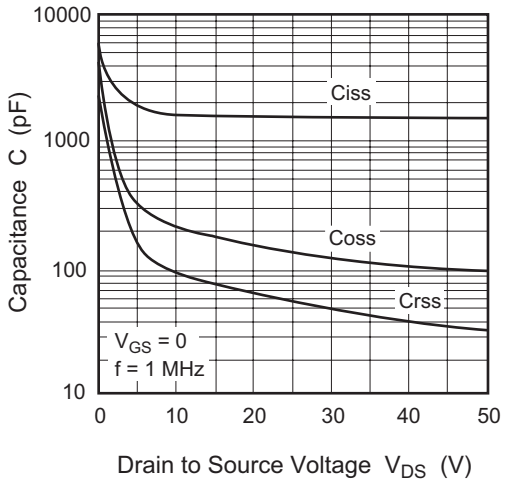
Forward Transfer Admittance vs. Drain Current



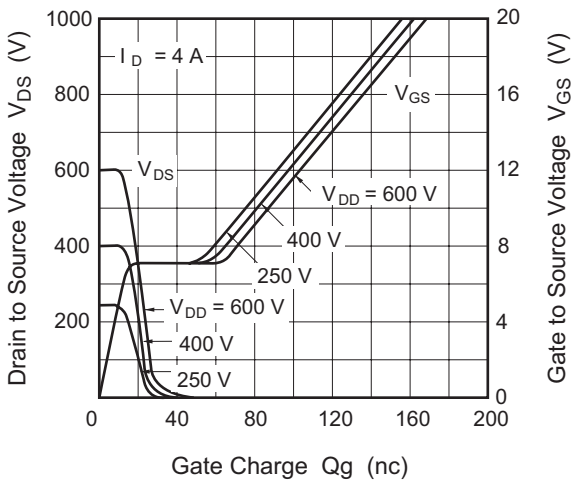
Body to Drain Diode Reverse Recovery Time



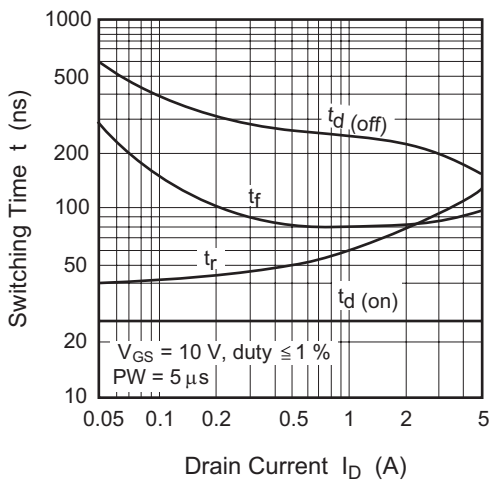
Typical Capacitance vs. Drain to Source Voltage



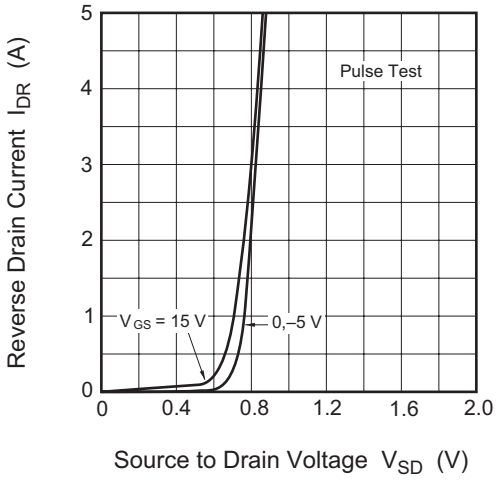
Dynamic Input Characteristics



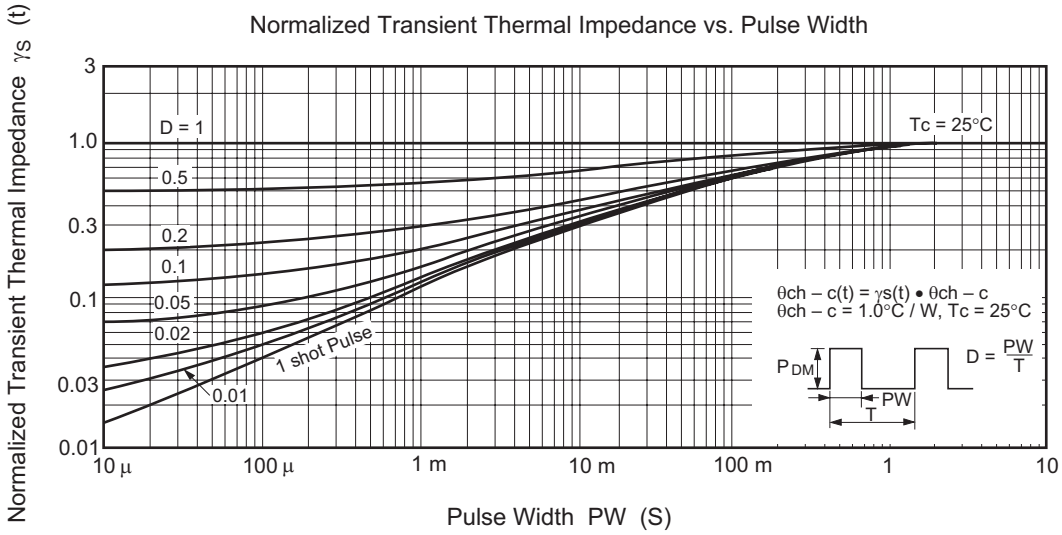
Switching Characteristics



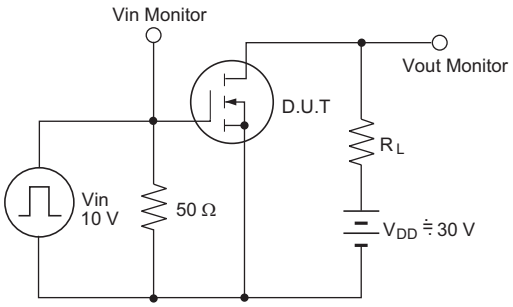
Reverse Drain Current vs.
Source to Drain Voltage



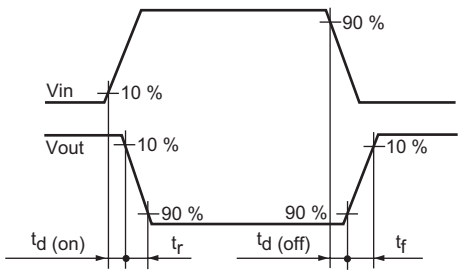
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



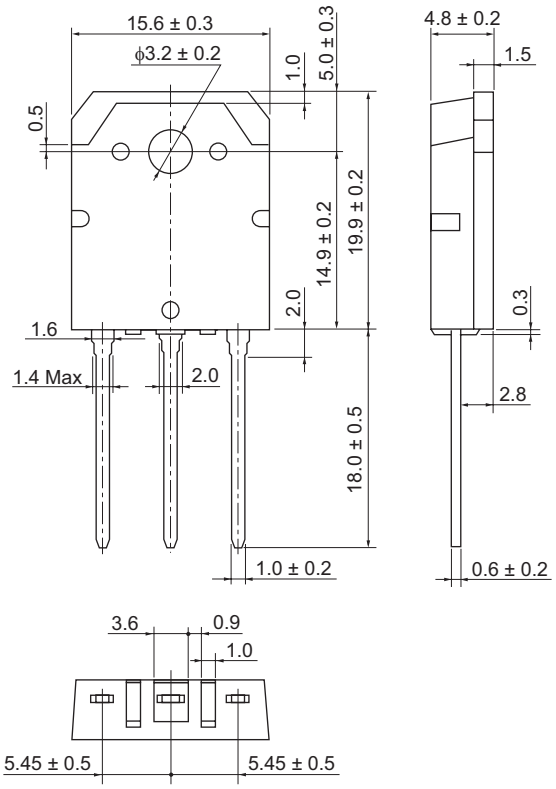
Waveforms



Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
TO-3P	SC-65	PRSS0004ZE-A	TO-3P / TO-3PV	5.0g

Unit: mm



Ordering Information

Part Name	Quantity	Shipping Container
2SK1835-E	360 pcs	Box (Tube)

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April 1st, 2010
Renesas Electronics Corporation

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