300mA HIGH PSRR LOW-NOISE LDO WITH ENABLE

Description

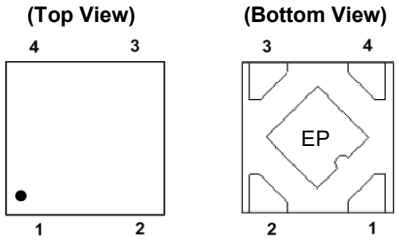
The AP7343 is a low dropout regulator with high output voltage accuracy, low $R_{DS(ON)}$, high PSRR, low output noise, and low quiescent current. This regulator is based on a CMOS process.

The AP7343 includes a voltage reference, error amplifier, current limit circuit, and an enable input to turn it on and off. With the integrated resistor network fixed output voltage versions can be delivered.

With its low power consumption and line and load transient response, the AP7343 is well suited for low-power handheld communication equipment.

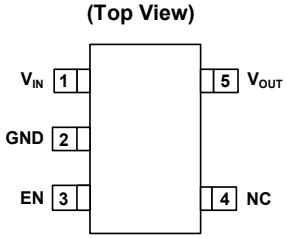
The AP7343 is packaged in the X2-DFN1010-4 (Type B) and SOT25 packages, allowing for the smallest footprint and a dense PCB layout.

Pin Assignments



X2-DFN1010-4 (Type B)

PIN1 – V_{OUT}, PIN2 – GND, PIN3 – EN, PIN4 – V_{IN}



SOT25

Features

- Low V_{IN} and Wide V_{IN} Range: 1.7V to 5.25V
- Guarantee Output Current: 300mA
- V_{OUT} Accuracy ±1%
- Ripple Rejection 75dB at 1kHz
- Low Output Noise, 60μVrms from 10Hz to 100kHz
- Quiescent Current as Low as 35μA
- V_{OUT} Fixed 0.9V to 3.6V
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **An automotive-compliant part is available under a separate datasheet (AP7343Q).**

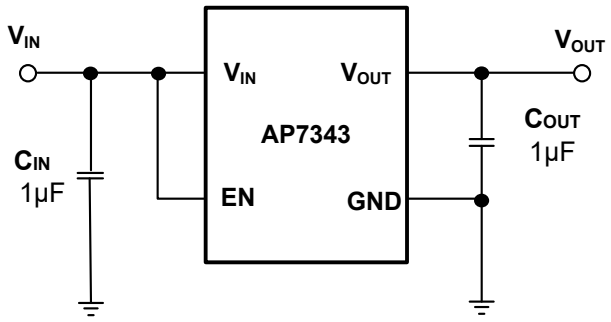
Applications

- Smart phones/tablets
- RF supplies
- Cameras
- Portable videos
- Portable media players
- Wireless adapters
- Wireless communication

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

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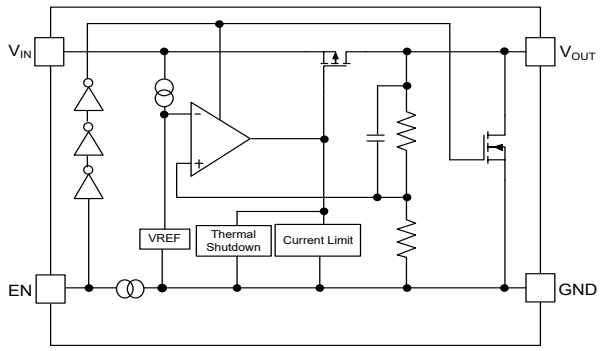
Typical Applications Circuit



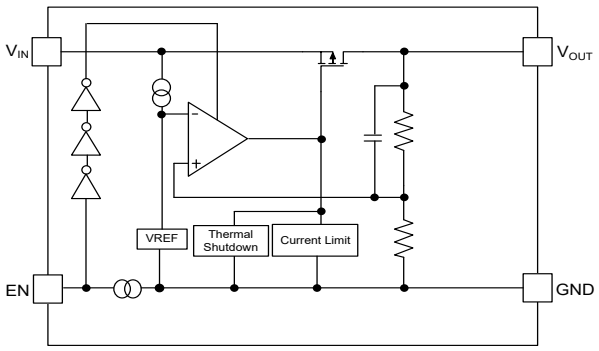
Pin Descriptions

Pin Number		Pin Name	Function
X2-DFN1010-4 (Type B)	SOT25		
1	5	V_{OUT}	Power Output Pin
2	2	GND	Ground
3	3	EN	Enable Pin This pin should be driven either high or low and must not be floating. Driving this pin high enables the regulator, while pulling it low puts the regulator into shutdown mode.
4	1	V_{IN}	Power Input Pin
—	4	NC	No Connection
EP	—	Exposed Pad	In PCB layout, prefer to use large copper area to cover this pad for better thermal dissipation, then connect this area to GND or leave it open. However, do not use it as GND electrode function alone.

Functional Block Diagram



AP7343D (With Discharge)



AP7343 (Without Discharge)

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Absolute Maximum Ratings (Note 4) (@ T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit
ESD HBM	Human Body Mode ESD Protection	>2	kV
ESD MM	Machine Mode ESD Protection	>200	V
V_{IN}	Input Voltage	6.0	V
V_{EN}	Input Voltage for EN Pin	6.0	V
V_{OUT}	Output Voltage	-0.3 to V_{IN} +0.3	V
I_{OUT}	Output Current	400	mA
P_D	Power Dissipation	400	mW
T_A	Operating Ambient Temperature	-40 to 85	°C
T_J	Operating Junction Temperature	125	°C
T_{STG}	Storage Temperature	-55 to 125	°C

Notes: 4. Stresses beyond those listed under *Absolute Maximum Ratings* can cause permanent damage to the device. These are stress ratings only and functional operation of the device at these conditions is not implied. Exposure to absolute-maximum-rated conditions for extended period can affect device reliability. Ratings apply to ambient temperature at +25°C. The JEDEC High-K board design used to derive this data was a 2 inch × 2 inch multilayer board with 1oz. internal power and ground planes and 2oz. copper traces on the top and bottom of the board.

Recommended Operating Conditions (@ T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Min	Max	Unit
V_{IN}	Input Voltage	1.7	5.25	V
I_{OUT}	Output Current	0	300	mA
T_J	Operating Junction Temperature	-40	125	°C
T_A	Operating Ambient Temperature	-40	85	°C

Package Thermal Information

Symbol	Parameter	Value		Unit
		X2-DFN1010-4 (Type B)	SOT25	
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance	237	179	°C/W
$R_{\theta JC(top)}$	Junction-to-Case (Top) Thermal Resistance	96	52	°C/W
$R_{\theta JB}$	Junction-to-Board Thermal Resistance	85.72	79.2	°C/W
ψ_{JT}	Junction-to-Top Characterization Parameter	8.49	24.9	°C/W
ψ_{JB}	Junction-to-Board Characterization Parameter	84.13	79.5	°C/W
$R_{\theta JC(bot)}$	Junction-to-Case (Bottom) Thermal Resistance	16.2	58.4	°C/W

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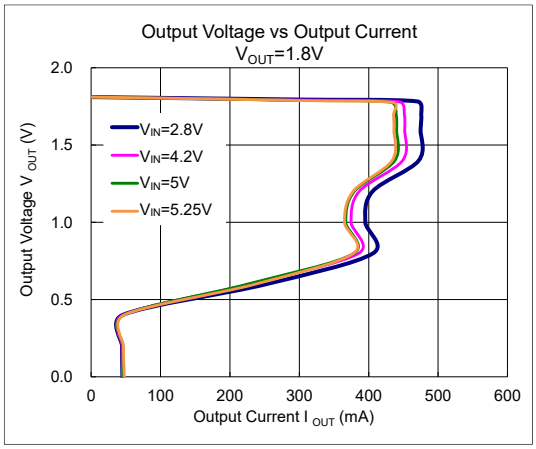
Electrical Characteristics (@ T_A = +25°C, V_{IN} = V_{OUT} +1.0V, C_{IN} = C_{OUT} = 1.0µF, I_{OUT} = 1.0mA, unless otherwise specified.)

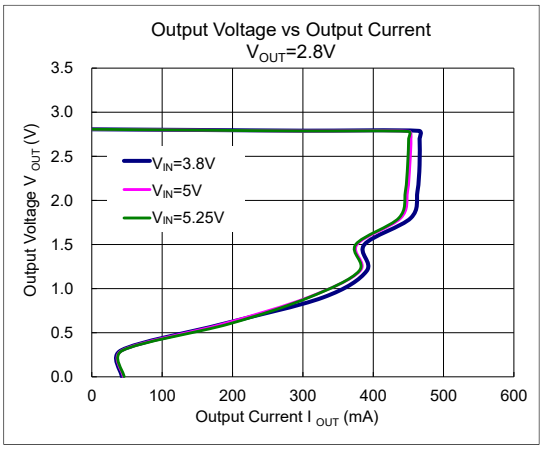
Parameter	Condition		Min	Typ	Max	Unit
Input Voltage	T_A = -40°C to 85°C		1.7	—	5.25	V
Output Voltage Accuracy (Note 11)	$V_{OUT}(T) \geq 2.0V$, $V_{IN} = V_{OUT}(T)+1V$	T_A = 25°C	$V_{OUT}(T)$* 0.99	$V_{OUT}(T)$	$V_{OUT}(T)$* 1.01	V
	$V_{OUT}(T) < 2.0V$, $V_{IN} = V_{OUT}(T)+1V$	T_A = 25°C	$V_{OUT}(T)$- 20mV	$V_{OUT}(T)$	$V_{OUT}(T)$+ 20mV	
Line Regulation ($dV_{OUT}/dV_{IN}/V_{OUT}$)	V_{IN} = ($V_{OUT-Nom}$ +1.0V) to 5.25V, I_{OUT} = 1.0mA		—	0.02	0.1	%/V
Load Regulation	V_{IN} = $V_{OUT-Nom}$ +1.0V, I_{OUT} = 1mA to 300mA		—	15	30	mV
Quiescent Current (Note 6)	I_{OUT} = 0mA		—	35	60	µA
$I_{STANDBY}$	V_{EN} = 0V (Disabled)		—	0.01	1.0	µA
Output Current	—		300	—	—	mA
Fold-back Short Current (Note 7)	V_{OUT} Short to Ground		—	55	—	mA
PSRR (Note 8)	V_{IN} = (V_{OUT}+1V) V_{DC} + 0.2Vp-pAC, $V_{OUT} \geq 1.8V$, I_{OUT} = 30mA	f = 1kHz	—	75	—	dB
Output Noise Voltage (Notes 8 & 9)	BW = 10Hz to 100kHz, I_{OUT} = 30mA		—	60	—	µVrms
Dropout Voltage (Note 5)	I_{OUT} = 300mA (X2-DFN1010-4 (Type B))	V_{OUT} = 0.9V	—	0.51	0.80	V
		V_{OUT} = 0.95V	—	0.49	0.75	
		1.0V < $V_{OUT} \leq$ 1.2V	—	0.46	0.70	
		1.2V < $V_{OUT} \leq$ 1.4V	—	0.39	0.58	
		1.4V < $V_{OUT} \leq$ 1.7V	—	0.35	0.44	
		1.7V < $V_{OUT} \leq$ 2.1V	—	0.30	0.39	
		2.1V < $V_{OUT} \leq$ 2.5V	—	0.26	0.34	
		2.5V < $V_{OUT} \leq$ 3.0V	—	0.25	0.30	
		3.0V < $V_{OUT} \leq$ 3.6V	—	0.22	0.29	
	I_{OUT} = 300mA (SOT25)	V_{OUT} = 0.9V	—	0.51	0.82	V
		V_{OUT} = 0.95V	—	0.49	0.75	
		1.0V < $V_{OUT} \leq$ 1.2V	—	0.46	0.72	
		1.2V < $V_{OUT} \leq$ 1.4V	—	0.39	0.60	
		1.4V < $V_{OUT} \leq$ 1.7V	—	0.35	0.46	
		1.7V < $V_{OUT} \leq$ 2.1V	—	0.30	0.41	
		2.1V < $V_{OUT} \leq$ 2.5V	—	0.26	0.36	
		2.5V < $V_{OUT} \leq$ 3.0V	—	0.25	0.32	
		3.0V < $V_{OUT} \leq$ 3.6V	—	0.22	0.31	
Output Voltage Temperature Coefficient	I_{OUT} = 30mA, T_A = -40°C to 85°C		—	±30	—	ppm/°C
Thermal Shutdown Threshold (TSHDN)	—		—	150	—	°C
Thermal Shutdown Hysteresis (THYS)	—		—	20	—	°C
EN Input Low Voltage	—		0	—	0.5	V
EN Input High Voltage	—		1.3	—	5.25	V
EN Input Leakage	V_{EN} = 0V, V_{IN} = 5.0V or V_{EN} = 5.0V, V_{IN} = 0V		-1.0	—	+1.0	µA
On Resistance of N-Channel for Auto-Discharge (Note 10)	V_{IN} = 4.0V, V_{EN} = 0V (Disabled)		—	30	—	Ω
Thermal Resistance Junction to Ambient (θ_{JA})	X2-DFN1010-4 (Type B)		—	237	—	°C/W
	SOT25		—	179	—	
Thermal Resistance Junction to Case (θ_{JC})	X2-DFN1010-4 (Type B)		—	96	—	
	SOT25		—	52	—	

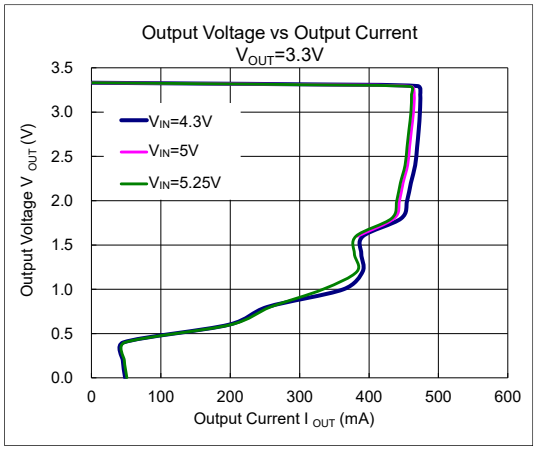
Notes:
5. Dropout voltage is the voltage difference between the input and the output at which the output voltage drops 2% below its nominal value.
6. Quiescent current is defined here as the difference in current between the input and the output.
7. Short circuit current is measured with V_{OUT} pulled to GND.
8. This specification is guaranteed by design.
9. To make sure lowest environment noise minimizes the influence on noise measurement.
10. The AP7343 has 2 options for output, built-in discharge and non-discharge.
11. Potential multiple grades based on following output voltage accuracy.

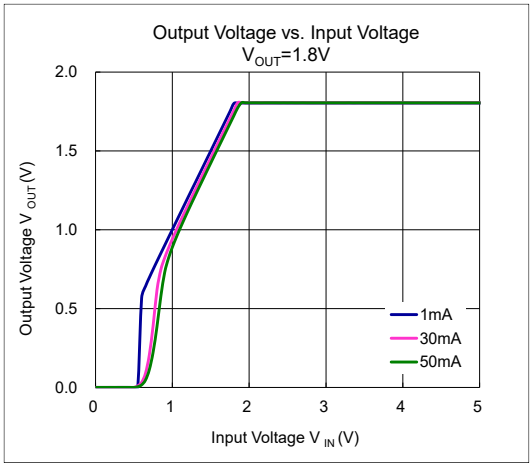
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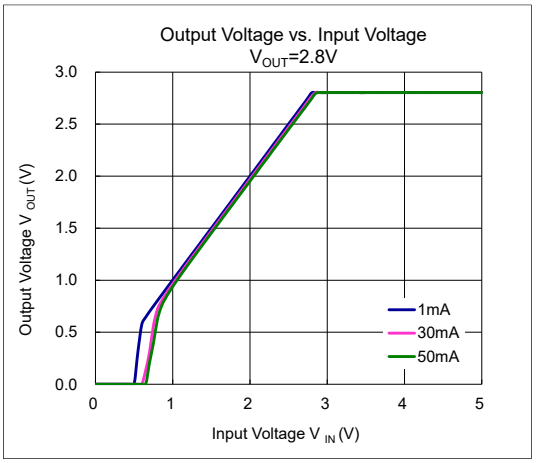
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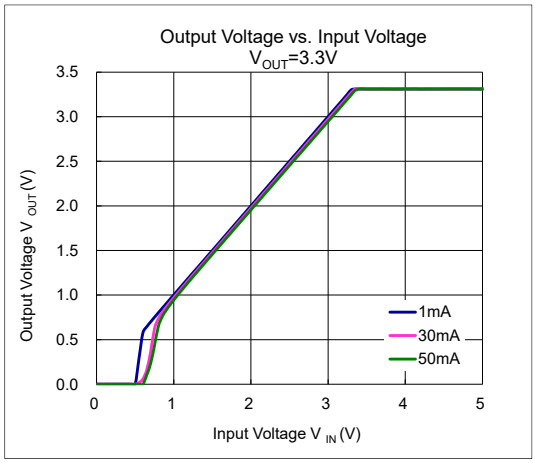






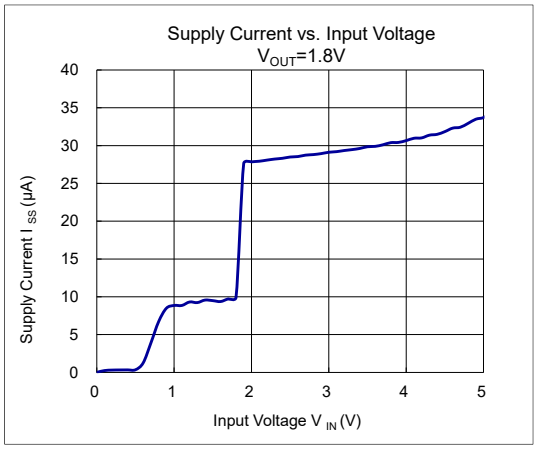


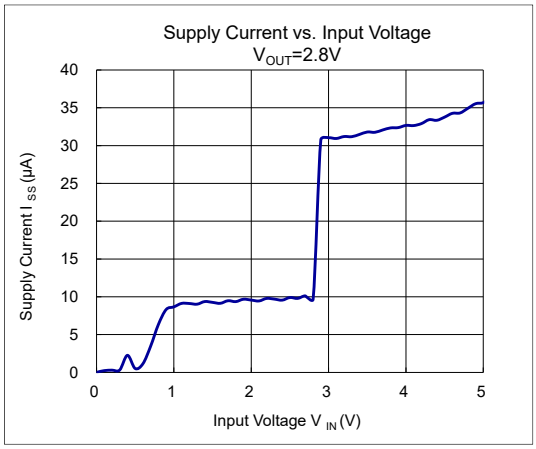


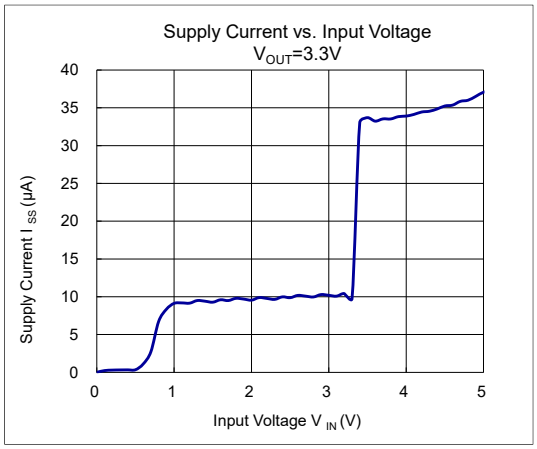


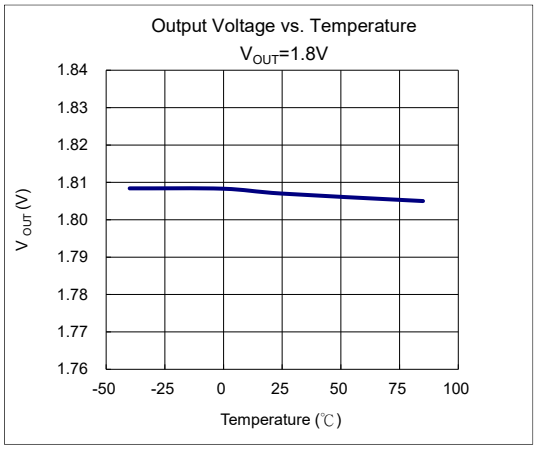
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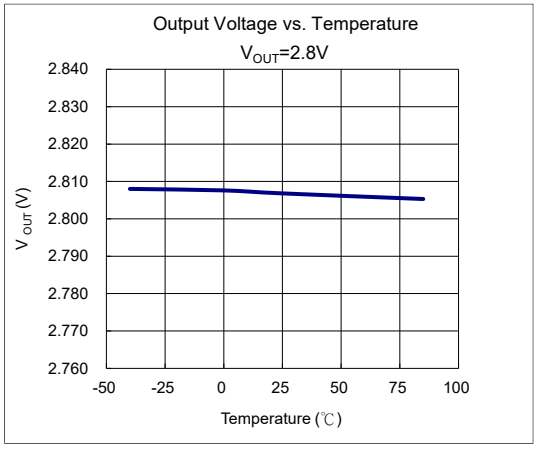
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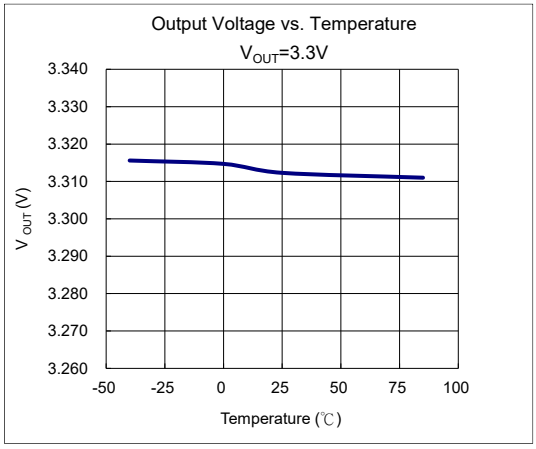






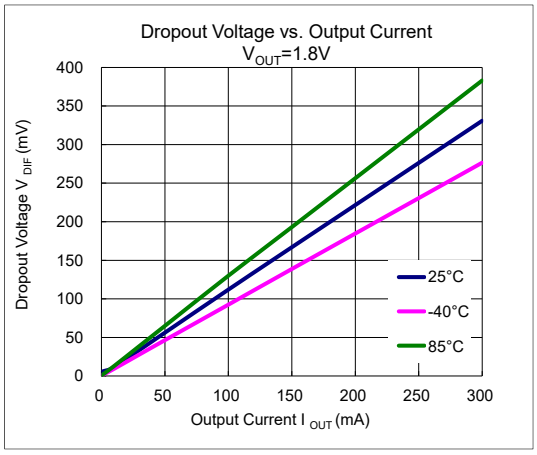


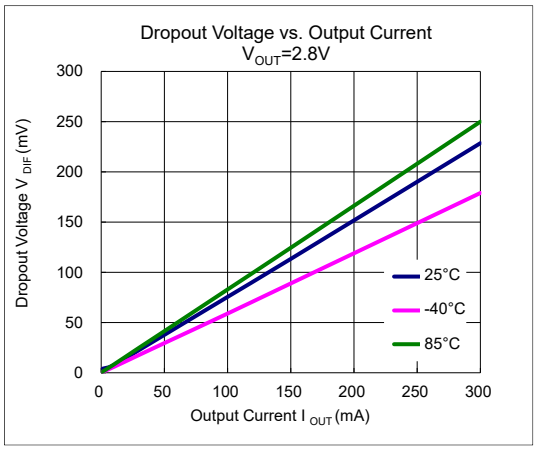


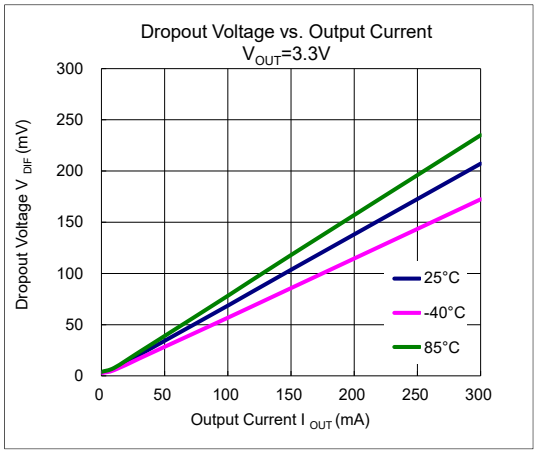


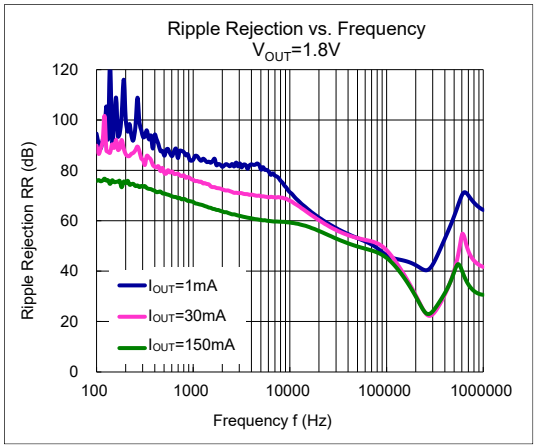
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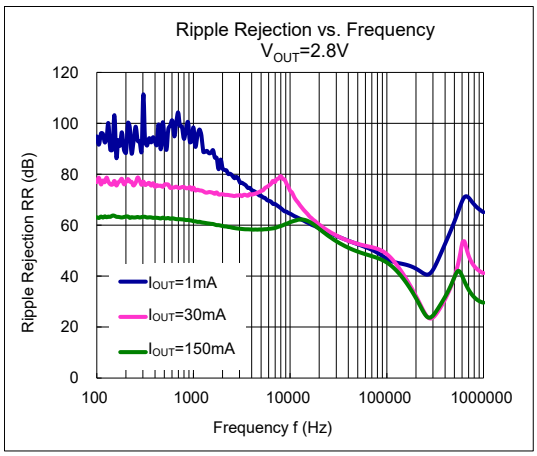
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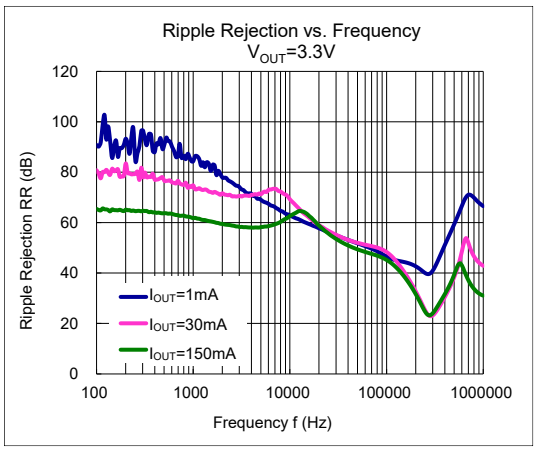






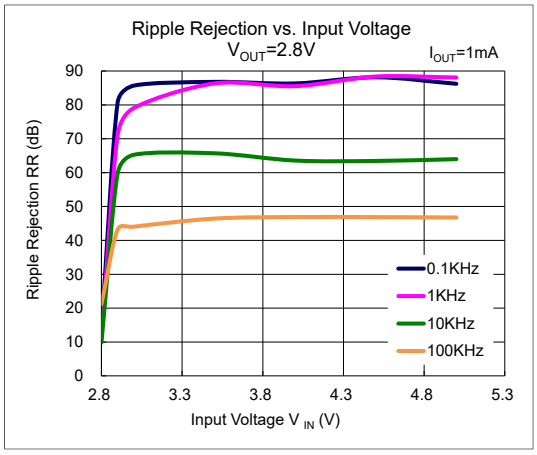


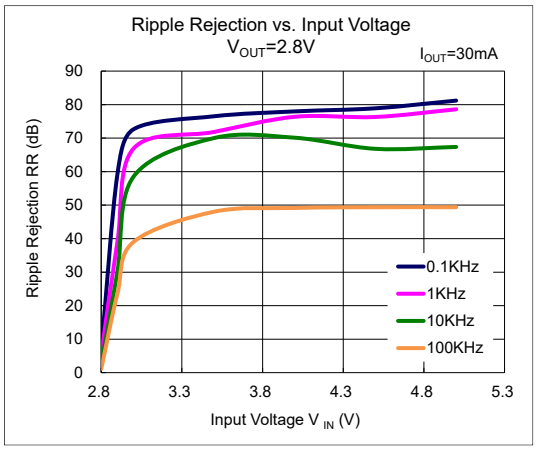




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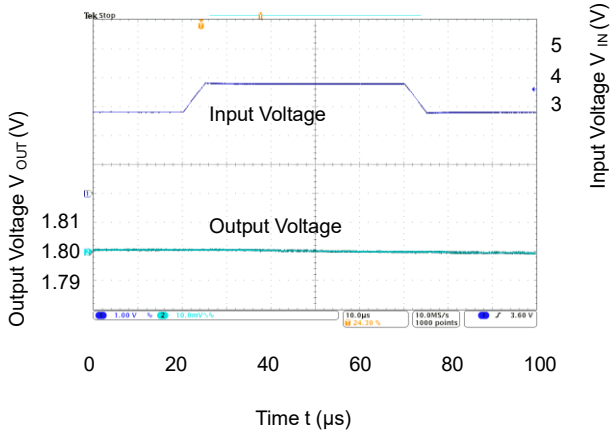
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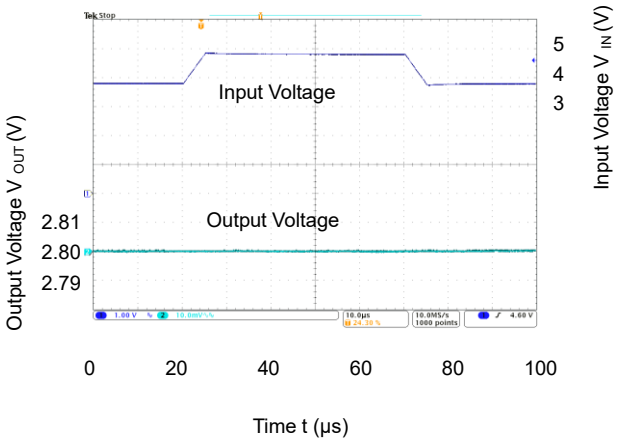


Line Transient Response Waveforms (I_{OUT} = 30mA, t_R = t_F = 5µs, C_{IN} = None, C_{OUT1} = C_{OUT2} = 1µF, T_A = +25°C)

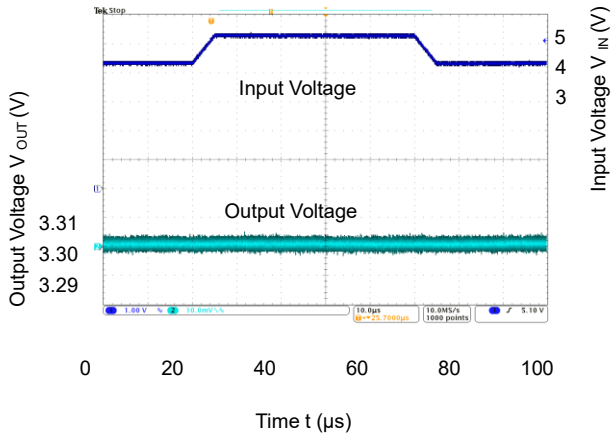
V_{OUT} = 1.8V



V_{OUT} = 2.8V



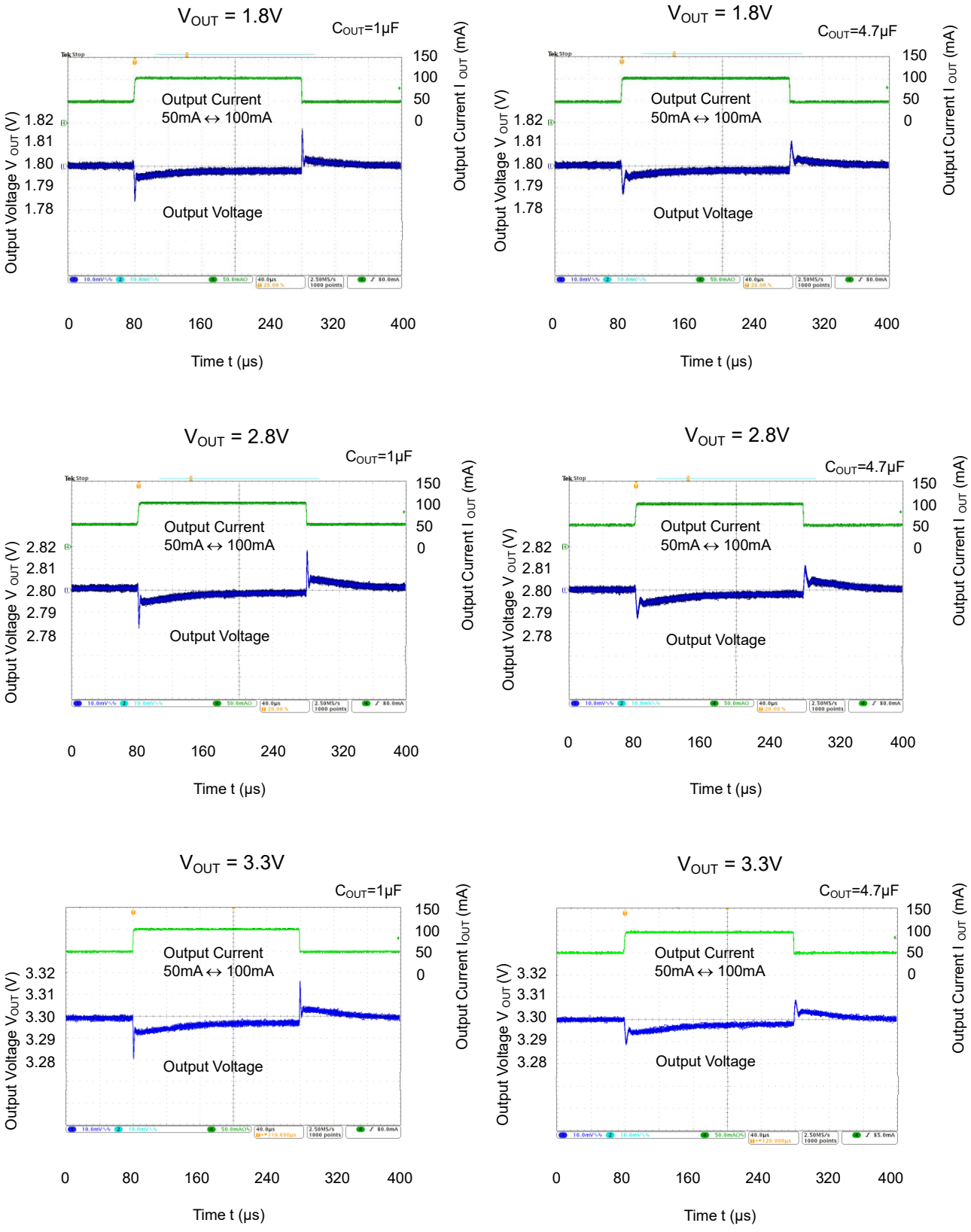
V_{OUT} = 3.3V



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Typical Characteristics (continued)

Load Transient Response Waveforms (V_{IN} = V_{OUT}+1V, C_{IN} = 1μF, T_A = +25°C)

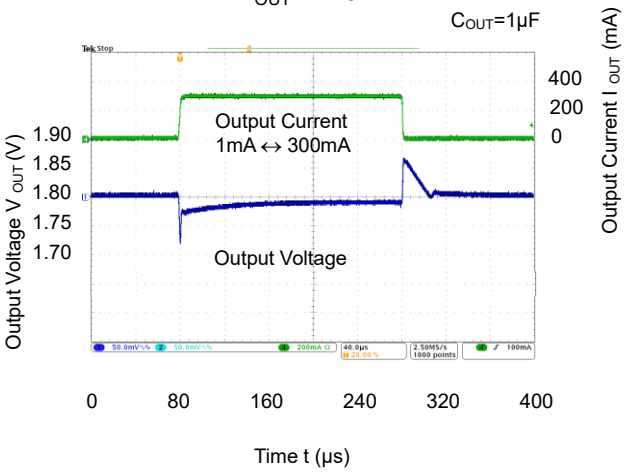


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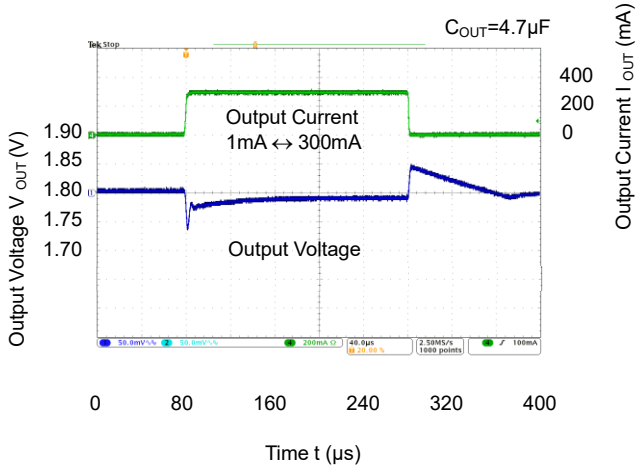
Typical Characteristics (continued)

Load Transient Response Waveforms (V_{IN} = V_{OUT}+1V, C_{IN} = 1μF, T_A = +25°C)

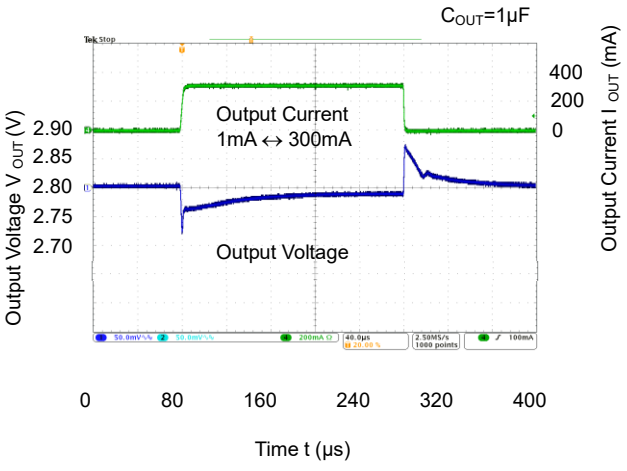
V_{OUT} = 1.8V



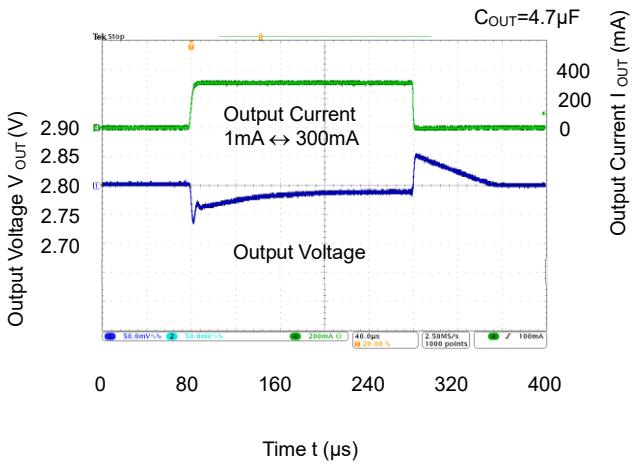
V_{OUT} = 1.8V



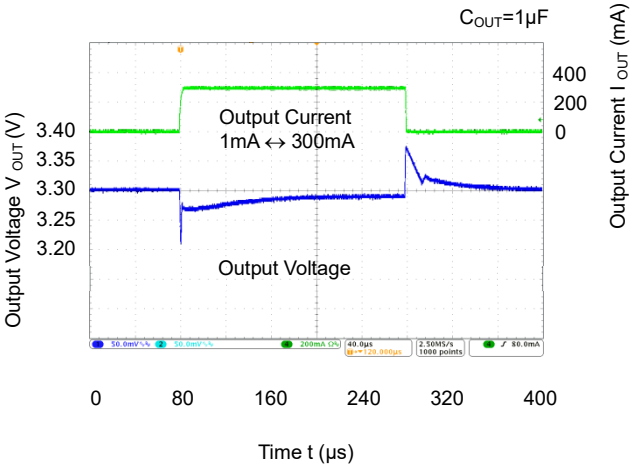
V_{OUT} = 2.8V



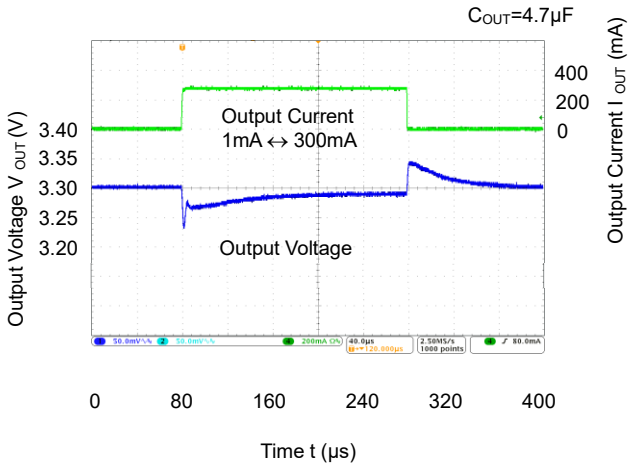
V_{OUT} = 2.8V



V_{OUT} = 3.3V



V_{OUT} = 3.3V

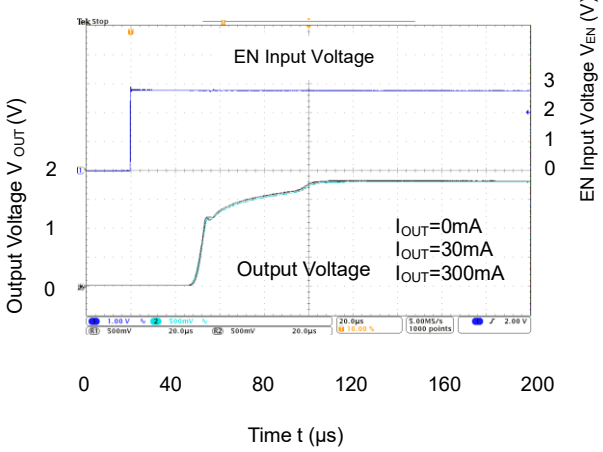


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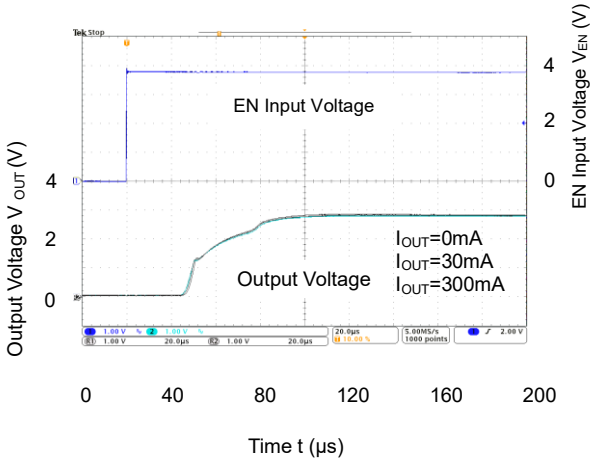
Typical Characteristics (continued)

Turn-On & Turn-Off Waveforms (V_{IN} = V_{OUT}+1V, C_{IN} = 1μF, C_{OUT} = 1μF, T_A = +25°C)

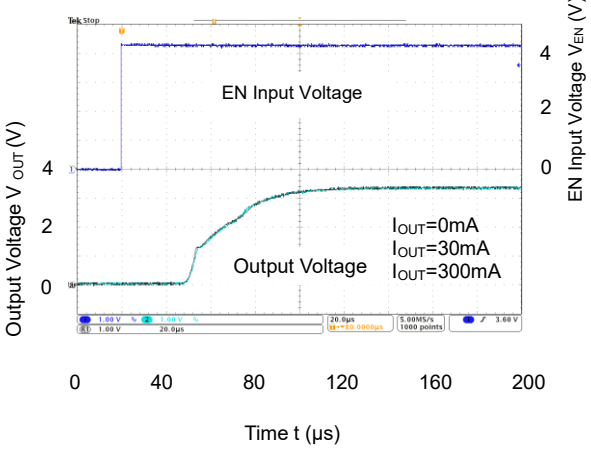
V_{OUT} = 1.8V



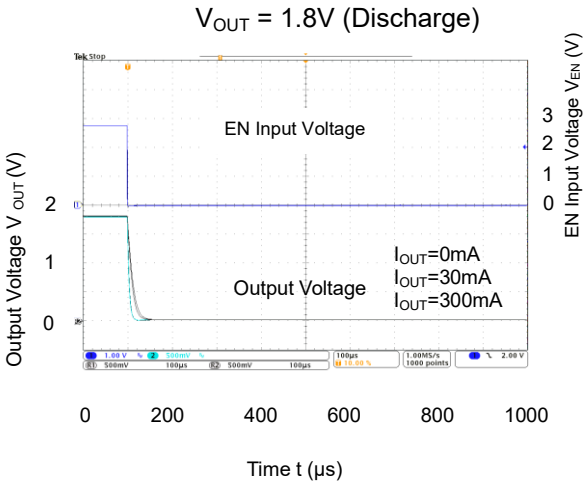
V_{OUT} = 2.8V



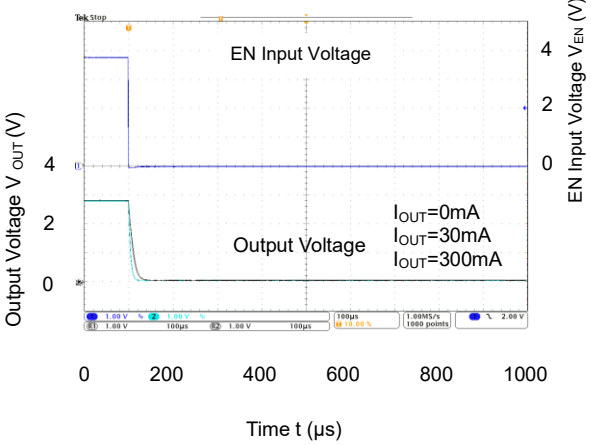
V_{OUT} = 3.3V



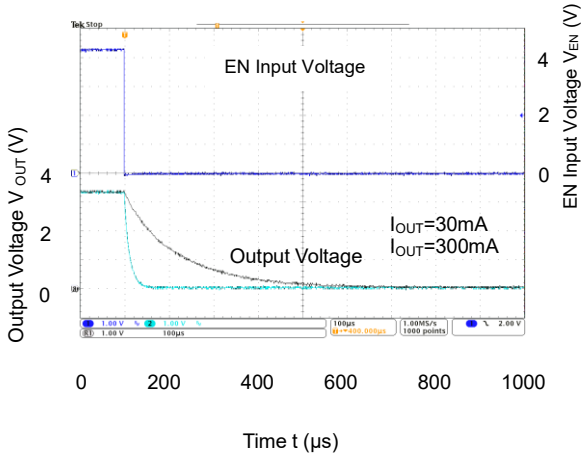
V_{OUT} = 1.8V (Discharge)



V_{OUT} = 2.8V (Discharge)



V_{OUT} = 3.3V (non-Discharge)



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Application Information

Output Capacitor

An output capacitor (C_{OUT}) is needed to improve transient response and maintain stability. The AP7343 is stable with very small ceramic output capacitors. The ESR (equivalent series resistance) and capacitance drive the selection. If the application has large load variations, it is recommended to utilize low-ESR bulk capacitors. It is recommended to place ceramic capacitors as close as possible to the load and the ground pin, and care should be taken to reduce the impedance in the layout.

Input Capacitor

To prevent the input voltage from dropping during load steps, it is recommended to utilize an input capacitor (C_{IN}). A minimum 0.47μF ceramic capacitor is recommended between V_{IN} and GND pins to decouple input power supply glitch. This input capacitor must be located as close as possible to the device to assure input stability and reduce noise. For PCB layout, a wide copper trace is required for both V_{IN} and GND pins.

Enable Control

The AP7343 is turned on by setting the EN pin high, and is turned off by pulling it low. If this feature is not used, the EN pin should be tied to the V_{IN} pin to keep the regulator output on at all times. To ensure proper operation, the signal source used to drive the EN pin must be able to swing above and below the specified turn-on/off voltage thresholds listed in the Electrical Characteristics section.

Short-Circuit Protection

When V_{OUT} pin is short-circuit to GND, short-circuit protection will be triggered and clamp the output current to approximately 60mA. This feature protects the regulator from overcurrent and damage due to overheating.

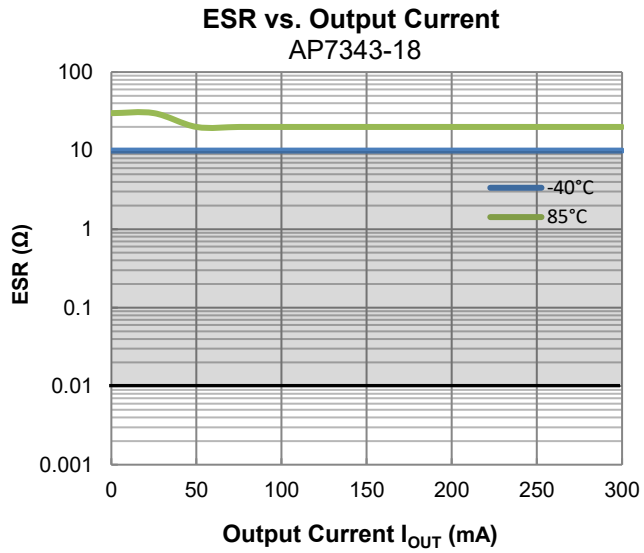
Layout Considerations

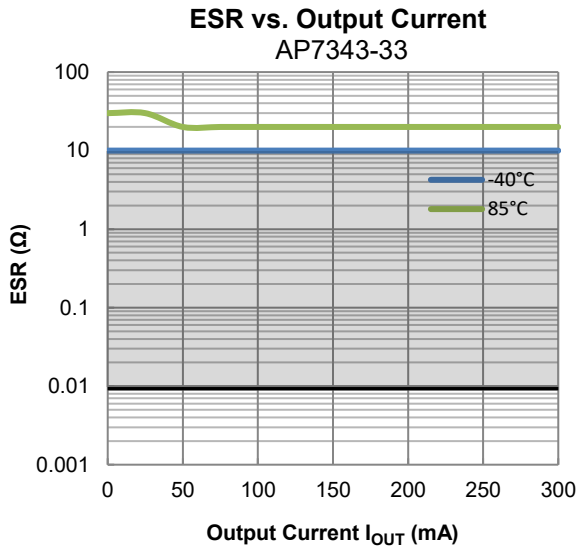
For good ground loop and stability, the input and output capacitors should be located close to the input, output, and ground pins of the device. The regulator ground pin should be connected to the external circuit ground to reduce voltage drop caused by trace impedance. Ground plane is generally used to reduce trace impedance. Wide trace should be used for large current paths from V_{IN} to V_{OUT}, and load circuit.

ESR vs. Output Current

A ceramic-type output capacitor is recommended for this series; however, other output capacitors with low ESR may be used. The relationship between the I_{OUT} (Output Current) and the ESR of an output capacitor are shown below. The stable region is marked as the hatched area in the graph.

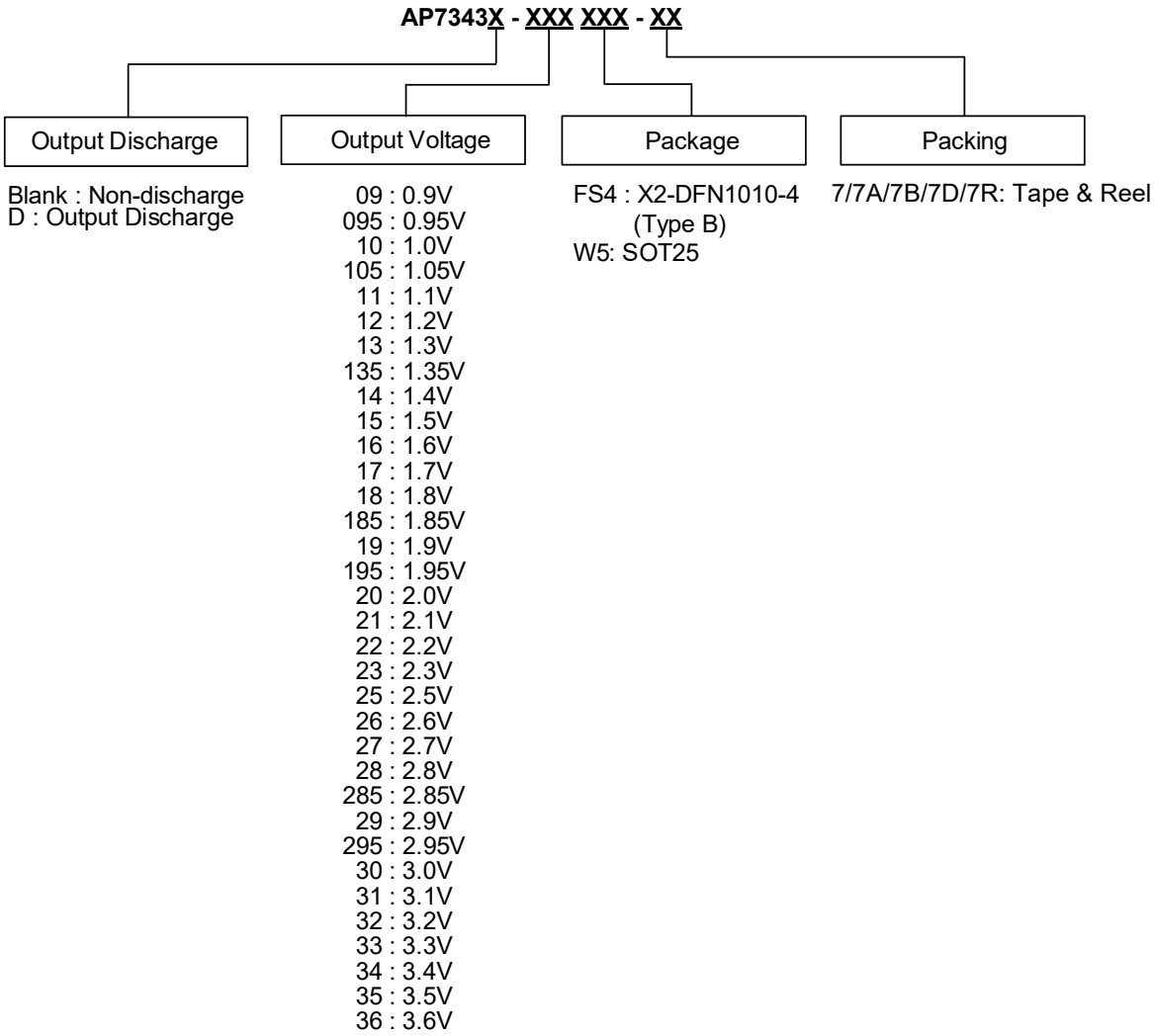
Measurement conditions: Frequency Band: 10Hz to 2MHz, Temperature: -40°C to +85°C.





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Ordering Information (Note 12)



Orderable Part Number	Package Code	Package	Packing	
			Qty.	Carrier
AP7343-XXXFS4-7A	FS4	X2-DFN1010-4 (Type B)	10,000	7” Tape and Reel
AP7343-XXXFS4-7B	FS4	X2-DFN1010-4 (Type B)	10,000	7” Tape and Reel
AP7343D-XXXFS4-7B	FS4	X2-DFN1010-4 (Type B)	10,000	7” Tape and Reel
AP7343D-XXXFS4-7D	FS4	X2-DFN1010-4 (Type B)	5,000	7” Tape and Reel
AP7343D-XXXFS4-7R	FS4	X2-DFN1010-4 (Type B)	5,000	7” Tape and Reel
AP7343D-XXXFS4-7	FS4	X2-DFN1010-4 (Type B)	5,000	7” Tape and Reel
AP7343-XXXW5-7	W5	SOT25	3,000	7” Tape and Reel
AP7343D-XXXW5-7	W5	SOT25	3,000	7” Tape and Reel

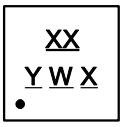
Note: 12. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

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Marking Information

(1) X2-DFN1010-4 (Type B)

(Top View)



XX: Identification Code
Y: Year: 0 to 9
W: Week: A to Z: 1 to 26 Week;
a to z: 27 to 52 Week; z Represents
52 and 53 Week
X: A to Z: Internal Code

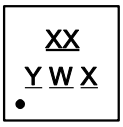
Orderable Part Number	Package	Identification Code
AP7343-09FS4-7B	X2-DFN1010-4 (Type B)	7B
AP7343-10FS4-7B	X2-DFN1010-4 (Type B)	7C
AP7343-105FS4-7B	X2-DFN1010-4 (Type B)	7D
AP7343-11FS4-7B	X2-DFN1010-4 (Type B)	7E
AP7343-12FS4-7B	X2-DFN1010-4 (Type B)	7F
AP7343-135FS4-7B	X2-DFN1010-4 (Type B)	7G
AP7343-15FS4-7B	X2-DFN1010-4 (Type B)	7H
AP7343-18FS4-7B	X2-DFN1010-4 (Type B)	7J
AP7343-185FS4-7B	X2-DFN1010-4 (Type B)	7K
AP7343-22FS4-7B	X2-DFN1010-4 (Type B)	7M
AP7343-25FS4-7B	X2-DFN1010-4 (Type B)	7N
AP7343-28FS4-7B	X2-DFN1010-4 (Type B)	7P
AP7343-285FS4-7B	X2-DFN1010-4 (Type B)	7R
AP7343-295FS4-7B	X2-DFN1010-4 (Type B)	7S
AP7343-30FS4-7B	X2-DFN1010-4 (Type B)	7T
AP7343-31FS4-7B	X2-DFN1010-4 (Type B)	7U
AP7343-32FS4-7B	X2-DFN1010-4 (Type B)	7V
AP7343-33FS4-7B	X2-DFN1010-4 (Type B)	7W
AP7343D-12FS4-7A	X2-DFN1010-4 (Type B)	8F
AP7343D-15FS4-7A	X2-DFN1010-4 (Type B)	8K
AP7343D-18FS4-7A	X2-DFN1010-4 (Type B)	8P
AP7343D-20FS4-7A	X2-DFN1010-4 (Type B)	8T
AP7343D-25FS4-7A	X2-DFN1010-4 (Type B)	8X
AP7343D-28FS4-7A	X2-DFN1010-4 (Type B)	6J
AP7343D-30FS4-7A	X2-DFN1010-4 (Type B)	6P
AP7343D-33FS4-7A	X2-DFN1010-4 (Type B)	6T
AP7343D-09FS4-7B	X2-DFN1010-4 (Type B)	8B
AP7343D-095FS4-7B	X2-DFN1010-4 (Type B)	6X
AP7343D-10FS4-7B	X2-DFN1010-4 (Type B)	8C
AP7343D-105FS4-7B	X2-DFN1010-4 (Type B)	8D
AP7343D-11FS4-7B	X2-DFN1010-4 (Type B)	8E
AP7343D-12FS4-7B	X2-DFN1010-4 (Type B)	8F
AP7343D-13FS4-7B	X2-DFN1010-4 (Type B)	8G
AP7343D-135FS4-7B	X2-DFN1010-4 (Type B)	8H
AP7343D-14FS4-7B	X2-DFN1010-4 (Type B)	8J
AP7343D-15FS4-7B	X2-DFN1010-4 (Type B)	8K
AP7343D-16FS4-7B	X2-DFN1010-4 (Type B)	8M
AP7343D-17FS4-7B	X2-DFN1010-4 (Type B)	8N
AP7343D-18FS4-7B	X2-DFN1010-4 (Type B)	8P
AP7343D-185FS4-7B	X2-DFN1010-4 (Type B)	8R
AP7343D-19FS4-7B	X2-DFN1010-4 (Type B)	8S
AP7343D-195FS4-7B	X2-DFN1010-4 (Type B)	6Y
AP7343D-20FS4-7B	X2-DFN1010-4 (Type B)	8T
AP7343D-21FS4-7B	X2-DFN1010-4 (Type B)	8U
AP7343D-22FS4-7B	X2-DFN1010-4 (Type B)	8V
AP7343D-23FS4-7B	X2-DFN1010-4 (Type B)	8W
AP7343D-25FS4-7B	X2-DFN1010-4 (Type B)	8X
AP7343D-26FS4-7B	X2-DFN1010-4 (Type B)	8Y
AP7343D-27FS4-7B	X2-DFN1010-4 (Type B)	8Z
AP7343D-28FS4-7B	X2-DFN1010-4 (Type B)	6J
AP7343D-285FS4-7B	X2-DFN1010-4 (Type B)	6K
AP7343D-29FS4-7B	X2-DFN1010-4 (Type B)	6M
AP7343D-295FS4-7B	X2-DFN1010-4 (Type B)	6N
AP7343D-30FS4-7B	X2-DFN1010-4 (Type B)	6P
AP7343D-31FS4-7B	X2-DFN1010-4 (Type B)	6R

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Marking Information (continued)

(1) X2-DFN1010-4 (Type B)

(Top View)



XX: Identification Code
Y: Year: 0 to 9
W: Week: A to Z: 1 to 26 Week;
a to z: 27 to 52 Week; z Represents
52 and 53 Week
X: A to Z: Internal Code

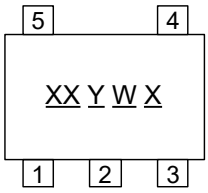
Orderable Part Number	Package	Identification Code
AP7343D-32FS4-7B	X2-DFN1010-4 (Type B)	6S
AP7343D-33FS4-7B	X2-DFN1010-4 (Type B)	6T
AP7343D-34FS4-7B	X2-DFN1010-4 (Type B)	6U
AP7343D-35FS4-7B	X2-DFN1010-4 (Type B)	6V
AP7343D-36FS4-7B	X2-DFN1010-4 (Type B)	6W
AP7343D-12FS4-7D	X2-DFN1010-4 (Type B)	8F
AP7343D-15FS4-7D	X2-DFN1010-4 (Type B)	8K
AP7343D-18FS4-7D	X2-DFN1010-4 (Type B)	8P
AP7343D-25FS4-7D	X2-DFN1010-4 (Type B)	8X
AP7343D-30FS4-7D	X2-DFN1010-4 (Type B)	6P
AP7343D-33FS4-7D	X2-DFN1010-4 (Type B)	6T
AP7343D-12FS4-7R	X2-DFN1010-4 (Type B)	8F
AP7343D-18FS4-7R	X2-DFN1010-4 (Type B)	8P
AP7343D-20FS4-7R	X2-DFN1010-4 (Type B)	8T
AP7343D-25FS4-7R	X2-DFN1010-4 (Type B)	8X
AP7343D-28FS4-7R	X2-DFN1010-4 (Type B)	6J
AP7343D-30FS4-7R	X2-DFN1010-4 (Type B)	6P
AP7343D-33FS4-7R	X2-DFN1010-4 (Type B)	6T
AP7343D-12FS4-7	X2-DFN1010-4 (Type B)	8F
AP7343D-18FS4-7	X2-DFN1010-4 (Type B)	8P
AP7343D-20FS4-7	X2-DFN1010-4 (Type B)	8T
AP7343D-25FS4-7	X2-DFN1010-4 (Type B)	8X
AP7343D-28FS4-7	X2-DFN1010-4 (Type B)	6J
AP7343D-30FS4-7	X2-DFN1010-4 (Type B)	6P
AP7343D-33FS4-7	X2-DFN1010-4 (Type B)	6T

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Marking Information (continued)

(2) SOT25

(Top View)



XX : Identification Code
Y : Year 0 to 9
W : Week : A to Z : 1 to 26 week;
a to z : 27 to 52 week; z represents
52 and 53 week
X : Internal Code

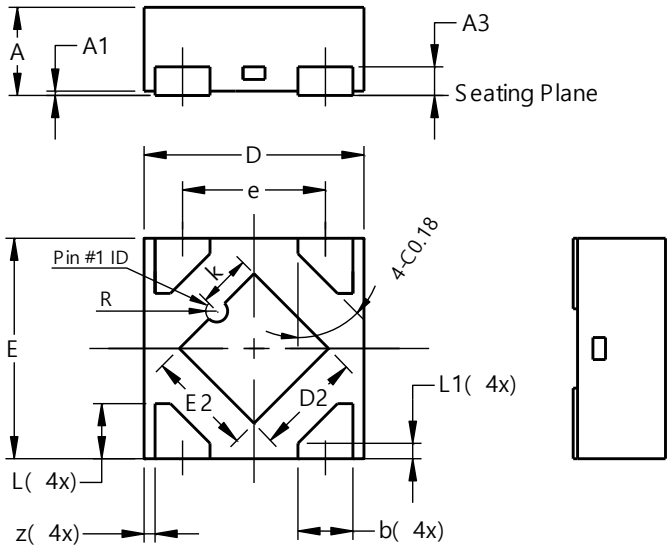
Orderable Part Number	Package	Identification Code
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AP7343-10W5-7	SOT25	7C
AP7343-105W5-7	SOT25	7D
AP7343-11W5-7	SOT25	7E
AP7343-12W5-7	SOT25	7F
AP7343-135W5-7	SOT25	7G
AP7343-15W5-7	SOT25	7H
AP7343-18W5-7	SOT25	7J
AP7343-185W5-7	SOT25	7K
AP7343-22W5-7	SOT25	7M
AP7343-25W5-7	SOT25	7N
AP7343-28W5-7	SOT25	7P
AP7343-285W5-7	SOT25	7R
AP7343-295W5-7	SOT25	7S
AP7343-30W5-7	SOT25	7T
AP7343-31W5-7	SOT25	7U
AP7343-32W5-7	SOT25	7V
AP7343-33W5-7	SOT25	7W
AP7343D-09W5-7	SOT25	8B
AP7343D-10W5-7	SOT25	8C
AP7343D-105W5-7	SOT25	8D
AP7343D-11W5-7	SOT25	8E
AP7343D-12W5-7	SOT25	8F
AP7343D-13W5-7	SOT25	8G
AP7343D-135W5-7	SOT25	8H
AP7343D-14W5-7	SOT25	8J
AP7343D-15W5-7	SOT25	8K
AP7343D-16W5-7	SOT25	8M
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AP7343D-18W5-7	SOT25	8P
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AP7343D-25W5-7	SOT25	8X
AP7343D-26W5-7	SOT25	8Y
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AP7343D-28W5-7	SOT25	6J
AP7343D-285W5-7	SOT25	6K
AP7343D-29W5-7	SOT25	6M
AP7343D-295W5-7	SOT25	6N
AP7343D-30W5-7	SOT25	6P
AP7343D-31W5-7	SOT25	6R
AP7343D-32W5-7	SOT25	6S
AP7343D-33W5-7	SOT25	6T
AP7343D-34W5-7	SOT25	6U
AP7343D-35W5-7	SOT25	6V
AP7343D-36W5-7	SOT25	6W

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Package Outline Dimensions

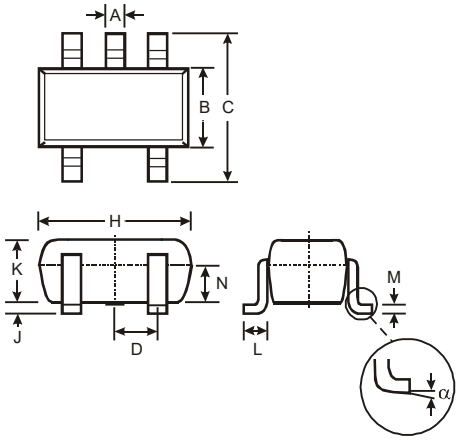
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) X2-DFN1010-4 (Type B)



X2-DFN1010-4 (Type B)			
Dim	**Min**	**Max**	**Typ**
A	-	0.40	0.39
A1	0.00	0.05	0.02
A3	-	-	0.13
b	0.20	0.30	0.25
D	0.95	1.05	1.00
D2	0.43	0.53	0.48
E	0.95	1.05	1.00
E2	0.43	0.53	0.48
e	-	-	0.65
k	0.19	0.29	0.24
L	0.20	0.30	0.25
L1	0.02	0.12	0.07
R	0.02	0.08	0.05
z	-	-	0.050
All Dimensions in mm			

(2) SOT25



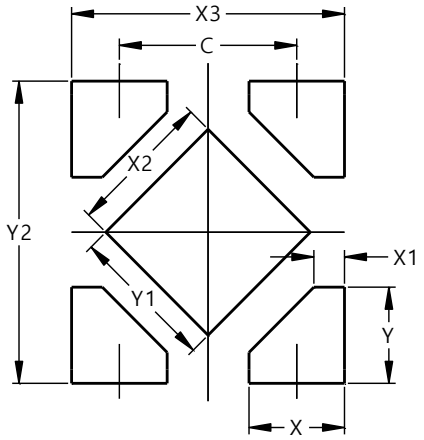
SOT25			
Dim	**Min**	**Max**	**Typ**
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	-	-	0.95
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
N	0.70	0.80	0.75
α	0°	8°	-
All Dimensions in mm			

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Suggested Pad Layout

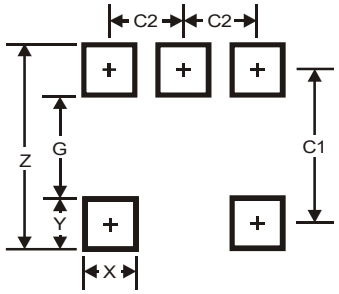
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) X2-DFN1010-4 (Type B)



Dimensions	Value (in mm)
C	0.650
X	0.350
X1	0.112
X2	0.530
X3	1.00
Y	0.350
Y1	0.530
Y2	1.100

(2) SOT25



Dimensions	Value
Z	3.20
G	1.60
X	0.55
Y	0.80
C1	2.40
C2	0.95

Mechanical Data

- Moisture Sensitivity: Level 1 Per J-STD-020
- Terminals:
  - X2-DFN1010-4 (Type B): Finish - NiPdAu over Copper Leads, Solderable per MIL-STD-202, Method 208 e4
  - SOT25: Finish - Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 e3
- Weight:
  - X2-DFN1010-4 (Type B): 0.001 grams (Approximate)
  - SOT25: 0.016 grams (Approximate)

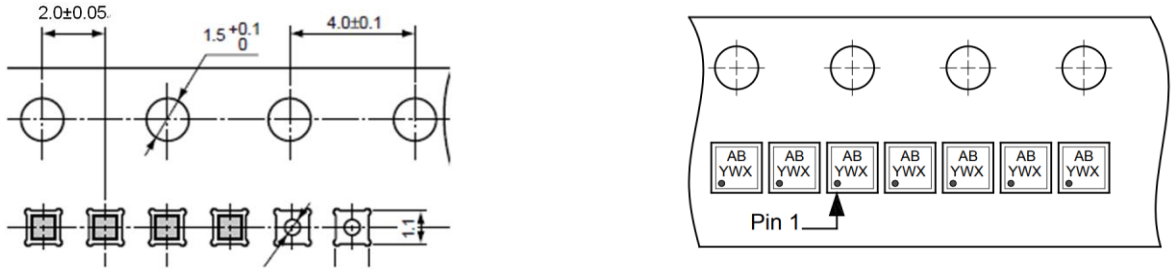
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Tape Orientation (Note 13)

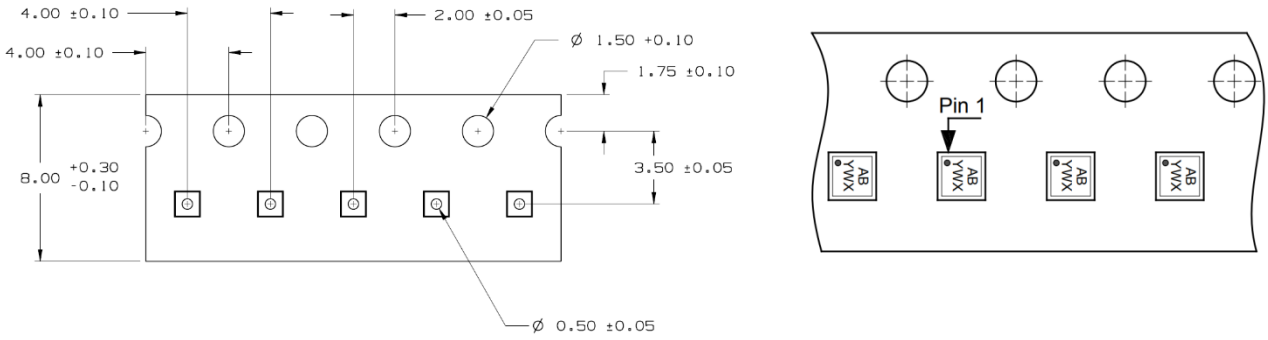
For AP7343D-XXXFS4-7A



For AP7343-XXXFS4-7B & AP7343D-XXXFS4-7B



For AP7343D-XXXFS4-7

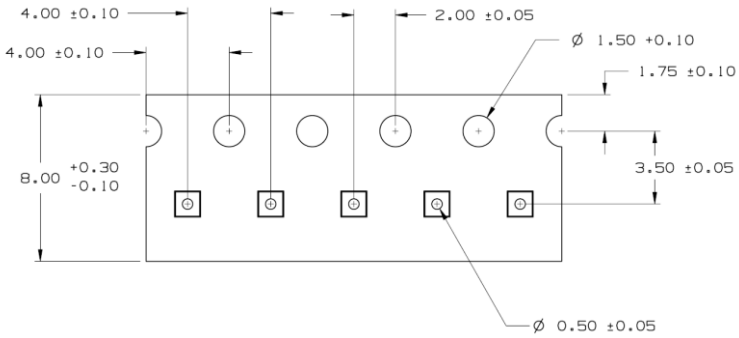


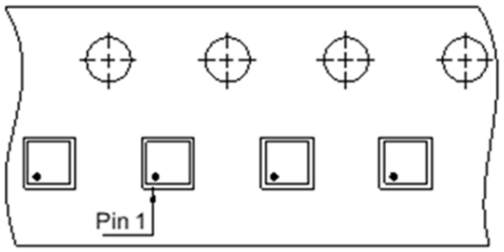
Note: 13. The taping orientation of other package types can be found on our website at http://www.diodes.com/package-outlines.html.

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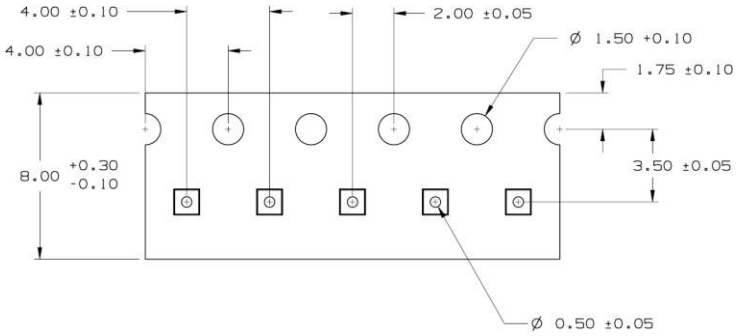
Tape Orientation (Note 13) (continued)

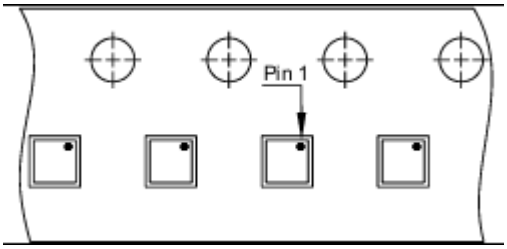
For AP7343D-XXXFS4-7R



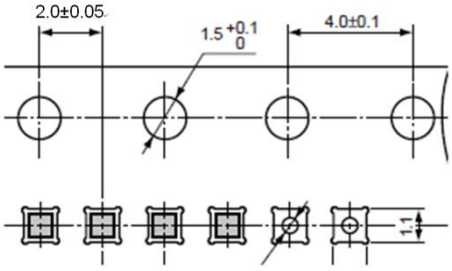


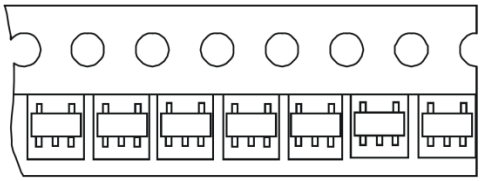
For AP7343D-XXXFS4-7D





For AP7343-XXXW5-7 & AP7343D-XXXW5-7





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