Product Summary

BV_{DSS}	$R_{DS(ON)}$ Max	I_D Max T_A = +25°C
20V	175mΩ @ V_{GS} = 4.5V	1.6A
	240mΩ @ V_{GS} = 2.5V	1.3A
	360mΩ @ V_{GS} = 1.8V	1.1A

Description

This new generation MOSFET has been designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

Applications

- Load switches

Features

- Low Gate Threshold Voltage
- Fast Switching Speed
- ESD Protected Gate
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/**

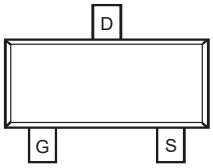
Mechanical Data

- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 e3
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)

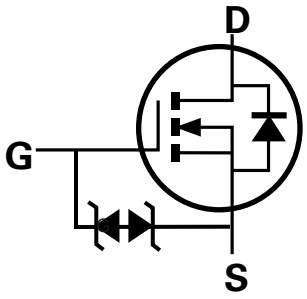

ESD Protected Gate

SOT23


Top View



Top View
Pin-Out



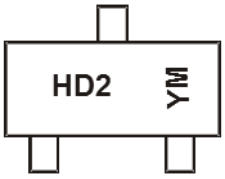
Equivalent Circuit

Ordering Information (Note 4)

Orderable Part Number	Package	Packing	
		Qty.	Carrier
DMN2310U-7	SOT23	3000	Tape & Reel
DMN2310U-13	SOT23	10000	Tape & Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



HD2 = Product Type Marking Code
$\overline{Y}$M or YM = Date Code Marking
$\overline{Y}$ or Y = Year (ex: N = 2026)
M = Month (ex: 9 = September)

Date Code Key

Year	2019	-	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035
Code	G	-	N	P	R	S	T	U	V	W	X	Y

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	20	V
Gate-Source Voltage			V_{GSS}	±8	V
Continuous Drain Current (Note 6) V_{GS} = 4.5V	Steady State	T_A = 25°C T_A = 85°C	I_D	1.6 1.2	A
Maximum Continuous Body Diode Forward Current (Note 6)			I_S	0.82	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I_{DM}	4.8	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_D	0.48	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	260	°C/W
Total Power Dissipation (Note 6)		P_D	0.68	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	184	°C/W
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	20	—	—	V	V_{GS} = 0V, I_D = 250µA
Zero Gate Voltage Drain Current T_J = 25°C	I_{DSS}	—	—	1.0	µA	V_{DS} = 20V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	10	µA	V_{GS} = ±8V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	0.45	—	0.95	V	V_{DS} = V_{GS}, I_D = 250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	140	175	mΩ	V_{GS} = 4.5V, I_D = 300mA
			180	240		V_{GS} = 2.5V, I_D = 250mA
			245	360		V_{GS} = 1.8V, I_D = 100mA
Diode Forward Voltage	V_{SD}	—	0.8	1.2	V	V_{GS} = 0V, Is = 1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	38	—	pF	V_{DS} = 10V, V_{GS} = 0V, f = 1.0MHz
Output Capacitance	C_{oss}	—	10	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	6	—	pF	
Total Gate Charge	Q_g	—	0.7	—	nC	V_{GS} = 4.5V, V_{DS} = 10V, I_D = 6A
Gate-Source Charge	Q_{gs}	—	0.1	—	nC	
Gate-Drain Charge	Q_{gd}	—	0.1	—	nC	
Turn-On Delay Time	$t_{D(ON)}$	—	8	—	ns	V_{DD} = 10V, V_{GS} = 5V, R_L = 1.7Ω, R_G = 6Ω
Turn-On Rise Time	t_R	—	138	—	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	—	154	—	ns	
Turn-Off Fall Time	t_F	—	180	—	ns	

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

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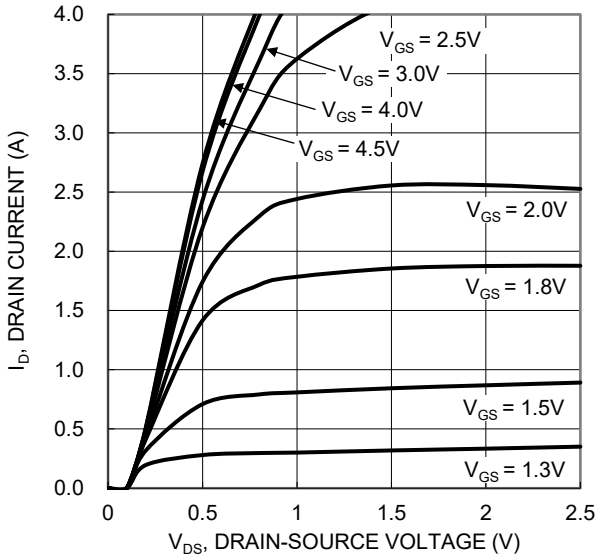


Figure 1. Typical Output Characteristic

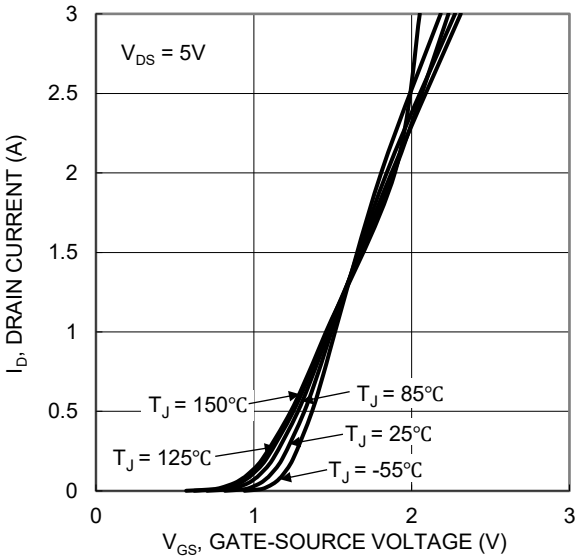


Figure 2. Typical Transfer Characteristic

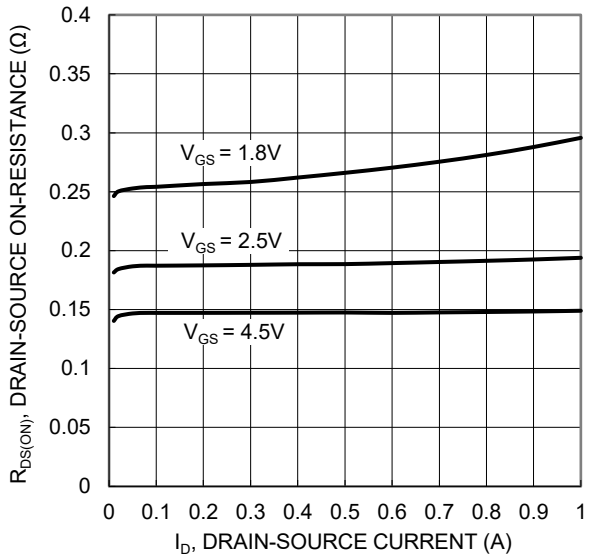


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

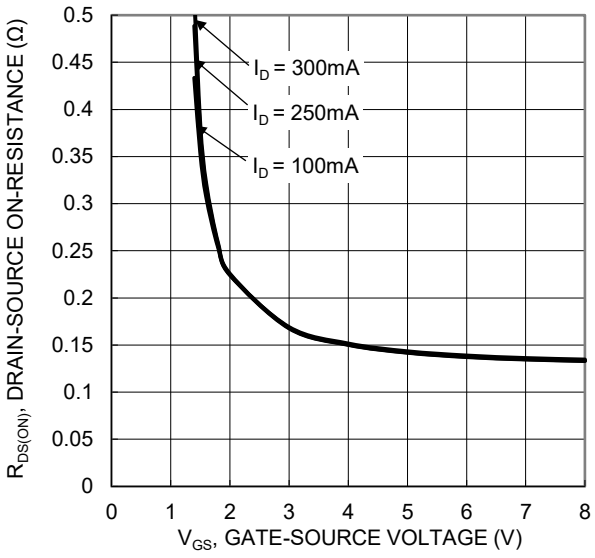


Figure 4. Typical Transfer Characteristic

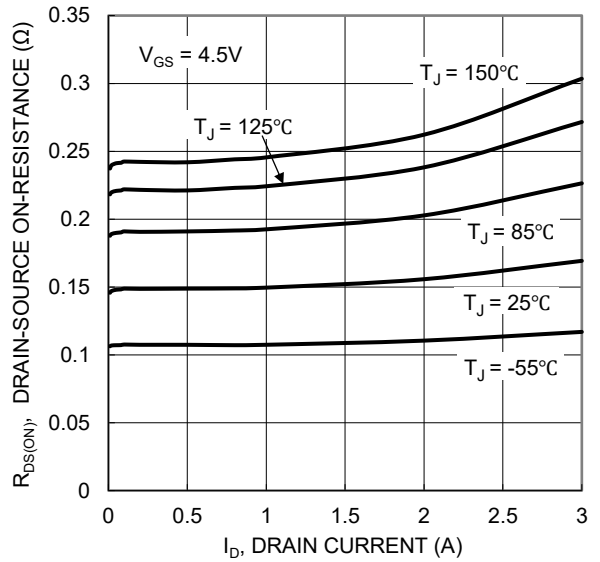


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

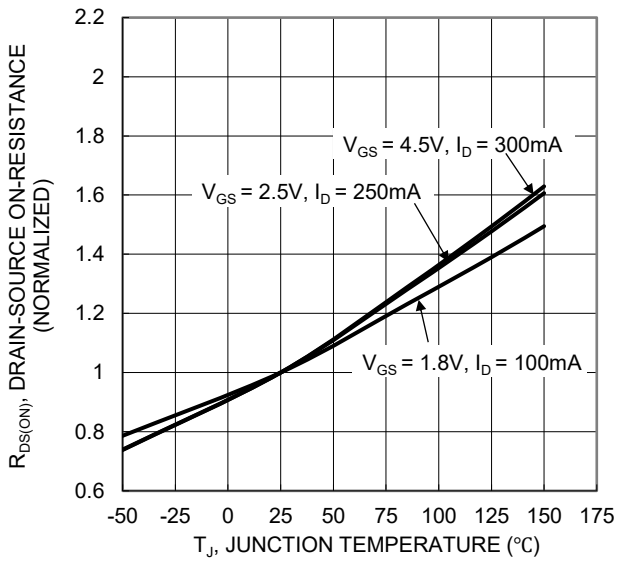


Figure 6. On-Resistance Variation with Junction Temperature

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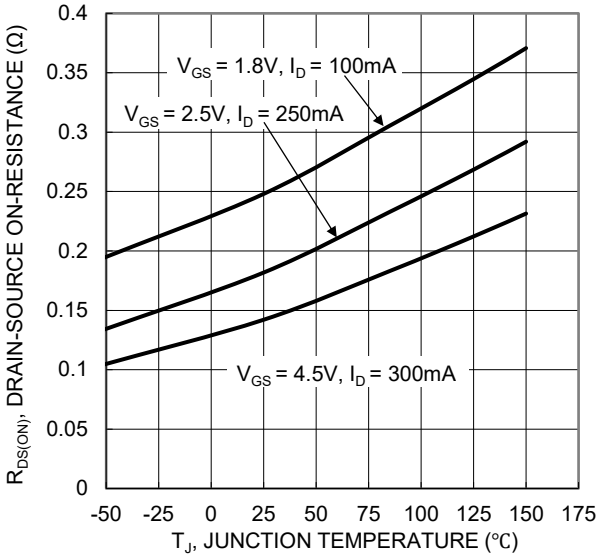


Figure 7. On-Resistance Variation with Junction Temperature

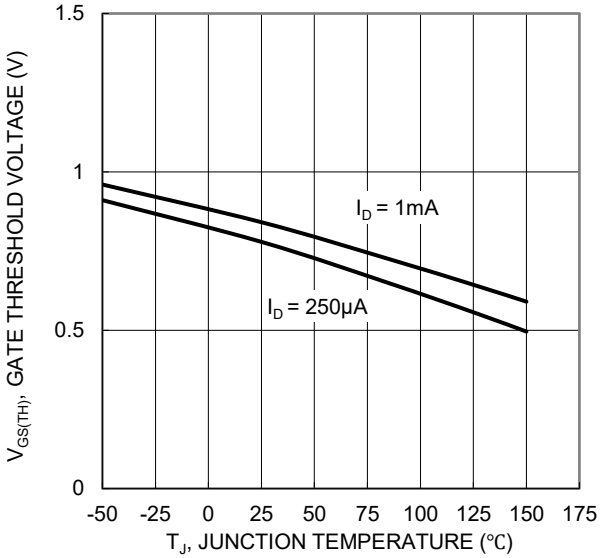


Figure 8. Gate Threshold Variation vs. Junction Temperature

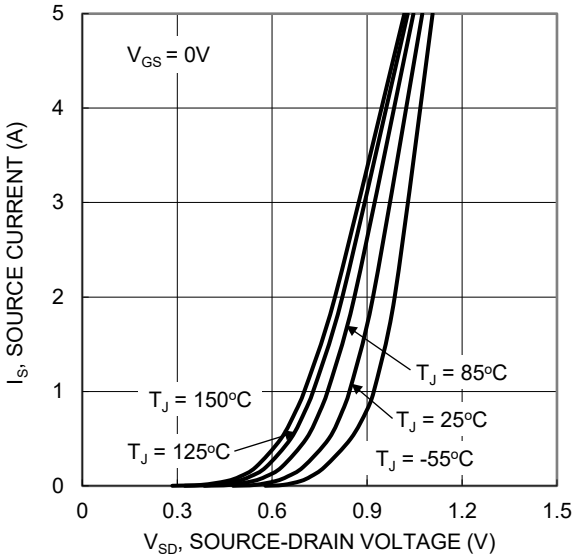


Figure 9. Diode Forward Voltage vs. Current

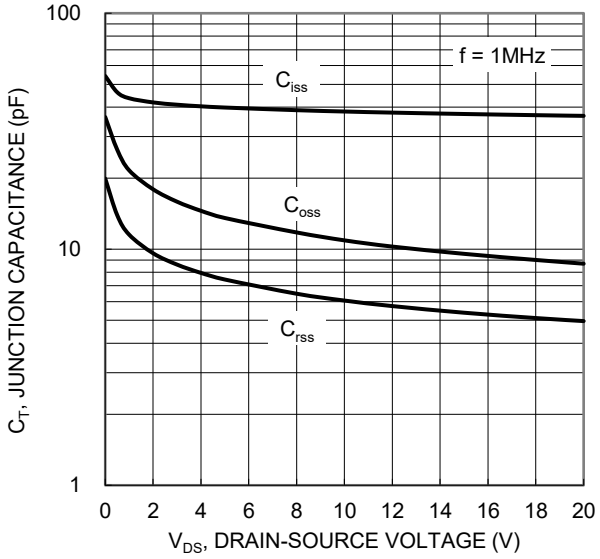


Figure 10. Typical Junction Capacitance

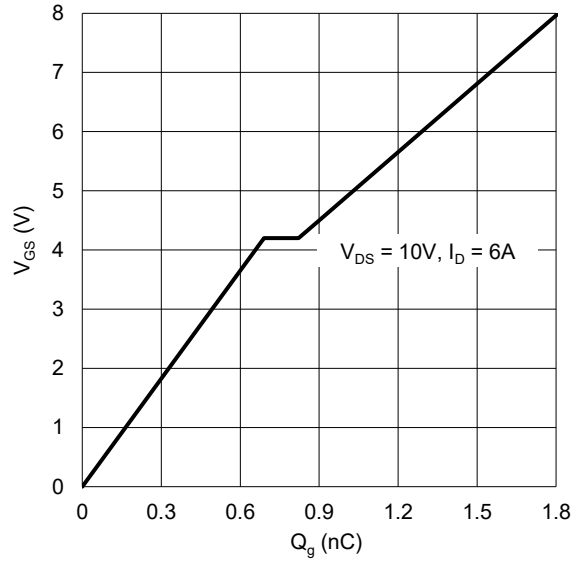


Figure 11. Gate Charge

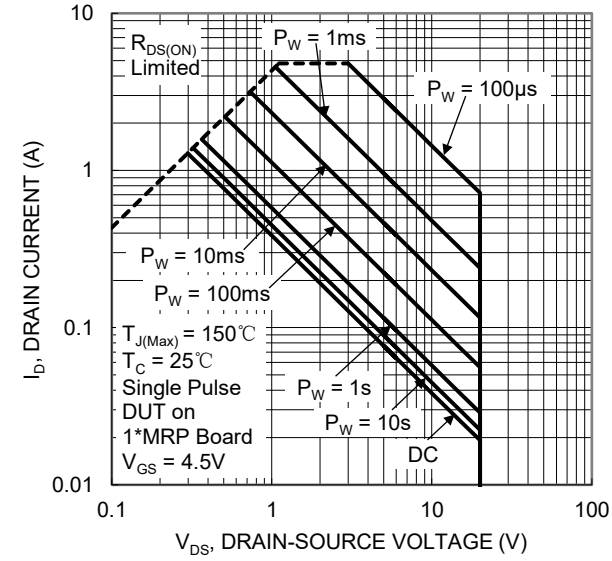


Figure 12. SOA, Safe Operation Area

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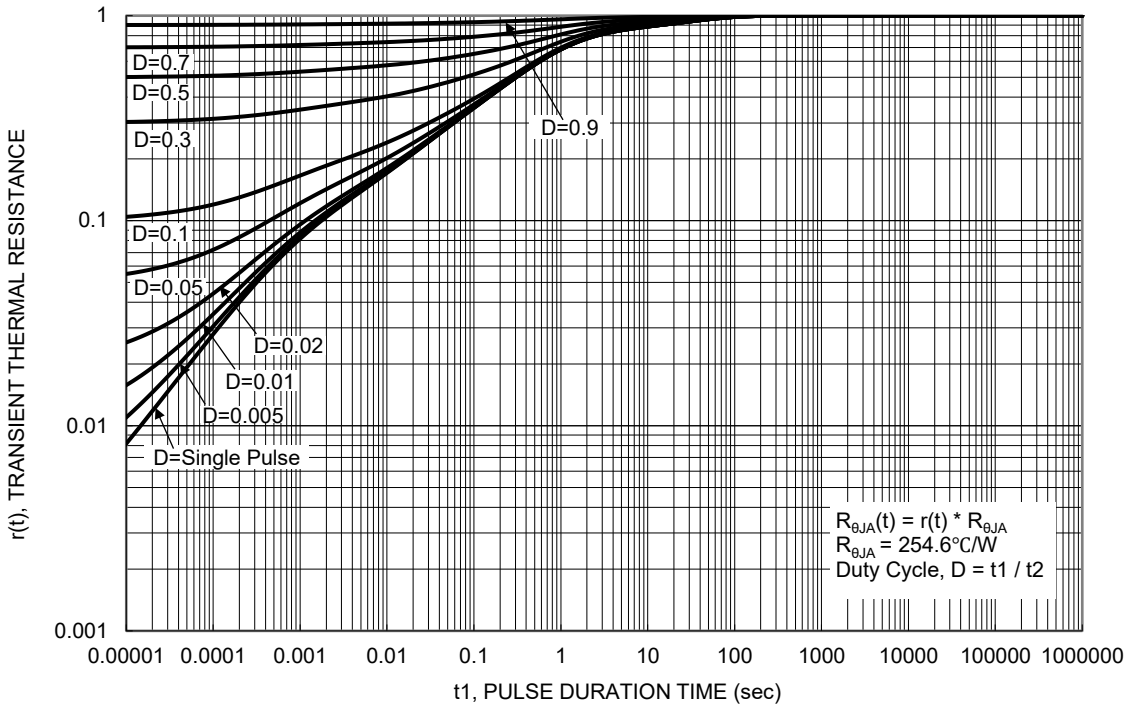


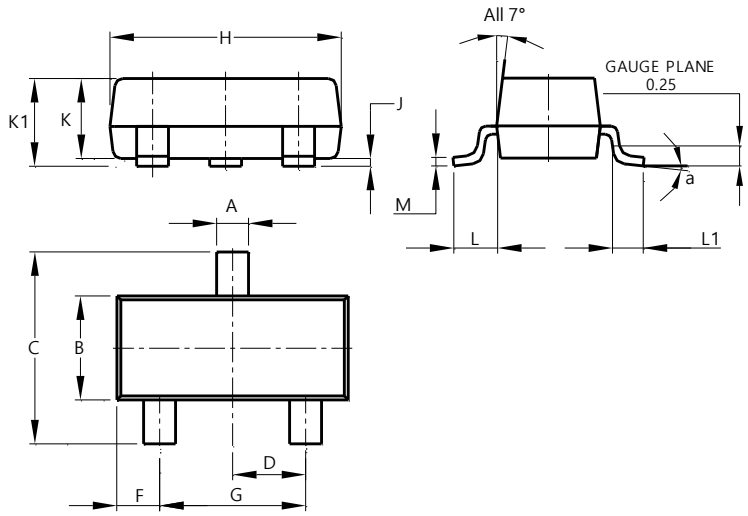
Figure 13. Transient Thermal Resistance

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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

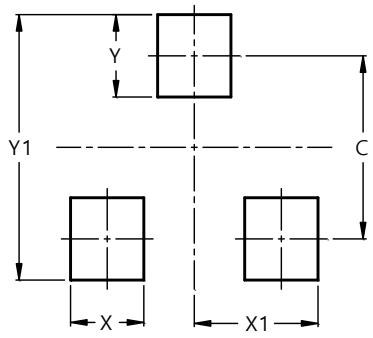


SOT23			
Dim	**Min**	**Max**	**Typ**
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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