30V N-CHANNEL ENHANCEMENT MODE MOSFET
POWERDI®

NEW PRODUCT

Product Summary

$V_{(BR)DSS}$	$R_{DS(ON)\ max}$	I_D T_A = +25°C
30V	12mΩ @ V_{GS} = 10V	10.8A
	16mΩ @ V_{GS} = 4.5V	9.5A

Description and Applications

This new generation MOSFET has been designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Power Management Functions
- Analog Switch

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- <1.1mm Package Profile – Ideal for Thin Applications
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

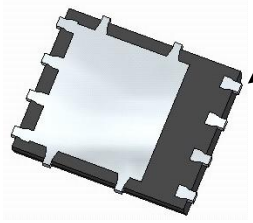
Mechanical Data

- Case: POWERDI5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.097 grams (Approximate)

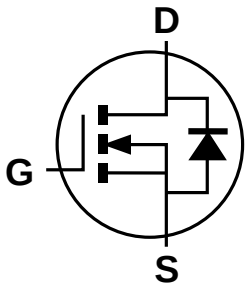
POWERDI5060-8



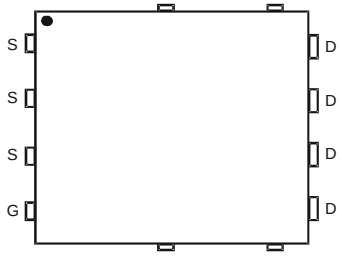
Top View



Bottom View



Internal Schematic



Top View
Pin Configuration

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3016LPS-13	POWERDI5060-8	2500 / Tape & Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



= Manufacturer's Marking
N3016LP = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Digit of Year (ex: 14 = 2014)
WW = Week Code (01 to 53)

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	30	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (Note 6) V_{GS} = 10V	Steady State	T_A = +25°C T_A = +70°C	I_D	10.8 8.5	A
	t<10s	T_A = +25°C T_A = +70°C	I_D	15.5 12.3	A
Continuous Drain Current (Note 6) V_{GS} = 4.5V	Steady State	T_A = +25°C T_A = +70°C	I_D	9.5 7.5	A
	t<10s	T_A = +25°C T_A = +70°C	I_D	13.5 10.8	A
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I_{DM}	70	A
Avalanche Current (Note 7) L = 0.1mH			I_{AS}	22	A
Avalanche Energy (Note 7) L = 0.1mH			E_{AS}	24	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)		P_D	1.18	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	109	°C/W
	t<10s		49	°C/W
Total Power Dissipation (Note 6)		P_D	2.75	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	46	°C/W
	t<10s		24	°C/W
Thermal Resistance, Junction to Case (Note 6)		$R_{\theta JC}$	4.5	°C/W
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV_{DSS}	30	-	-	V	V_{GS} = 0V, I_D = 250µA
Zero Gate Voltage Drain Current	I_{DSS}	-	-	1	µA	V_{DS} = 30V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	-	-	±100	nA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(th)}$	1.4	-	2.0	V	V_{DS} = V_{GS}, I_D = 250µA
Static Drain-Source On-Resistance	$R_{DS\ (ON)}$	-	8.5	12	mΩ	V_{GS} = 10V, I_D = 20A
		-	10.5	16		V_{GS} = 4.5V, I_D = 20A
Diode Forward Voltage	V_{SD}	-	0.7	1.0	V	V_{GS} = 0V, I_S = 1A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C_{iss}	-	1415	-	pF	V_{DS} = 15V, V_{GS} = 0V, f = 1.0MHz
Output Capacitance	C_{oss}	-	119	-	pF	
Reverse Transfer Capacitance	C_{rss}	-	82	-	pF	
Gate Resistance	R_g	-	3.0	-	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz
Total Gate Charge (V_{GS} = 4.5V)	Q_g	-	11.3	-	nC	V_{DS} = 15V, I_D = 12A
Total Gate Charge (V_{GS} = 10V)	Q_g	-	25.1	-	nC	
Gate-Source Charge	Q_{gs}	-	3.5	-	nC	
Gate-Drain Charge	Q_{gd}	-	3.6	-	nC	
Turn-On Delay Time	$t_{D(ON)}$	-	4.8	-	ns	V_{DD} = 15V, V_{GS} = 10V, R_L = 1.25Ω, R_G = 3Ω
Turn-On Rise Time	t_R	-	16.5	-	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	-	26.1	-	ns	
Turn-Off Fall Time	t_F	-	5.6	-	ns	
Reverse Recovery Time	t_{RR}	-	12.3	-	ns	I_F = 12A, di/dt = 500A/µs
Reverse Recovery Charge	Q_{rr}	-	10.4	-	nC	

Notes:
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep T_J = +25°C.
8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.

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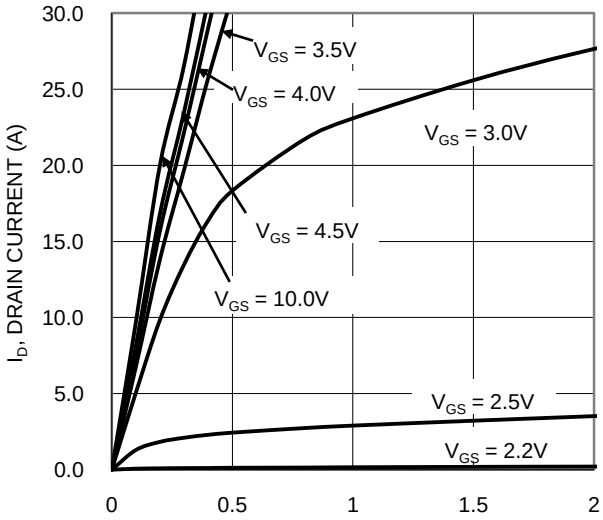


Figure1 Typical Output Characteristic

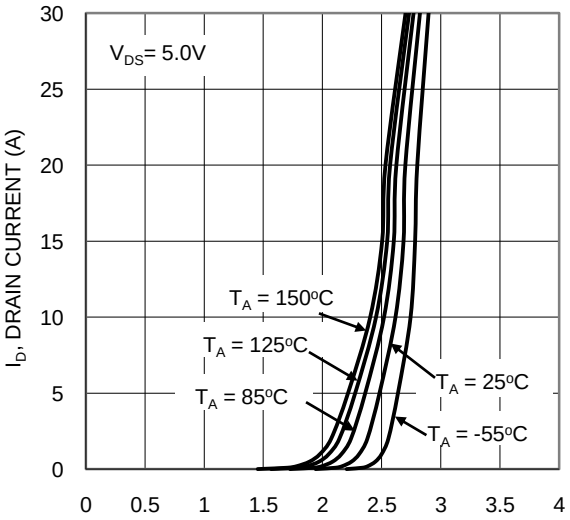


Figure 2 Typical Transfer Characteristic

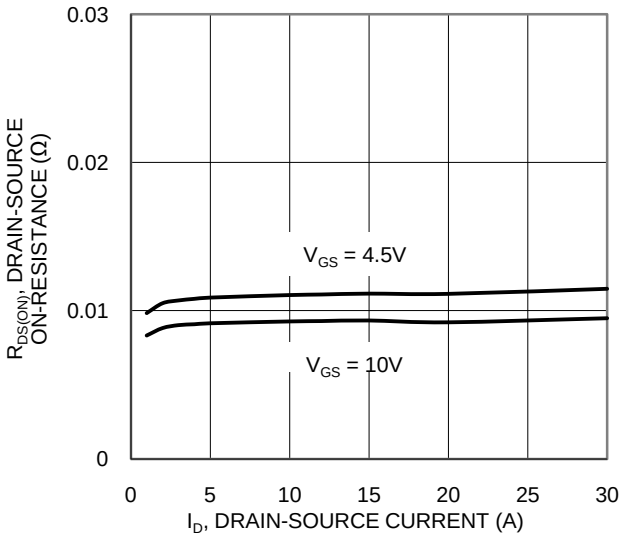


Figure 3 Typical On-Resistance vs Drain Current and Gate Voltage

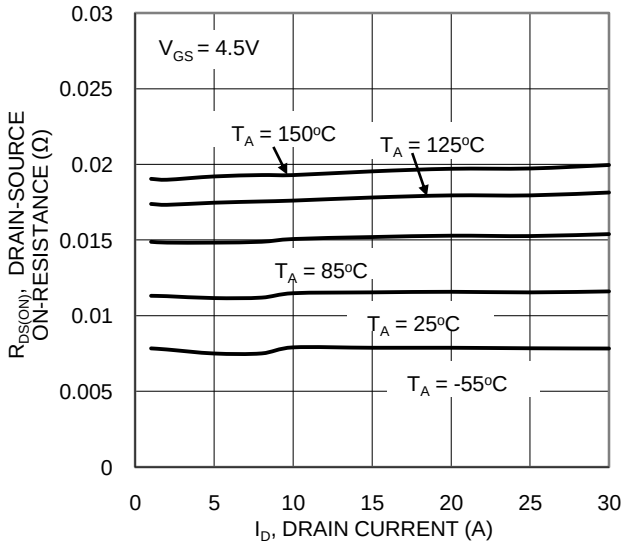


Figure 4 Typical On-Resistance vs Drain Current and Temperature

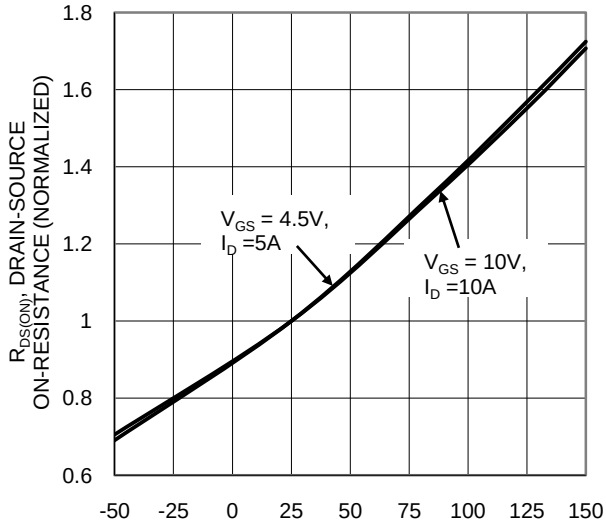


Figure 5 On-Resistance Variation with Temperature

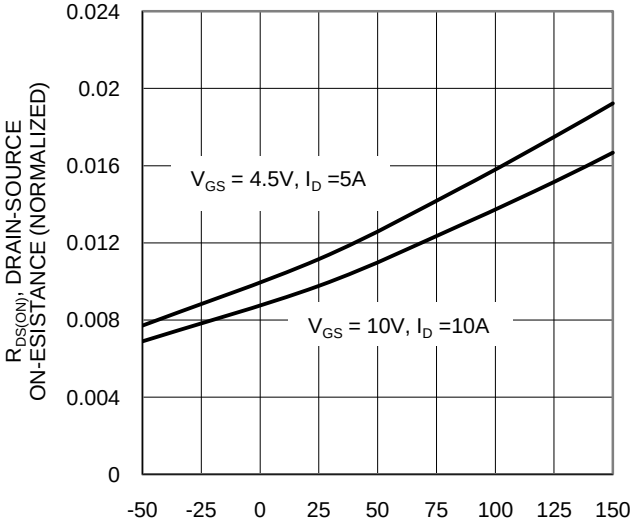


Figure 6 On-Resistance Variation with Temperature

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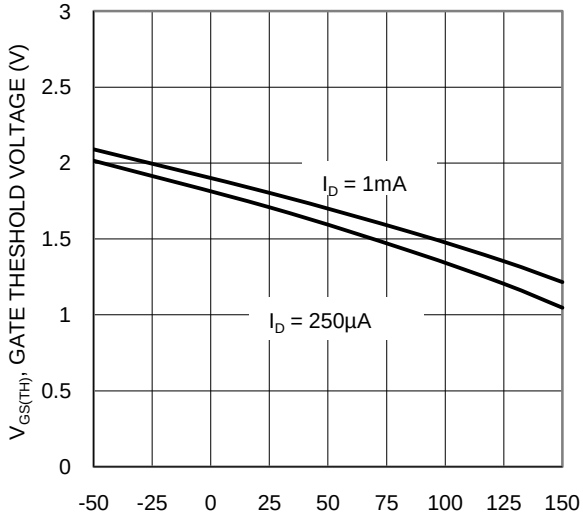


Figure 7 Gate Theshold Variation vs Junction Temperature

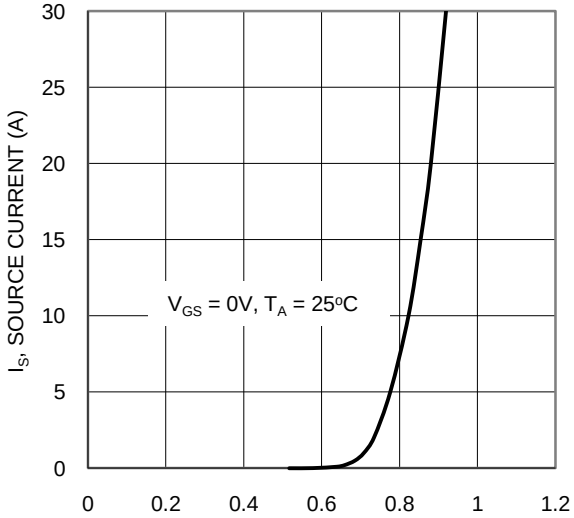


Figure 8 Diode Forward Voltage vs Current

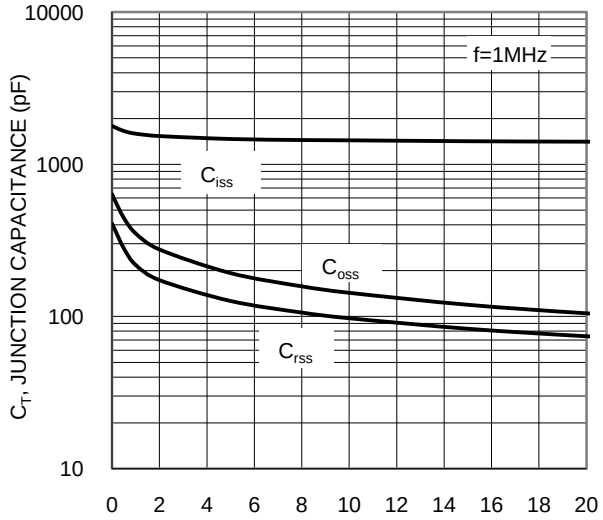


Figure 9 Typical Junction Capacitance

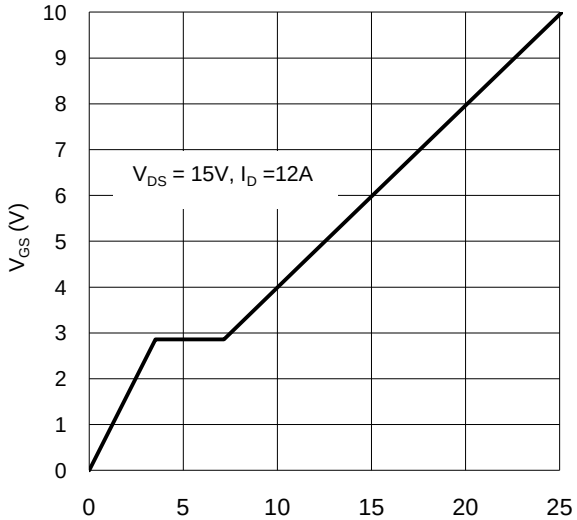


Figure 10 Gate Charge

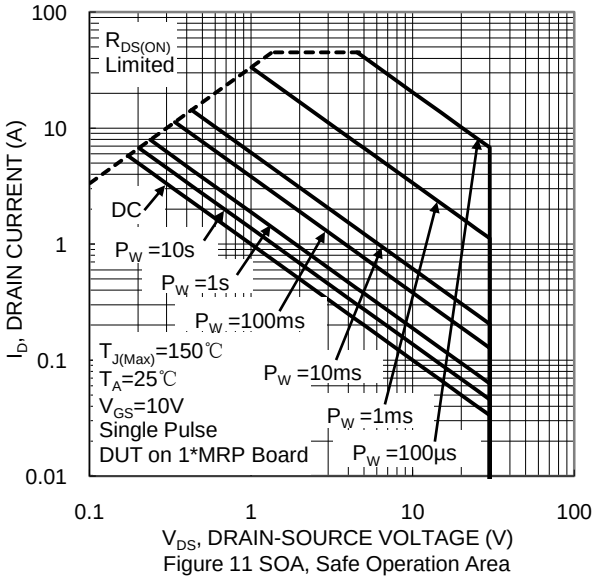


Figure 11 SOA, Safe Operation Area

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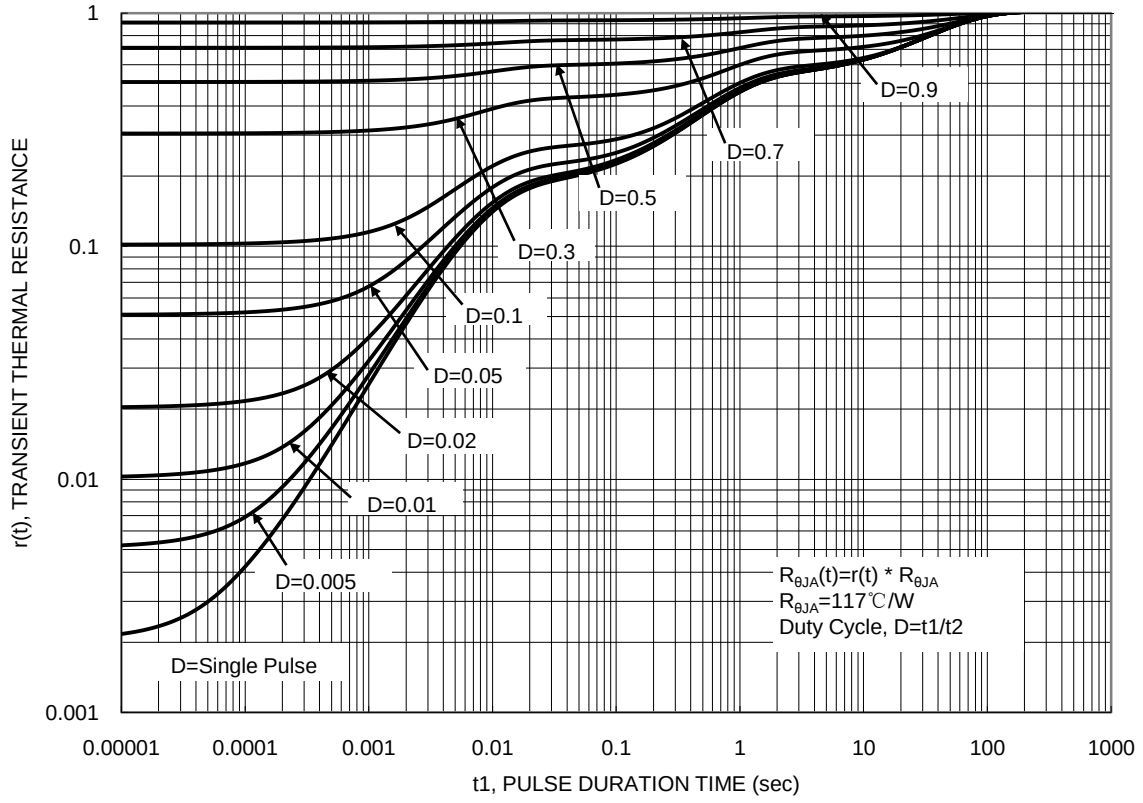


Figure 12 Transient Thermal Resistance

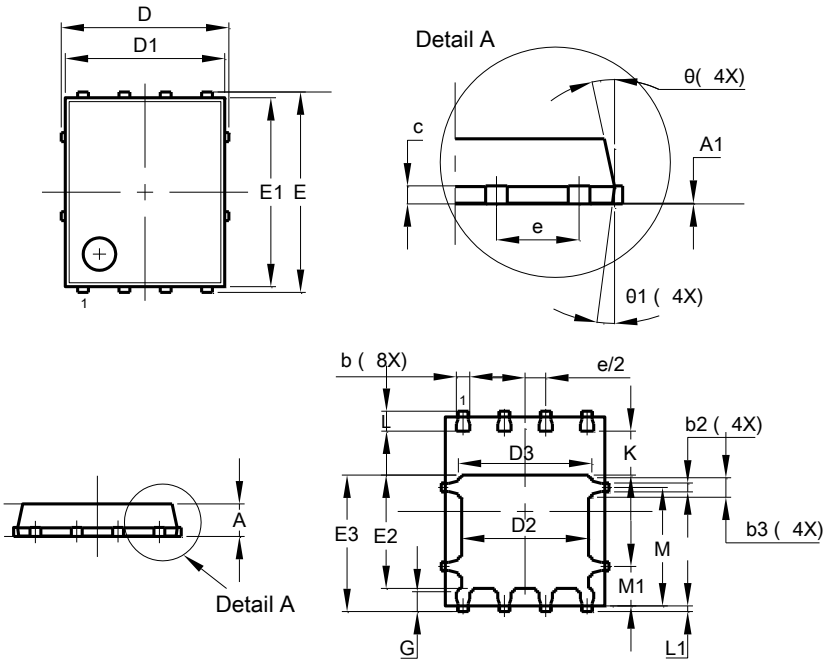
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Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

(1) Package Type: POWERDI®5060-8

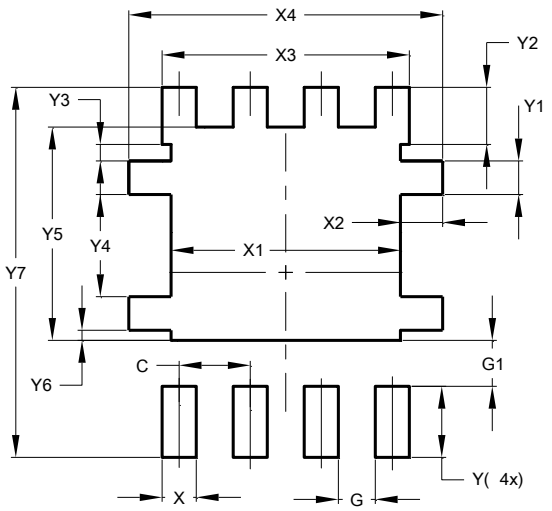


POWERDI®5060-8			
Dim	**Min**	**Max**	**Typ**
A	0.90	1.10	1.00
A1	0.00	0.05	
b	0.33	0.51	0.41
b2	0.200	0.350	0.273
b3	0.40	0.80	0.60
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.70	4.10	3.90
D3	3.90	4.30	4.10
E	6.15 BSC		
E1	5.60	6.00	5.80
E2	3.28	3.68	3.48
E3	3.99	4.39	4.19
e	1.27 BSC		
G	0.51	0.71	0.61
K	0.51		
L	0.51	0.71	0.61
L1	0.100	0.200	0.175
M	3.235	4.035	3.635
M1	1.00	1.40	1.21
Θ	10º	12º	11º
Θ1	6º	8º	7º
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

(1) Package Type: POWERDI®5060-8



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610

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