## Product Summary

| $BV_{DSS}$ | $R_{DS(ON)}$ | $I_D$ $T_A$ = +25°C |
|---|---|---|
| 60V | 2Ω @ $V_{GS}$ = 10V | 407mA |
| | 3Ω @ $V_{GS}$ = 5V | 332mA |

## Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/**

## Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

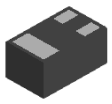
- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays

## Mechanical Data

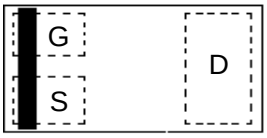
- Case: X1-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 e4
- Weight: 0.001 grams (Approximate)



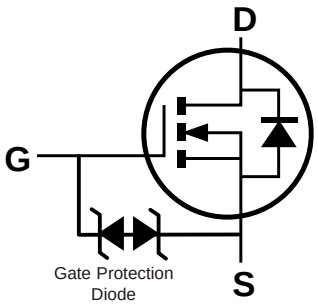

X1-DFN1006-3



Bottom View




Top View
Pin-Out



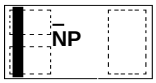

Equivalent Circuit

## Ordering Information (Note 4)

| Part Number | Case | Packaging |
|---|---|---|
| DMN62D4LFB-7B | X1-DFN1006-3 | 10,000 / Tape & Reel |

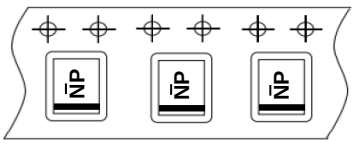
Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## Marking Information




Top View
Bar Denotes Gate and Source Side

$\overline{NP}$= Part Marking Code





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## Maximum Ratings (@$T_A$ = +25°C, unless otherwise specified.)

| Characteristic | | | Symbol | Value | Unit |
|---|---|---|---|---|---|
| Drain-Source Voltage | | | $V_{DSS}$ | 60 | V |
| Gate-Source Voltage | | | $V_{GSS}$ | ±20 | V |
| Continuous Drain Current (Note 5) $V_{GS}$ = 10V | Steady State | $T_A$ = +25°C<br>$T_A$ = +70°C | $I_D$ | 407<br>325 | mA |
| Maximum Body Diode Continuous Current (Note 5) | | | $I_S$ | 407 | mA |
| Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) | | | $I_{DM}$ | 1 | A |
| Pulsed Source Current (10µs Pulse, Duty Cycle = 1%) | | | $I_{SM}$ | 1 | A |

## Thermal Characteristics

| Characteristic | Symbol | Max | Unit |
|---|---|---|---|
| Power Dissipation (Note 5) | $P_D$ | 0.5 | W |
| Thermal Resistance, Junction to Ambient @$T_A$ = +25°C (Note 5) | $R_{\theta JA}$ | 251 | °C/W |
| Power Dissipation (Note 6) | $P_D$ | 1.2 | W |
| Thermal Resistance, Junction to Ambient @$T_A$ = +25°C (Note 6) | $R_{\theta JA}$ | 103 | °C/W |
| Operating and Storage Temperature Range | $T_J$, $T_{STG}$ | -55 to +150 | °C |

## Electrical Characteristics (@ $T_A$ = +25°C, unless otherwise stated.)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
|---|---|---|---|---|---|---|
| **OFF CHARACTERISTICS (Note 7)** | | | | | | |
| Drain-Source Breakdown Voltage | $BV_{DSS}$ | 60 | — | — | V | $V_{GS}$ = 0V, $I_D$ = 250µA |
| Zero Gate Voltage Drain Current $T_J$ = +25°C | $I_{DSS}$ | — | — | 1.0 | µA | $V_{DS}$ = 60V, $V_{GS}$ = 0V |
| Gate-Source Leakage | $I_{GSS}$ | — | — | ±10 | µA | $V_{GS}$ = ±20V, $V_{DS}$ = 0V |
| **ON CHARACTERISTICS (Note 7)** | | | | | | |
| Gate Threshold Voltage | $V_{GS(TH)}$ | 1.0 | — | 2.5 | V | $V_{DS}$ = $V_{GS}$, $I_D$ = 250µA |
| Static Drain-Source On-Resistance | $R_{DS(ON)}$ | — | 1.33 | 2 | Ω | $V_{GS}$ = 10V, $I_D$ = 0.5A |
| | | — | 1.36 | 3 | | $V_{GS}$ = 5V, $I_D$ = 0.05A |
| Diode Forward Voltage | $V_{SD}$ | — | 0.8 | 1.1 | V | $V_{GS}$ = 0V, $I_S$ = 115mA |
| **DYNAMIC CHARACTERISTICS (Note 8)** | | | | | | |
| Input Capacitance | $C_{iss}$ | — | 40 | — | pF | $V_{DS}$ = 30V, $V_{GS}$ = 0V, f = 1.0MHz |
| Output Capacitance | $C_{oss}$ | — | 4.8 | — | | |
| Reverse Transfer Capacitance | $C_{rss}$ | — | 2.8 | — | | |
| Gate Resistance | $R_g$ | — | 240 | — | Ω | $V_{DS}$ = 0V, $V_{GS}$ = 0V, f = 1MHz |
| Total Gate Charge($V_{GS}$ = 10V) | $Q_g$ | — | 1.1 | — | nC | $V_{DS}$ = 15V, $I_D$ = 250mA |
| Total Gate Charge($V_{GS}$ = 4.5V) | $Q_g$ | — | 0.6 | — | | |
| Gate-Source Charge | $Q_{gs}$ | — | 0.2 | — | | |
| Gate-Drain Charge | $Q_{gd}$ | — | 0.3 | — | | |
| Turn-On Delay Time | $t_{D(ON)}$ | — | 5.4 | — | ns | $V_{GS}$ = 10V, $V_{DS}$ = 30V, $R_L$ = 150Ω, $R_G$ = 25Ω, $I_D$ = 200mA |
| Turn-On Rise Time | $t_R$ | — | 2.2 | — | ns | |
| Turn-Off Delay Time | $t_{D(OFF)}$ | — | 34 | — | ns | |
| Turn-Off Fall Time | $t_F$ | — | 11 | — | ns | |

Notes:
5. Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to production testing.


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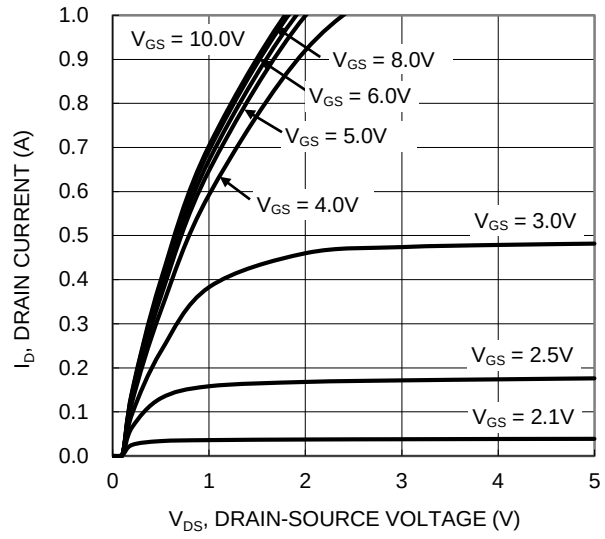



Figure 1. Typical Output Characteristic

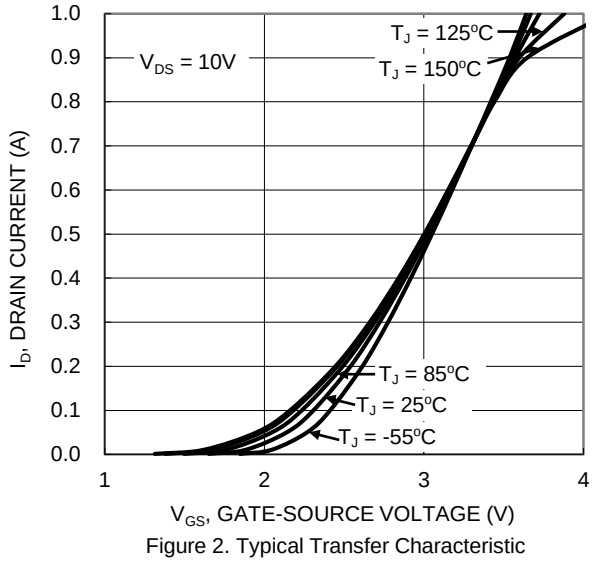



Figure 2. Typical Transfer Characteristic

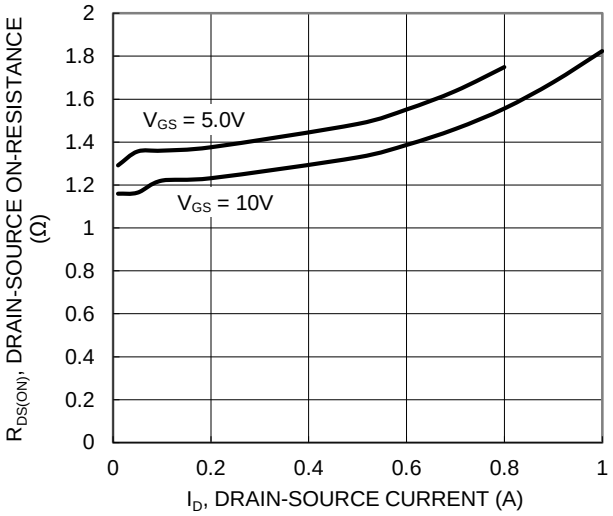



Figure 3. Typical On-Resistance vs Drain Current and Gate Voltage

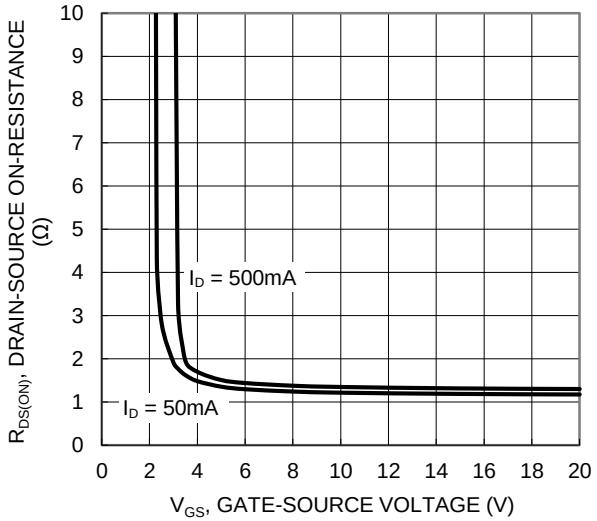



Figure 4. Typical Transfer Characteristic

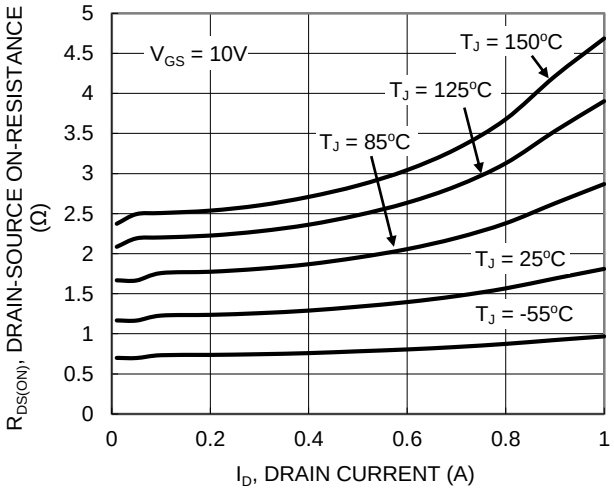



Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

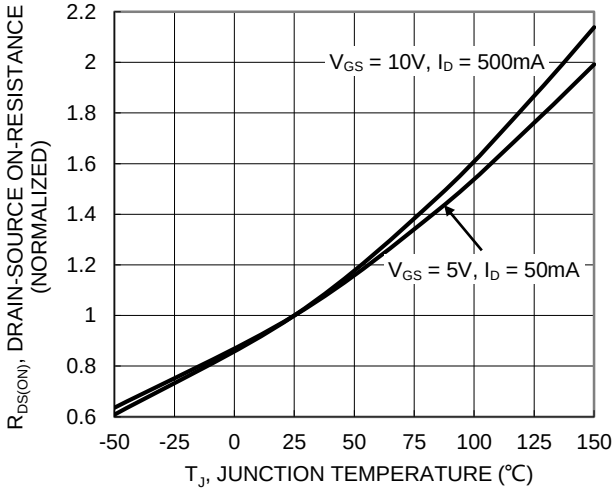



Figure 6. On-Resistance Variation with Junction Temperature


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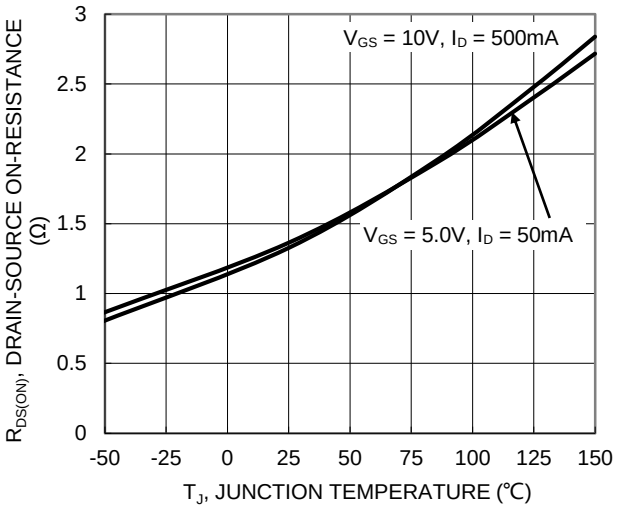



Figure 7. On-Resistance Variation with Junction Temperature

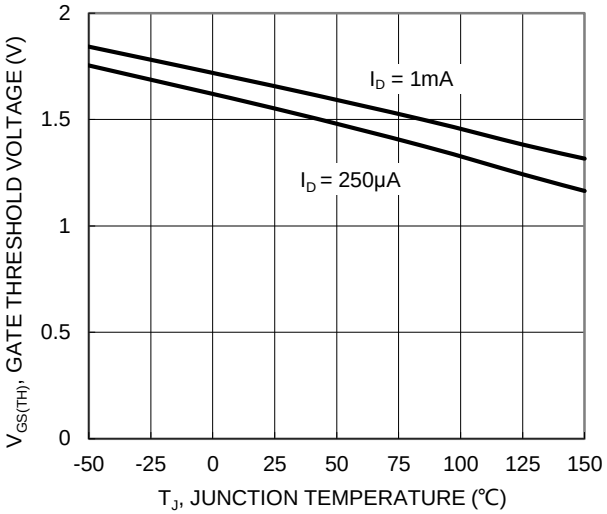



Figure 8. Gate Threshold Variation vs Junction Temperature

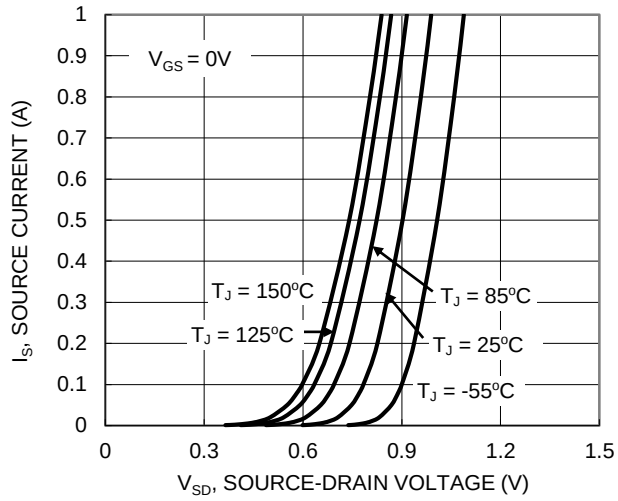



Figure 9. Diode Forward Voltage vs. Current

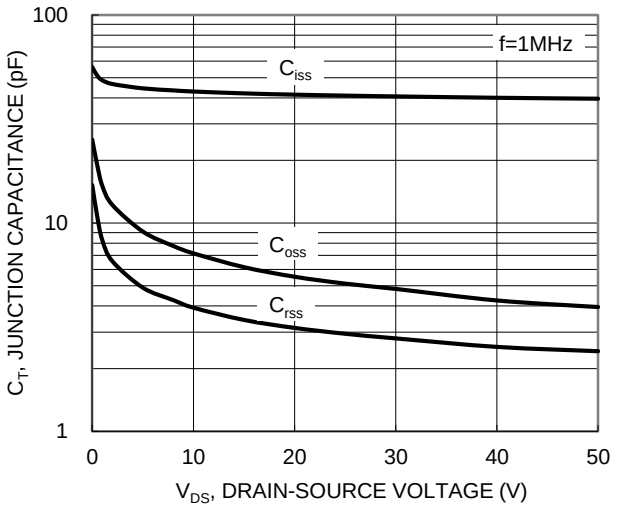



Figure 10. Typical Junction Capacitance

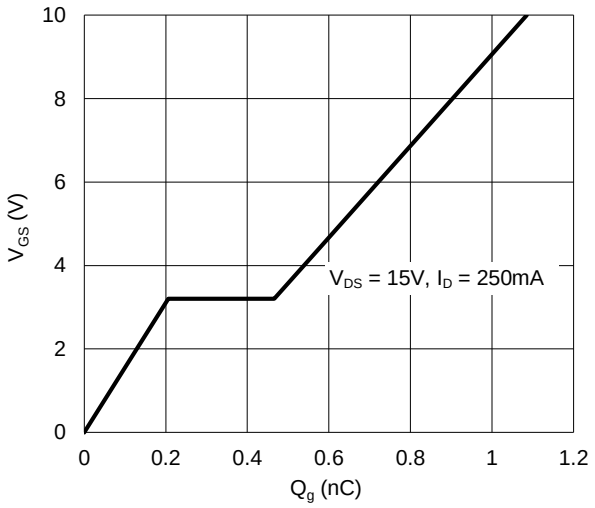



Figure 11. Gate Charge

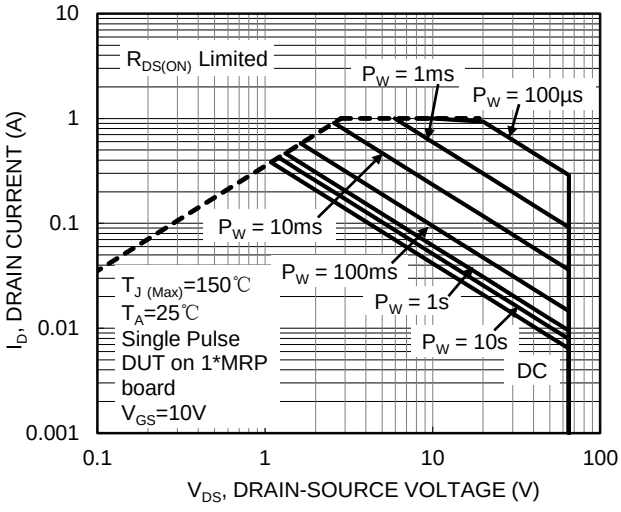



Figure 12. SOA, Safe Operation Area


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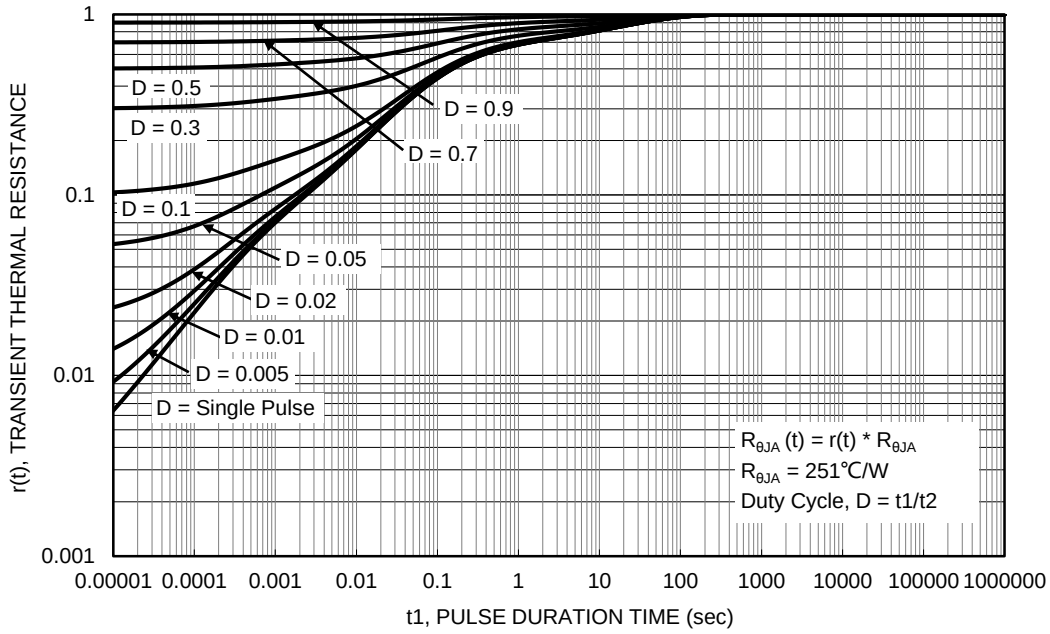


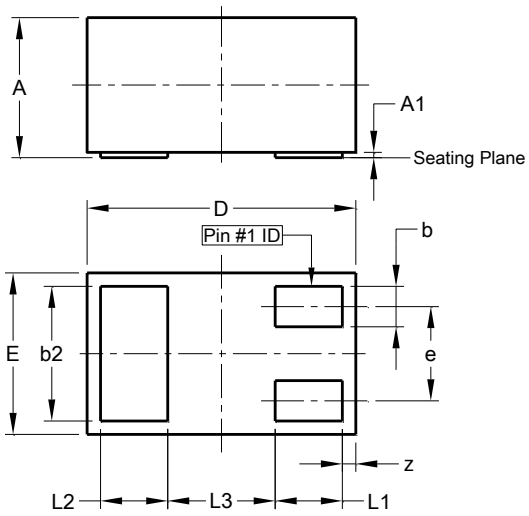

Figure 13. Transient Thermal Resistance


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## Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

**X1-DFN1006-3**

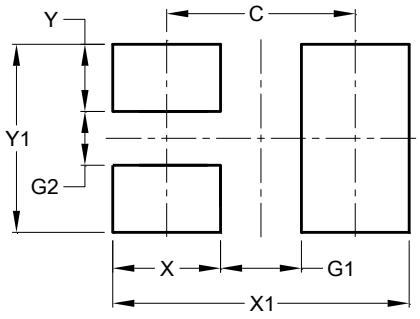



| X1-DFN1006-3 | | | |
|---|---|---|---|
| **Dim** | **Min** | **Max** | **Typ** |
| **A** | 0.47 | 0.53 | 0.50 |
| **A1** | 0.00 | 0.05 | 0.03 |
| **b** | 0.10 | 0.20 | 0.15 |
| **b2** | 0.45 | 0.55 | 0.50 |
| **D** | 0.95 | 1.075 | 1.00 |
| **E** | 0.55 | 0.675 | 0.60 |
| **e** | - | - | 0.35 |
| **L1** | 0.20 | 0.30 | 0.25 |
| **L2** | 0.20 | 0.30 | 0.25 |
| **L3** | - | - | 0.40 |
| **z** | 0.02 | 0.08 | 0.05 |
| **All Dimensions in mm** | | | |

## Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

**X1-DFN1006-3**




| Dimensions | Value (in mm) |
|---|---|
| **C** | 0.70 |
| **G1** | 0.30 |
| **G2** | 0.20 |
| **X** | 0.40 |
| **X1** | 1.10 |
| **Y** | 0.25 |
| **Y1** | 0.70 |


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