Product Summary

BV_{DSS}	$R_{DS(ON)}$ Max	I_D T_C = +25°C
-20V	100mΩ @ V_{GS} = -4.5V	-1.5A

Features and Benefits

- Low On-Resistance: $R_{DS(ON)}$
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 standards for High Reliability**
- **PPAP Capable (Note 4)**

Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Engine Management Systems
- DC-DC Converters
- Body Control Electronics

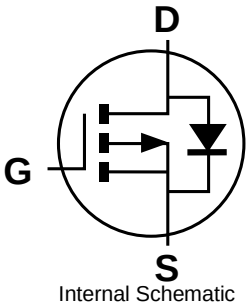
Mechanical Data

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish - Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 e3
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)

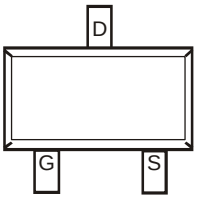
SOT323



Top View



Internal Schematic



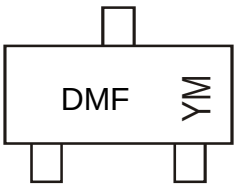
Top View

Ordering Information (Note 5)

Part Number	Case	Packaging
DMP2160UWQ-7	SOT323	3,000/Tape & Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product_compliance_definitions.html
5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



DMF = Product Type Marking Code
YM = Date Code Marking
Y or $\overline{Y}$ = Year (ex: E = 2017)
M = Month (ex: 9 = September)

Date Code Key

Year	2016	2017	2018	2019	2020	2021	2022
Code	D	E	F	G	H	I	J

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Drain-Source Voltage		V_{DSS}	-20	V
Gate-Source Voltage		V_{GSS}	±10	V
Drain Current (Note 6) Steady State	T_A = +25°C T_A = +70°C	I_D	-1.5 -1.2	A
Pulsed Drain Current		I_{DM}	-10	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 6)	P_D	350	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	360	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	-20	—	—	V	V_{GS} = 0V, I_D = -250µA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1	µA	V_{DS} = -20V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±100 ±800	nA	V_{GS} = ±8V, V_{DS} = 0V V_{GS} = ±10V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(th)}$	-0.4	-0.6	-0.9	V	V_{DS} = V_{GS}, I_D = -250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	75 90	100 120	mΩ	V_{GS} = -4.5V, I_D = -1.5A V_{GS} = -2.5V, I_D = -1.2A
Forward Transconductance	g_{FS}	—	4	—	S	V_{DS} =-10V, I_D = -1.5A
Diode Forward Voltage (Note 8)	V_{SD}	—	—	-1.0	V	V_{GS} = 0V, I_S = -1.0A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{ISS}	—	627	—	pF	V_{DS} = -10V, V_{GS} = 0V f = 1.0MHz
Output Capacitance	C_{OSS}	—	64	—	pF	
Reverse Transfer Capacitance	C_{RSS}	—	53	—	pF	

Notes: 6.Device mounted on 1in^2 FR-4 PCB with 2 oz. Copper. t ≤ 10 sec.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to production testing.

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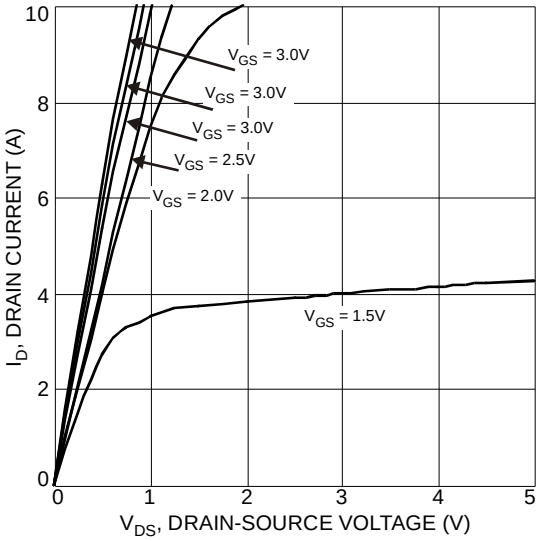


Fig. 1 Typical Output Characteristics

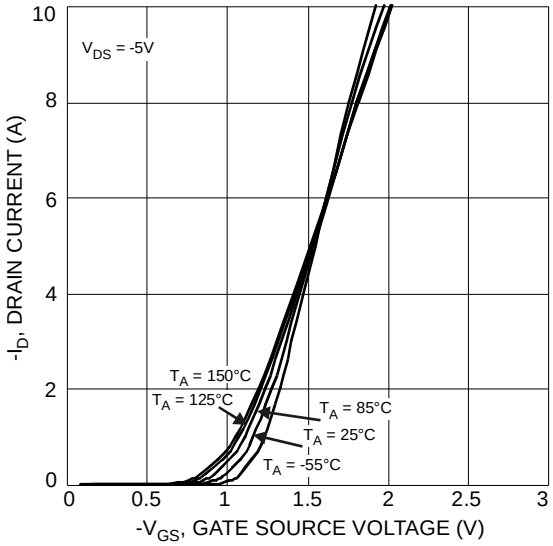


Fig. 2 Typical Transfer Characteristics

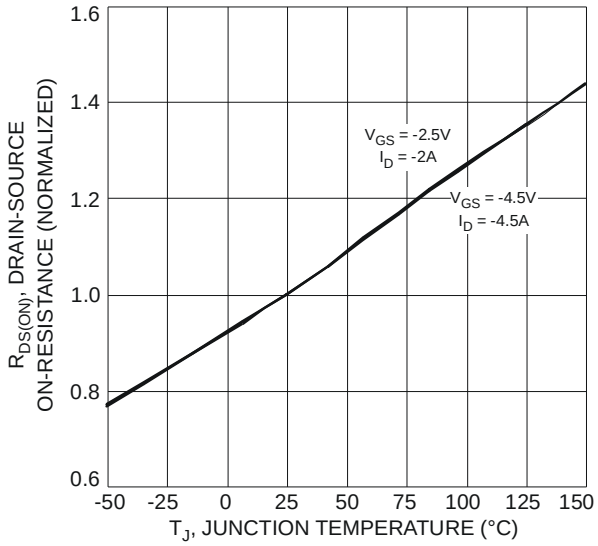


Fig. 3 On-Resistance Variation with Temperature

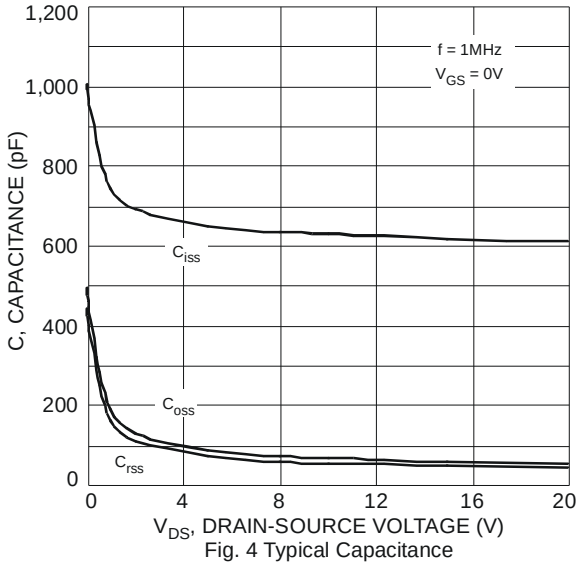


Fig. 4 Typical Capacitance

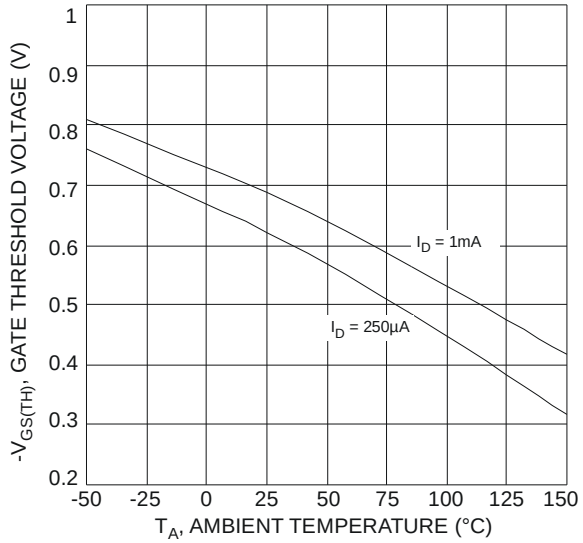


Fig. 5 Gate Threshold Variation vs. Ambient Temperature

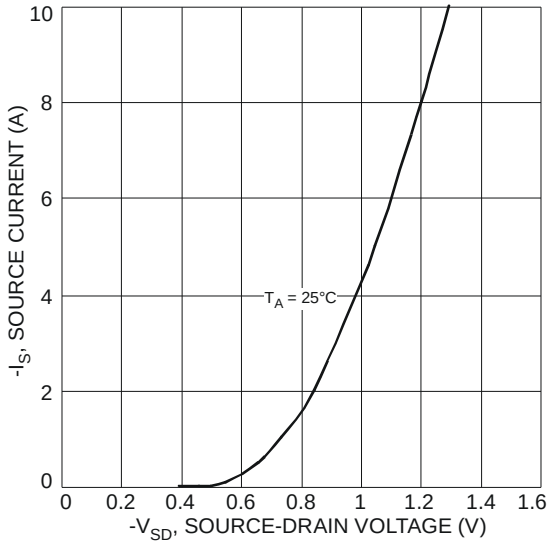


Fig. 6 Diode Forward Voltage vs. Current

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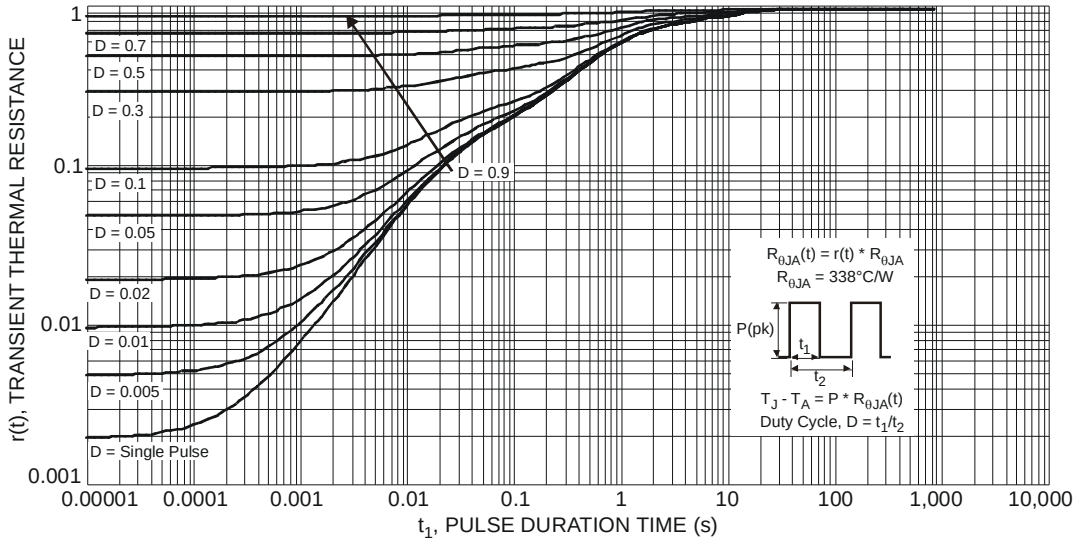


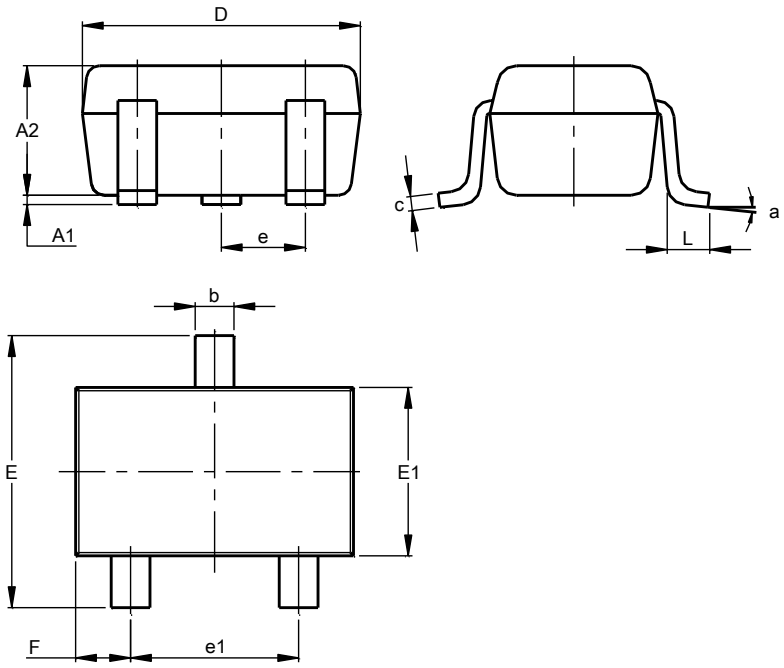
Fig. 7 Transient Thermal Response

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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323

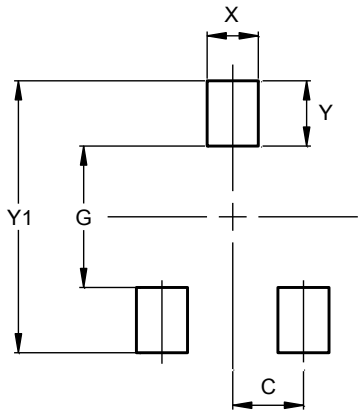


SOT323			
Dim	**Min**	**Max**	**Typ**
A1	0.00	0.10	0.05
A2	0.90	1.00	0.95
b	0.25	0.40	0.30
c	0.10	0.18	0.11
D	1.80	2.20	2.15
E	2.00	2.20	2.10
E1	1.15	1.35	1.30
e	0.650 BSC		
e1	1.20	1.40	1.30
F	0.375	0.475	0.425
L	0.25	0.40	0.30
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323



Dimensions	Value (in mm)
C	0.650
G	1.300
X	0.470
Y	0.600
Y1	2.500

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