Product Summary

BV_{DSS}	$R_{DS(ON)}$ Max	I_D Max T_C = +25°C
-30V	28mΩ @ V_{GS} = -10V	-21A
	38mΩ @ V_{GS} = -4.5V	-18A

Features and Benefits

- Low $R_{DS(ON)}$ – Minimizes On-State Losses
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- 100% Unclamped Inductive Switching – Ensures More Reliability
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Backlighting
- Power Management Functions
- DC-DC Converters

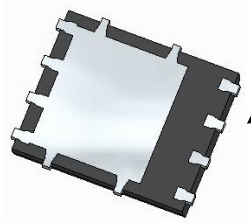
Mechanical Data

- Case: PowerDI5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.097 grams (Approximate)

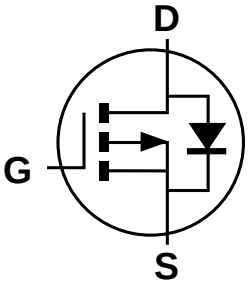
PowerDI5060-8



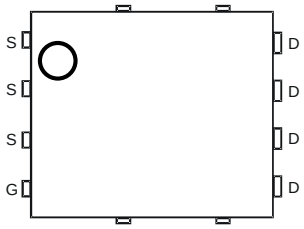
Top View



Bottom View



Internal Schematic



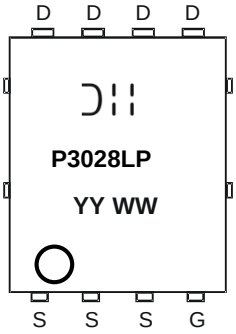
Top View
Pin Configuration

Ordering Information (Note 5)

Part Number	Case	Packaging
DMP3028LPSQ-13	PowerDI5060-8	2,500/Tape & Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/product-compliance-definitions/.
5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



= Manufacturer's Marking
P3028LP = Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 17 = 2017)
WW = Week (01 to 53)

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	-30	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current, V_{GS} = -10V (Note 8)	T_C = +25°C T_C = +70°C	I_D	-21 -17	A
Maximum Continuous Body Diode Forward Current (Note 8)		I_S	-20	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I_{DM}	-70	A
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		I_{SM}	-70	A
Avalanche Current, L = 0.1mH (Note 9)		I_{AS}	-21.4	A
Avalanche Energy, L = 0.1mH (Note 9)		E_{AS}	22	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)		P_D	1.28	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	100	°C/W
Total Power Dissipation (Note 7)		P_D	2.12	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	$R_{\theta JA}$	60	°C/W
Total Power Dissipation (Note 8)		P_D	35	W
Thermal Resistance, Junction to Case (Note 8)		$R_{\theta JC}$	3.0	°C/W
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 10)						
Drain-Source Breakdown Voltage	BV_{DSS}	-30	—	—	V	V_{GS} = 0V, I_D = -250µA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	-1	µA	V_{DS} = -30V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±100	nA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 10)						
Gate Threshold Voltage	$V_{GS(TH)}$	-1.0	-1.3	-2.4	V	V_{DS} = V_{GS}, I_D = -250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	18	28	mΩ	V_{GS} = -10V, I_D = -7A
		—	28	38		V_{GS} = -4.5V, I_D = -6.2A
Diode Forward Voltage	V_{SD}	—	-0.7	-1.2	V	V_{GS} = 0V, I_S = -2.1A
DYNAMIC CHARACTERISTICS (Note 11)						
Input Capacitance	C_{iss}	—	1372	—	pF	V_{DS} = -15V, V_{GS} = 0V, f = 1.0MHz
Output Capacitance	C_{oss}	—	161	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	127	—	pF	
Gate Resistance	R_g	—	8.5	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz
Total Gate Charge (V_{GS} = -4.5V)	Q_g	—	11	—	nC	V_{DS} = -15V, I_D = -7A
Total Gate Charge (V_{GS} = -10V)	Q_g	—	22	—	nC	
Gate-Source Charge	Q_{gs}	—	3.0	—	nC	
Gate-Drain Charge	Q_{gd}	—	3.7	—	nC	
Turn-On Delay Time	$t_{D(ON)}$	—	4.8	—	ns	V_{DD} = -15V, V_{GS} = -10V, R_g = 6Ω, I_D = -7A
Turn-On Rise Time	t_R	—	5.5	—	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	—	32.8	—	ns	
Turn-Off Fall Time	t_F	—	17.74	—	ns	
Reverse Recovery Time	t_{RR}	—	10.8	—	ns	I_S = -7A, dI/dt = 100A/µs
Reverse Recovery Charge	Q_{RR}	—	3.4	—	nC	

Notes:
6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
7. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
8. Thermal resistance from junction to soldering point (on the exposed drain pad).
9. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
10. Short duration pulse test used to minimize self-heating effect.
11. Guaranteed by design. Not subject to product testing.

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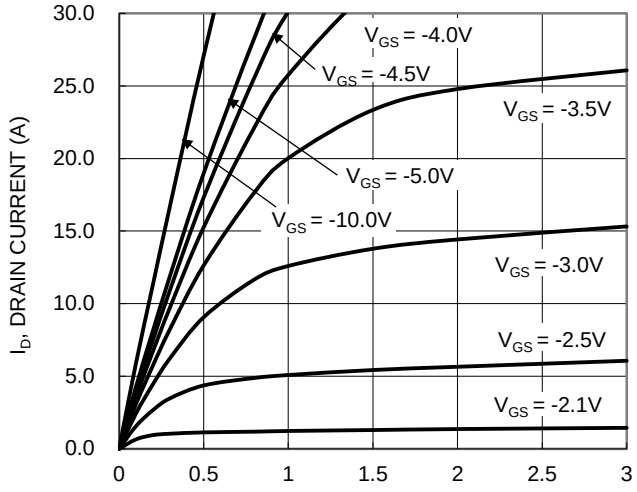


Figure 1. Typical Output Characteristic

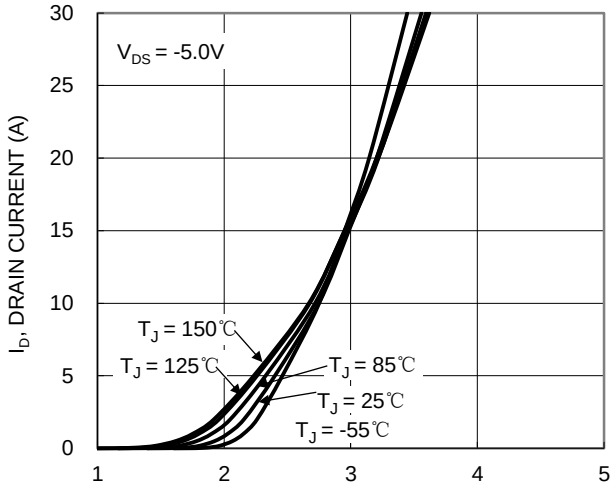


Figure 2. Typical Transfer Characteristic

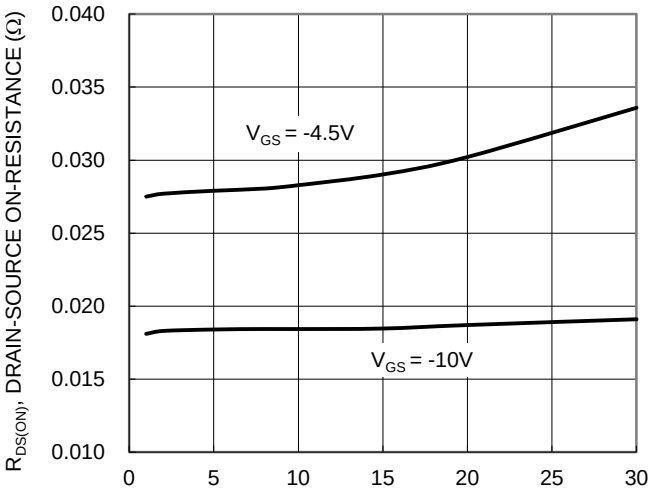


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

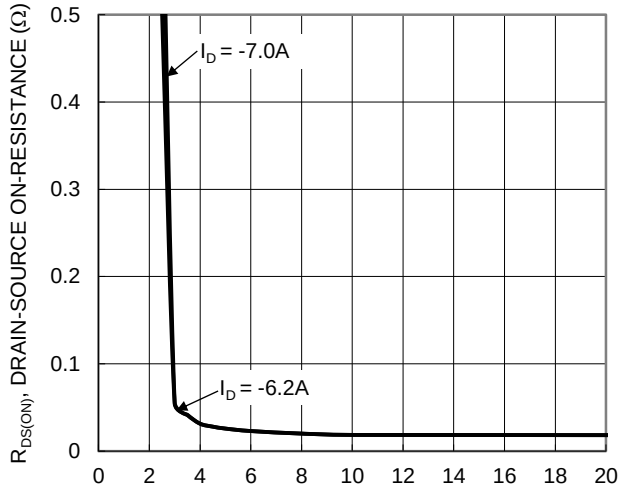


Figure 4. Typical Transfer Characteristic

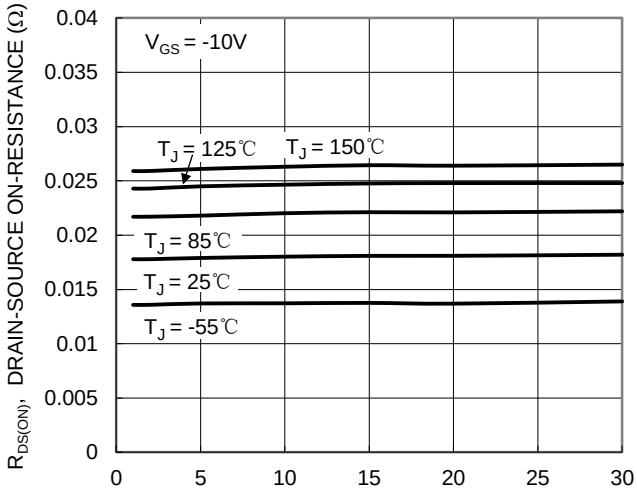


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

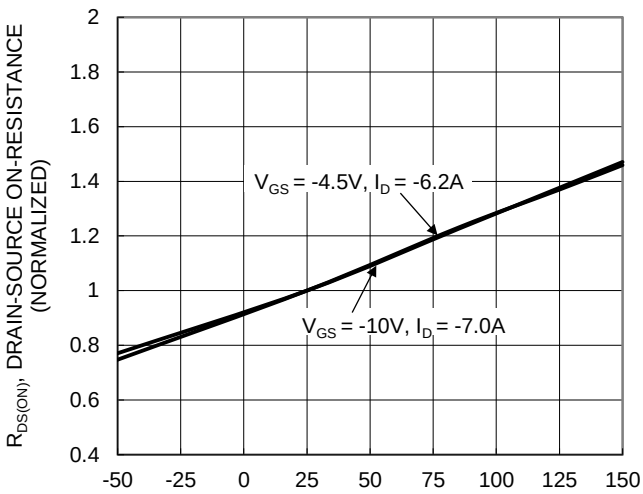


Figure 6. On-Resistance Variation with Temperature

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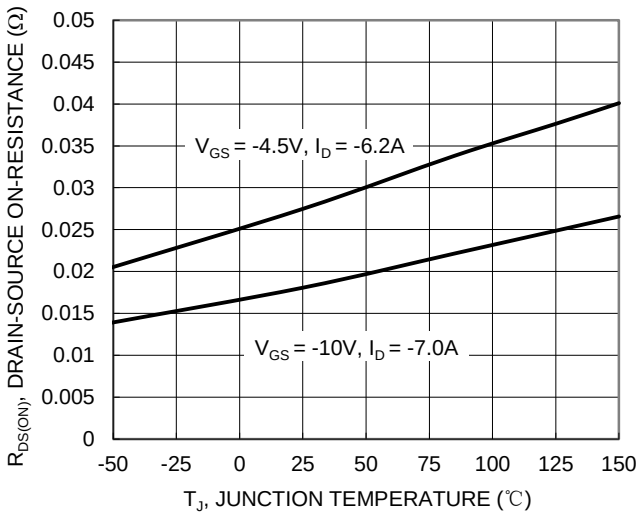


Figure 7. On-Resistance Variation with Temperature

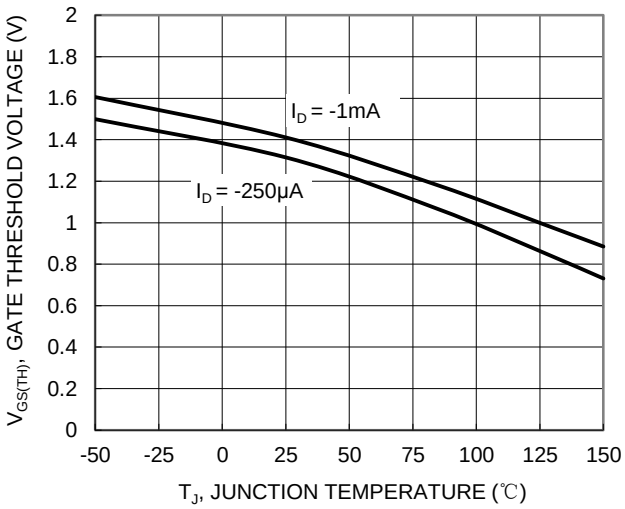


Figure 8. Gate Threshold Variation vs. Junction Temperature

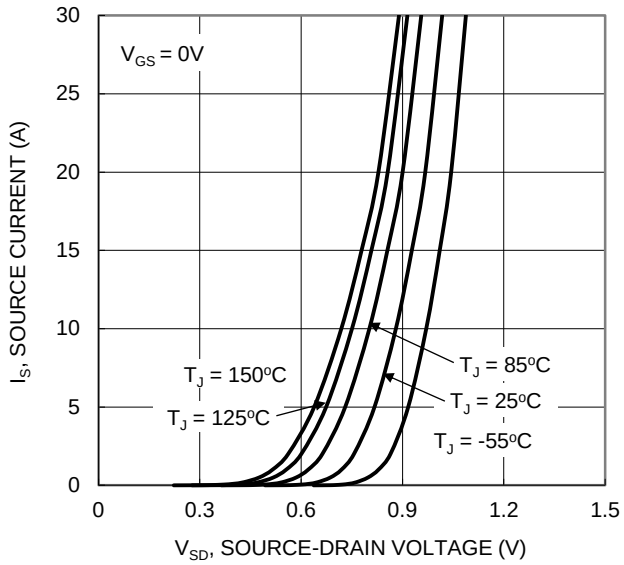


Figure 9. Diode Forward Voltage vs. Current

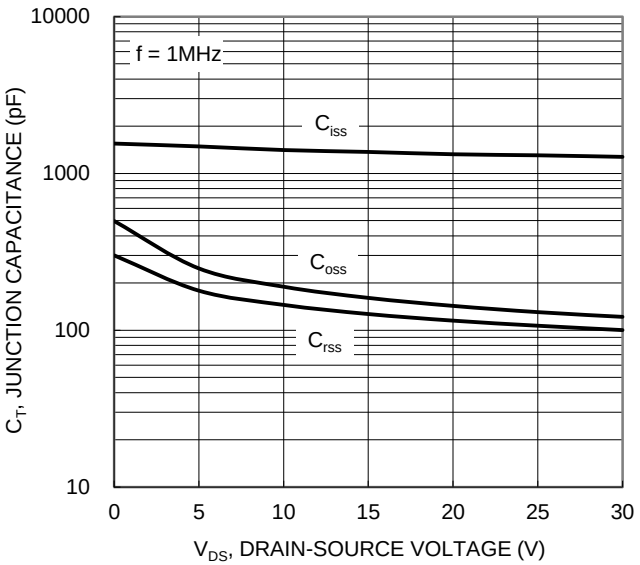


Figure 10. Typical Junction Capacitance

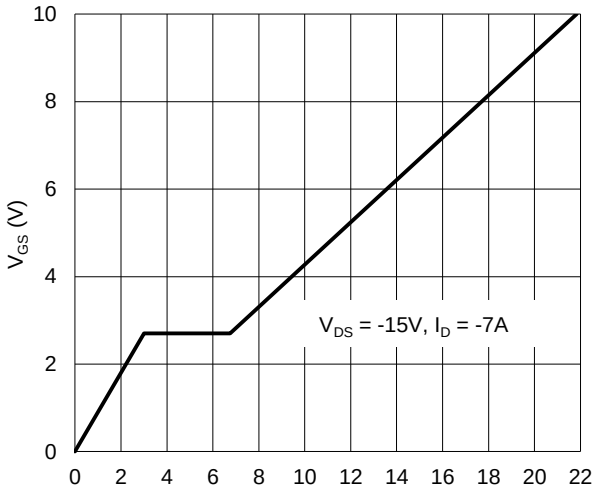


Figure 11. Gate Charge

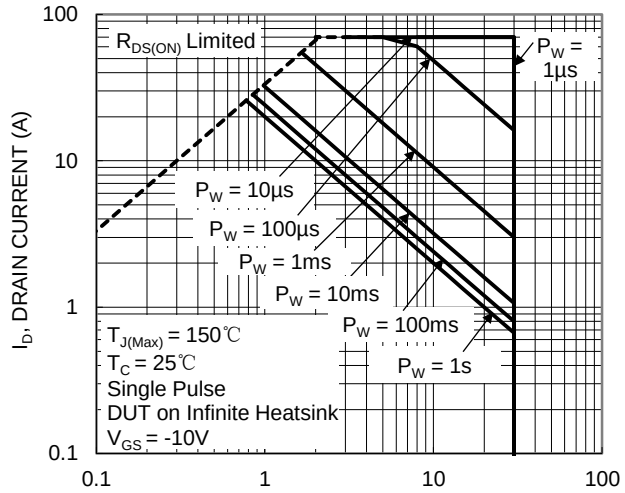


Figure 12. SOA, Safe Operation Area

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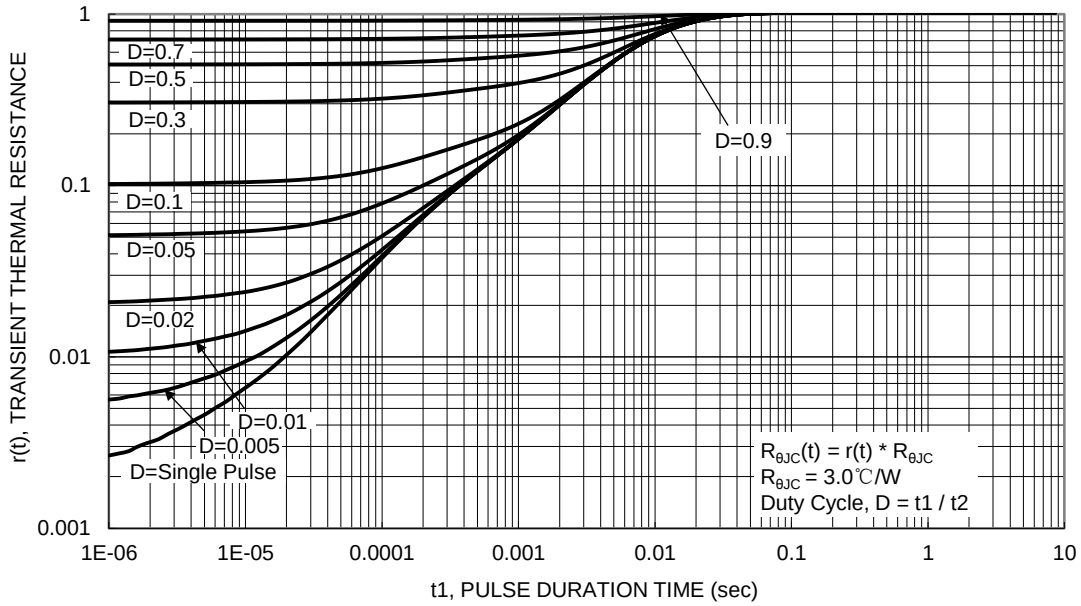


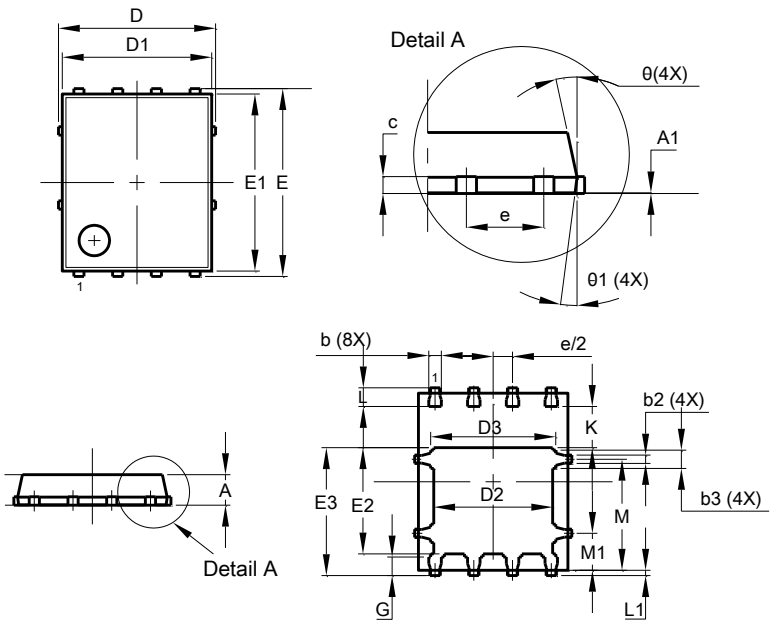
Figure 13. Transient Thermal Resistance

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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8

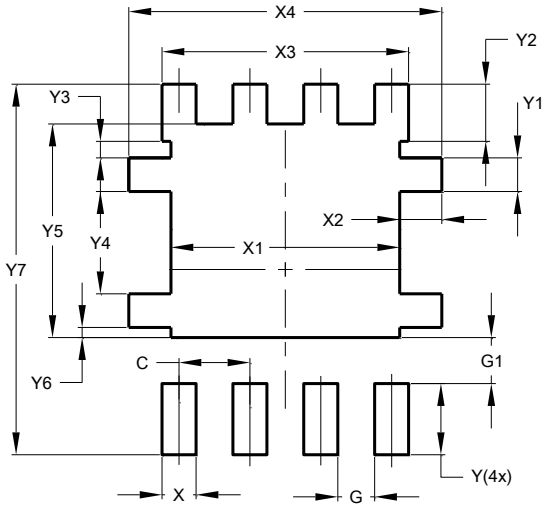


PowerDI5060-8			
Dim	**Min**	**Max**	**Typ**
A	0.90	1.10	1.00
A1	0.00	0.05	–
b	0.33	0.51	0.41
b2	0.200	0.350	0.273
b3	0.40	0.80	0.60
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.70	4.10	3.90
D3	3.90	4.30	4.10
E	6.15 BSC		
E1	5.60	6.00	5.80
E2	3.28	3.68	3.48
E3	3.99	4.39	4.19
e	1.27 BSC		
G	0.51	0.71	0.61
K	0.51	–	–
L	0.51	0.71	0.61
L1	0.100	0.200	0.175
M	3.235	4.035	3.635
M1	1.00	1.40	1.21
Θ	10°	12°	11°
Θ1	6°	8°	7°
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610

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