P-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at https://www.diodes.com/products/automotive/automotive-products/.**
- **This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. https://www.diodes.com/quality/product-definitions/**

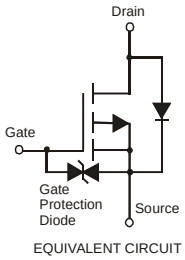
Mechanical Data

- Package: SC59
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.014 grams (Approximate)

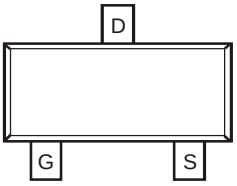

ESD Protected



SC59



EQUIVALENT CIRCUIT



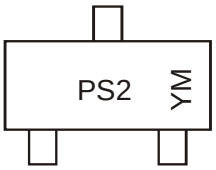
Internal Schematic

Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMP3030SN-7	SC59	3000	Tape & Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See http://www.diodes.com/quality/lead_free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



PS2 = Product Type Marking Code
YM = Date Code Marking
Y or $\overline{Y}$= Year (ex: I = 2021)
M = Month (ex: 9 = September)

Date Code Key

Year	2006	….	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	T	….	I	J	K	L	M	N	O	P	R	S

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	-30	V
Gate-Source Voltage	V_{GSS}	±20	V
Drain Current (Note 5) Steady State	I_D	-0.7	A
Pulsed Drain Current (Note 6)	I_{DM}	-2.8	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_D	500	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	250	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-65 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	-30	—	—	V	V_{GS} = 0V, I_D = -250µA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	-10	µA	V_{DS} = -30V, V_{GS} = 0V
Gate-Body Leakage	I_{GSS}	—	—	±10	µA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	-1.0	—	-3.0	V	V_{DS} = -10V, I_D = -1.0mA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	0.20 0.35	0.25 0.45	Ω	V_{GS} = -10V, I_D = -0.4A V_{GS} = -4.5V, I_D = -0.4A
Forward Transfer Admittance	$\|Y_{FS}\|$	—	1	—	S	V_{DS} = -10V, I_D = -0.4A
Diode Forward Voltage (Note 7)	V_{SD}	—	-0.8	-1.1	V	V_{GS} = 0V, I_S = -0.7A
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{iss}	—	160	—	pF	V_{DS} = -10V, V_{GS} = 0V f = 1.0MHz
Output Capacitance	C_{oss}	—	120	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	50	—	pF	
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	$t_{D(ON)}$	—	10	—	ns	V_{DD} = -10V, I_D = -0.4A, V_{GS} = -5.0V, R_{GEN} = 50Ω
Turn-Off Delay Time	$t_{D(OFF)}$	—	25	—	ns	
Turn-On Rise Time	t_R	—	25	—	ns	
Turn-Off Fall Time	t_F	—	40	—	ns	

Notes: 5. Device mounted on FR-4 PCB.
6. Pulse width ≤10µS, Duty Cycle ≤1%.
7. Short duration pulse test used to minimize self-heating effect.

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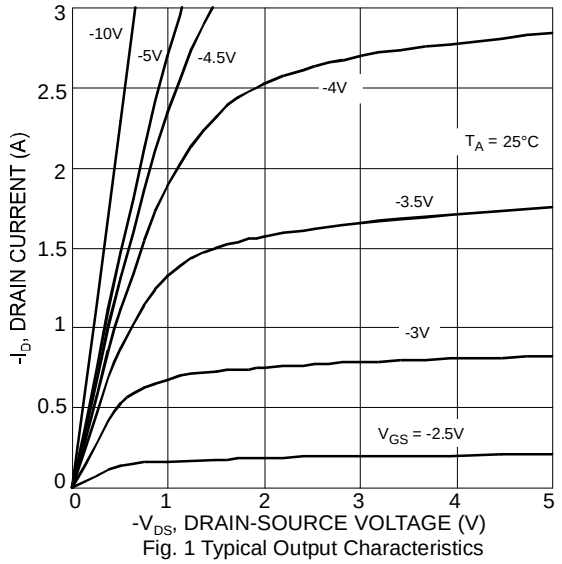


Fig. 1 Typical Output Characteristics

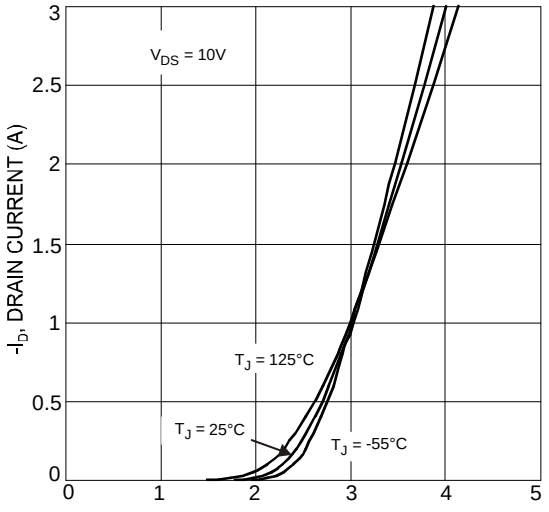


Fig. 2 Typical Transfer Characteristics

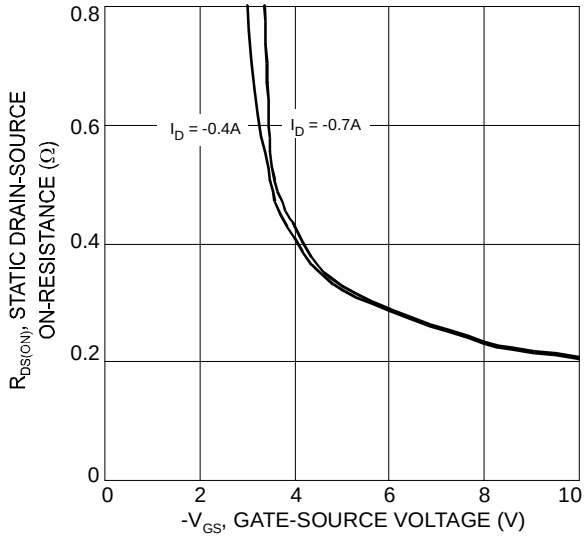


Fig. 3 On-Resistance vs. Gate Voltage

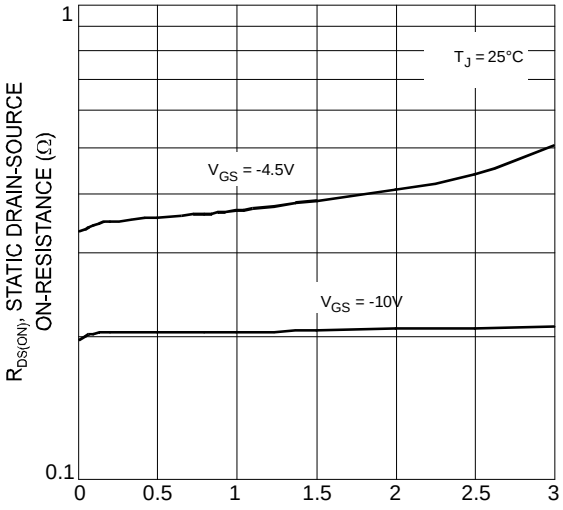


Fig. 4 On-Resistance vs. Drain Current

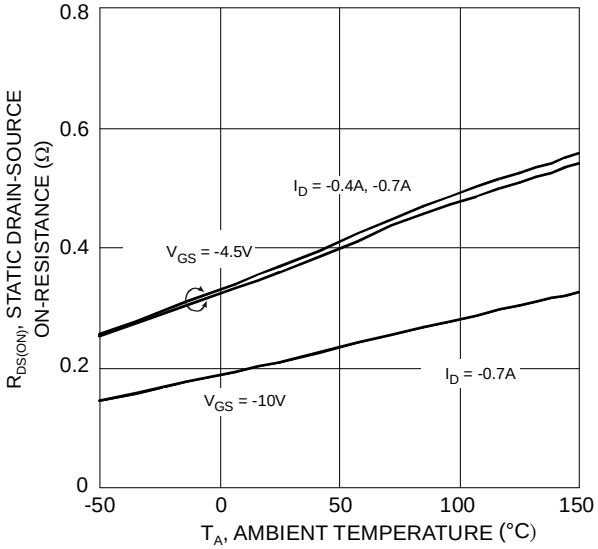


Fig. 5 On-Resistance Variation with Temperature

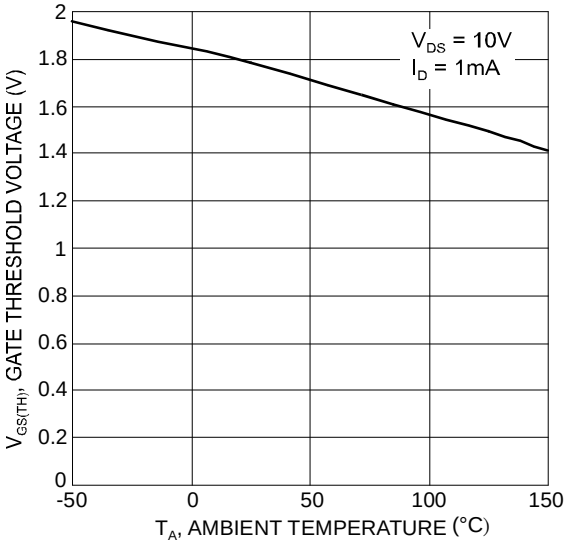


Fig. 6 Gate-Source Threshold Voltage with Temperature

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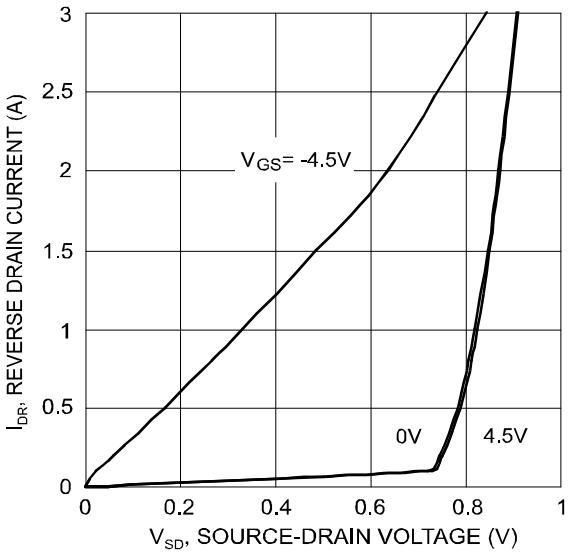


Fig. 7 Reverse Drain Current vs. Source-Drain Voltage

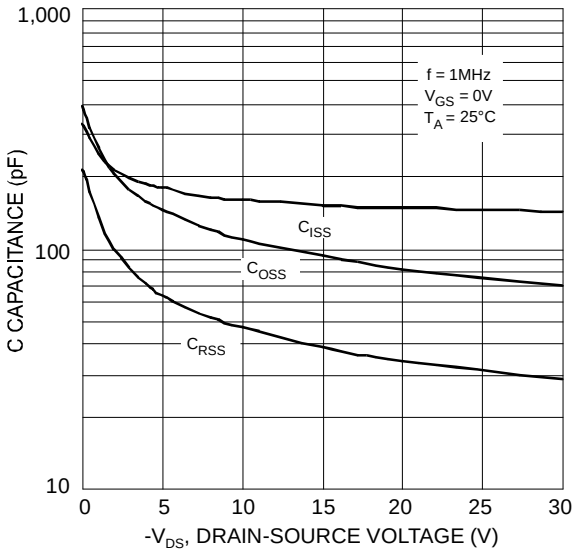


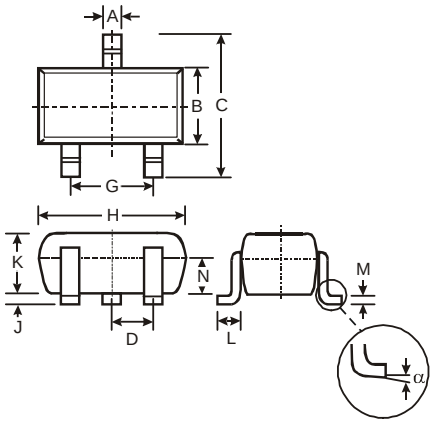
Fig. 8 Typical Total Capacitance

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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SC59

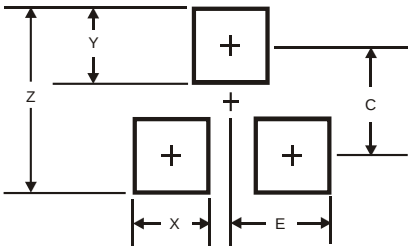


SC59			
Dim	**Min**	**Max**	**Typ**
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	-	-	0.95
G	-	-	1.90
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
N	0.70	0.80	0.75
a	0°	8°	-
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SC59



Dimensions	Value in mm
Z	3.4
X	0.8
Y	1.0
C	2.4
E	1.35

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