Product Summary

$V_{(BR)DSS}$	$R_{DS(on)\ max}$	I_D T_C = +25°C
-30V	20mΩ @ V_{GS} = -10V	-19.5A
	29mΩ @ V_{GS} = -5V	-16.2A

Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

Description

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(on)}$) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

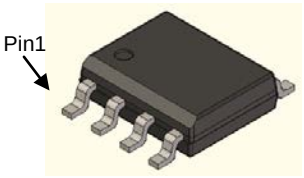
Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Weight: 0.076 grams (Approximate)

Applications

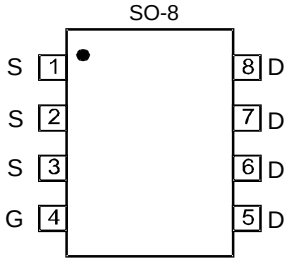
- DC-DC Converters
- Power Management Functions
- Backlighting

SO-8

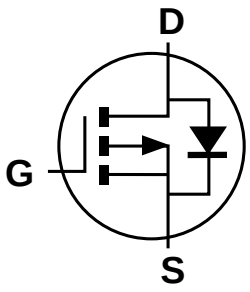


Top View

SO-8



Top View
Pin Configuration



Equivalent Circuit

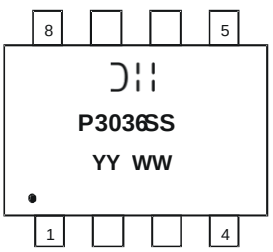
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP3036SSS-13	SO-8	2500 / Tape & Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

Marking Information

SO-8



= Manufacturer's Marking
P3036SS = Product Type Marking Code
YYWW = Date Code Marking
YY or $\overline{YY}$ = Year (ex: 19 = 2019)
WW = Week (01 - 53)

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	-30	V
Gate-Source Voltage		V_{GSS}	±25	V
Continuous Drain Current (Note 5) V_{GS} = -10V	T_C = +25°C T_C = +70°C	I_D	-19.5 -15.6	A
	T_A = +25°C T_A = +70°C	I_D	-11.4 -9.2	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I_{DM}	-80	A
Maximum Continuous Body Diode Forward Current (Note 6)		I_S	-3.6	A
Avalanche Current (Note 7) L = 0.3mH		I_{AS}	-17.5	A
Avalanche Energy (Note 7) L = 0.3mH		E_{AS}	64	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	T_A = +25°C	P_D	1.4	W
	T_A = +70°C		0.9	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\Theta JA}$	88	°C/W
	t<10s		37	
Total Power Dissipation (Note 6)	T_A = +25°C	P_D	1.9	W
	T_A = +70°C		1.2	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\Theta JA}$	65	°C/W
	t<10s		32	
Thermal Resistance, Junction to Case (Note 6)		$R_{\Theta JC}$	11	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV_{DSS}	-30	—	—	V	V_{GS} = 0V, I_D = -1mA
Zero Gate Voltage Drain Current T_J = +25°C	I_{DSS}	—	—	-1.0	µA	V_{DS} = -30V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±100	nA	V_{GS} = ±25V, V_{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(th)}$	-1.0	-1.7	-3.0	V	V_{DS} = V_{GS}, I_D = -250µA
Static Drain-Source On-Resistance	$R_{DS\ (ON)}$	—	16	20	mΩ	V_{GS} = -10V, I_D = -9A
		—	22	29		V_{GS} = -5V, I_D = -7A
Diode Forward Voltage	V_{SD}	—	-0.7	-1.0	V	V_{GS} = 0V, I_S = -1A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C_{iss}	—	1931	—	pF	V_{DS} = -15V, V_{GS} = 0V, f = 1.0MHz
Output Capacitance	C_{oss}	—	226	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	168	—	pF	
Gate Resistance	R_g	—	10.9	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge at (V_{GS} = -5V)	Q_g	—	8.8	—	nC	V_{DS} = -15V, I_D = -10A
Total Gate Charge at (V_{GS} = -10V)	Q_g	—	16.5	—	nC	V_{DS} = -15V, I_D = -10A
Gate-Source Charge	Q_{gs}	—	2.6	—	nC	
Gate-Drain Charge	Q_{gd}	—	3.6	—	nC	
Turn-On Delay Time	$t_{D(on)}$	—	8.2	—	ns	V_{GEN} = -10V, V_{DD} = -15V, R_{GEN} = 3Ω, I_D = -10A
Turn-On Rise Time	t_r	—	14	—	ns	
Turn-Off Delay Time	$t_{D(off)}$	—	65	—	ns	
Turn-Off Fall Time	t_f	—	31.6	—	ns	

Notes:
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.
7. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep T_J = +25°C.
8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.

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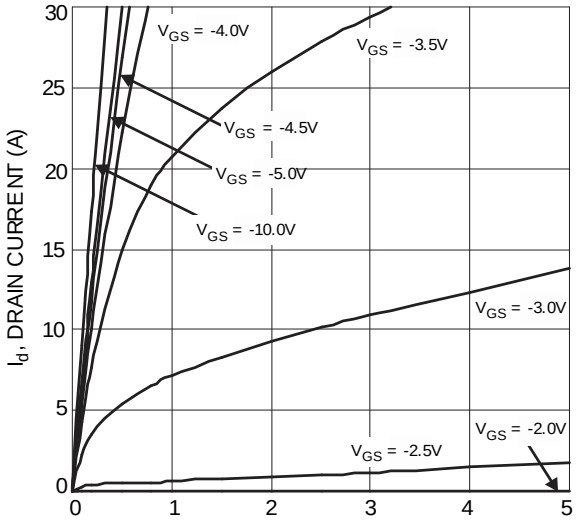


Figure 1 Typical Output Characteristic

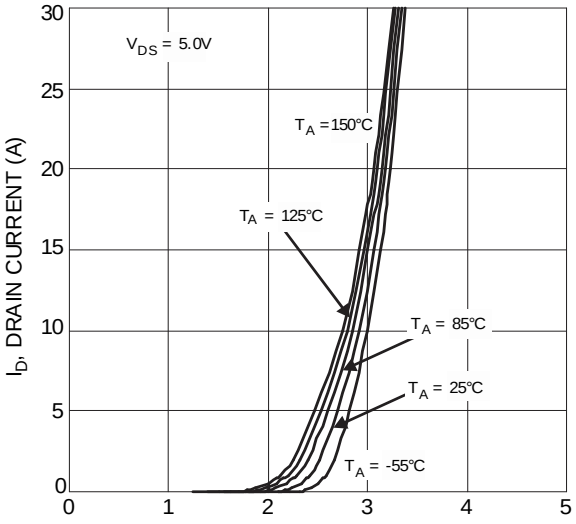


Figure 2 Typical Transfer Characteristics

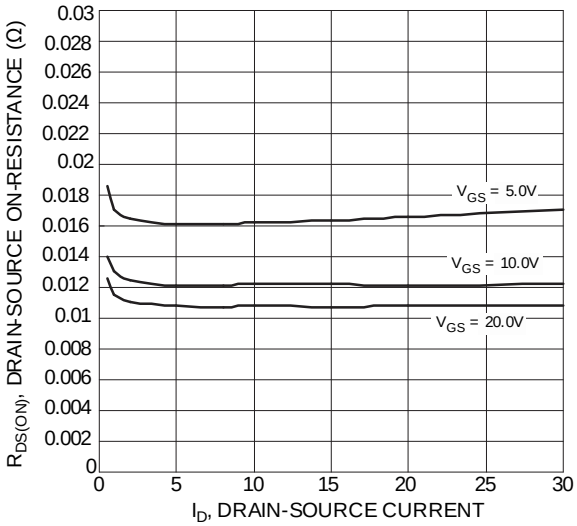


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

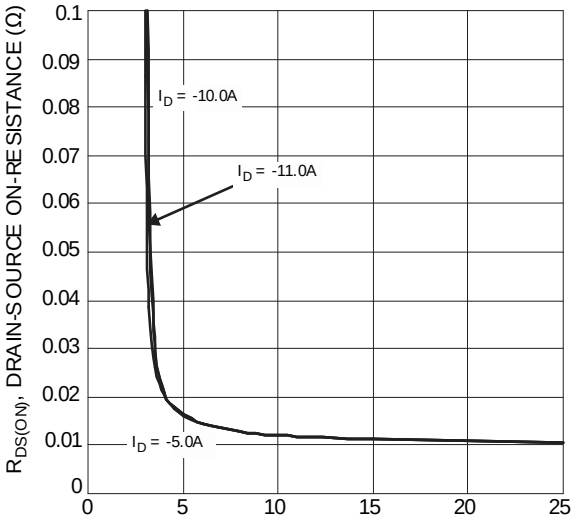


Figure 4 Typical Transfer Characteristics

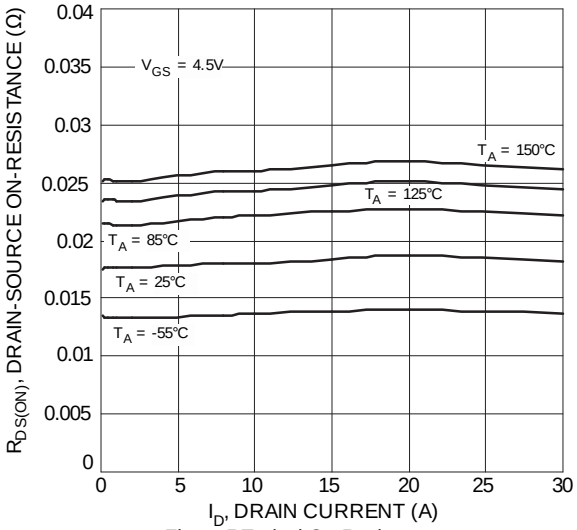


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

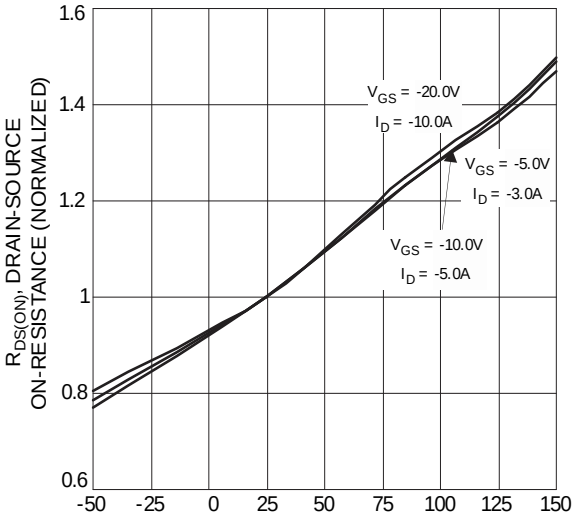


Figure 6 On-Resistance Variation with Temperature

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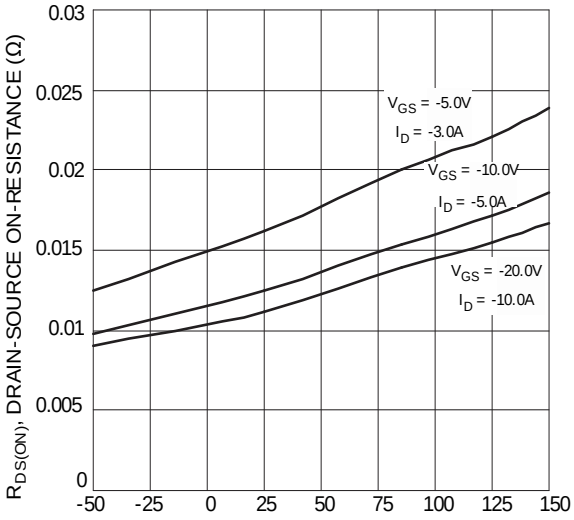


Figure 7 On-Resistance Variation with Temperature

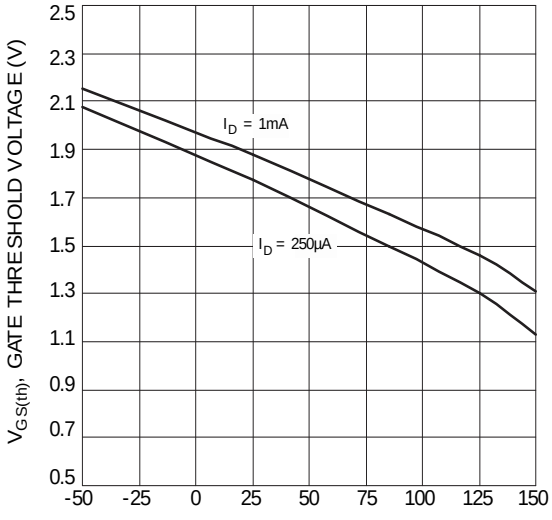


Figure 8 Gate Threshold Variation vs. Ambient Temperature

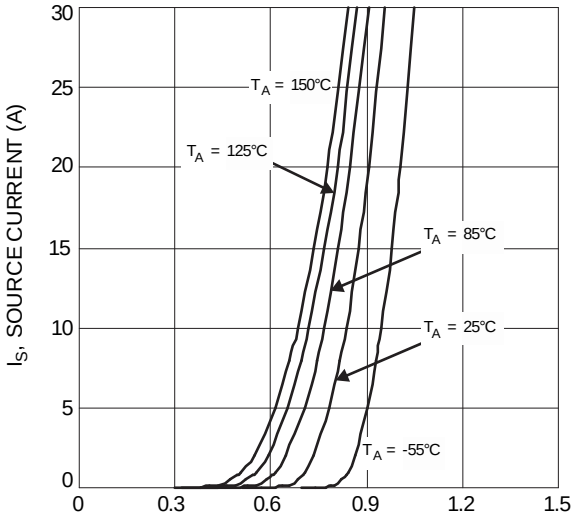


Figure 9 Diode Forward Voltage vs. Current

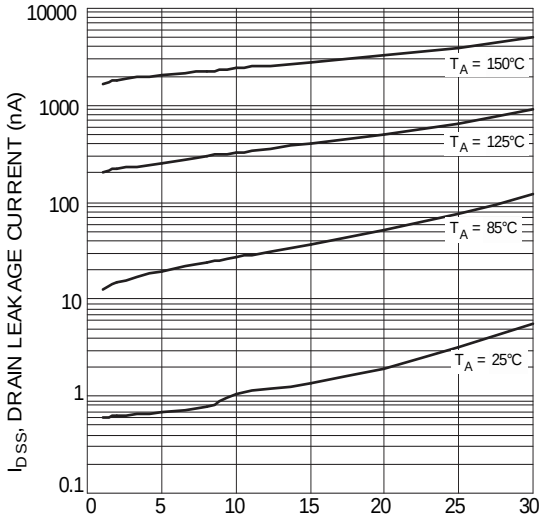


Figure 10 Typical Drain-Source Leakage Current vs. Voltage

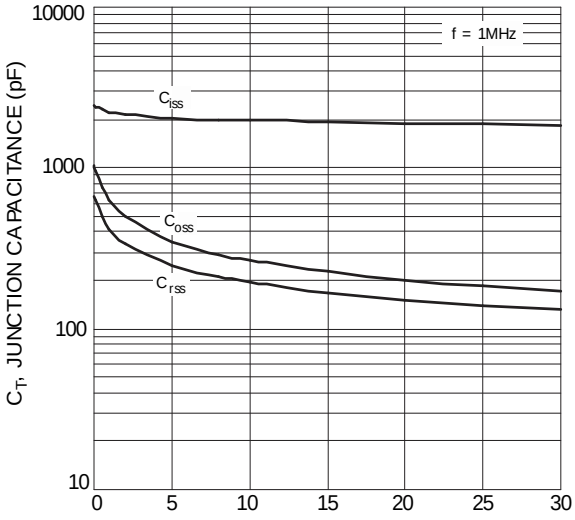


Figure 11 Typical Junction Capacitance

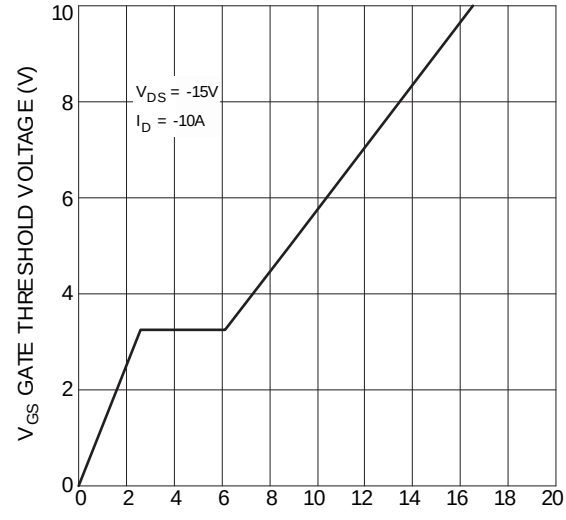


Figure 12 Gate Charge

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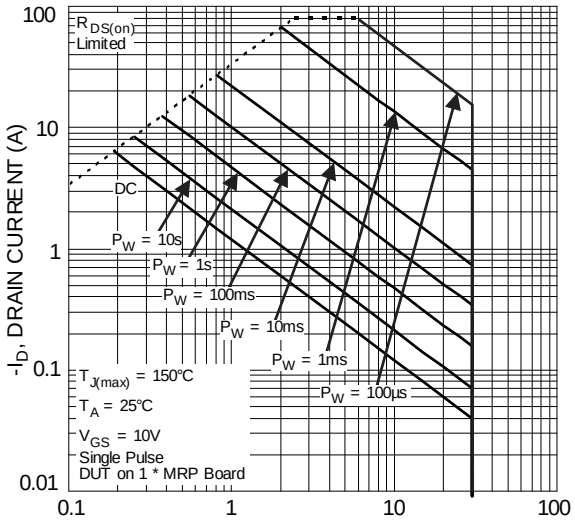


Figure 13 SOA, Safe Operation Area

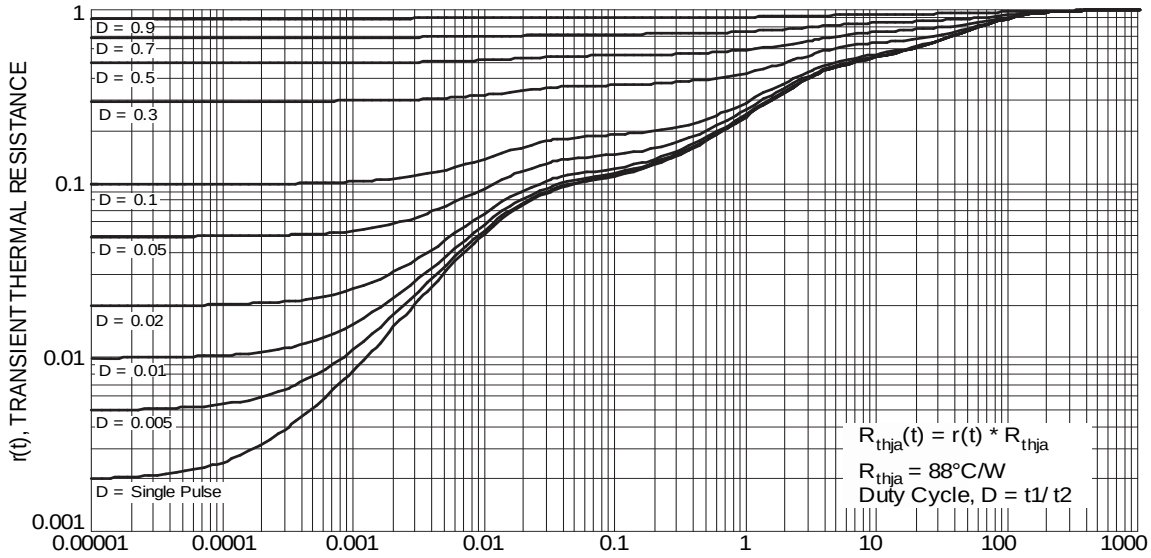


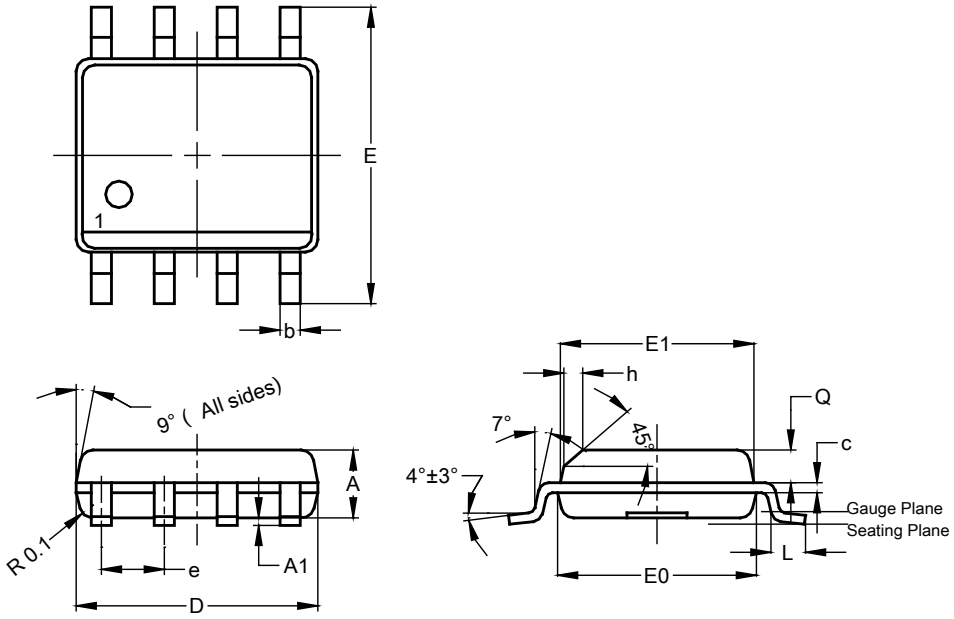
Figure 14 Transient Thermal Resistance

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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8

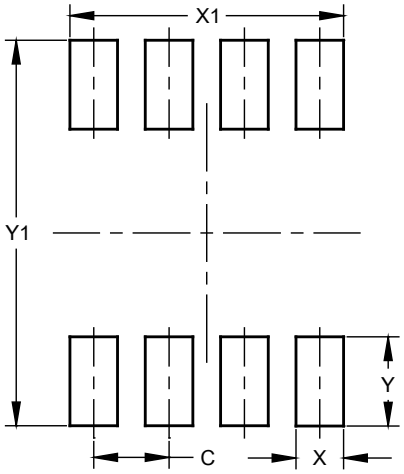


SO-8			
Dim	**Min**	**Max**	**Typ**
A	1.40	1.50	1.45
A1	0.10	0.20	0.15
b	0.30	0.50	0.40
c	0.15	0.25	0.20
D	4.85	4.95	4.90
E	5.90	6.10	6.00
E1	3.80	3.90	3.85
E0	3.85	3.95	3.90
e	--	--	1.27
h	-	--	0.35
L	0.62	0.82	0.72
Q	0.60	0.70	0.65
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8



Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50

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