30V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV_{DSS}	$R_{DS(ON)}$ Max	I_D Max @ T_A = +25°C
-30V	2.4Ω @ V_{GS} = -10V	-250mA
	4Ω @ V_{GS} = -4.5V	-200mA

Description

This MOSFET has been designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Load switches
- Portable applications
- Power management functions

Features

- Low On-Resistance
- ESD Protected Gate
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at https://www.diodes.com/products/automotive/automotive-products/.**
- **This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. https://www.diodes.com/quality/product-definitions/**

Mechanical Data

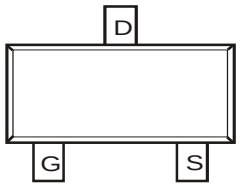
- Package: SOT323
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Lead Free Plating (Matte Tin Finish Annealed over Alloy 42 Leadframe). (e3)
- Weight: 0.006 grams (Approximate)



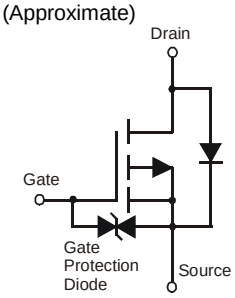
SOT323



Top View



Top View
Pin-out



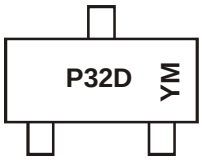
Equivalent Circuit

Ordering Information (Note 4)

Part Number	Package	Marking	Reel Size (inches)	Packing	
				Qty.	Carrier
DMP32D4SW-7	SOT323	P32D	7	3,000	Reel
DMP32D4SW-13	SOT323	P32D	13	10,000	Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



P32D = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: J = 2022)
M = Month (ex: 9 = September)

Date Code Key

Year	2012	…	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	Z	…	J	K	L	M	N	O	P	R	S	T

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-30	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (Note 6)	V_{GS} = -10V	T_A = +25°C T_A = +70°C	I_D	-250 -200	mA
Pulsed Drain Current (Note 6)			I_{DM}	-1	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation	(Note 5)	P_D	300	mW
	(Note 6)		432	
Thermal Resistance, Junction to Ambient	(Note 5)	$R_{\theta JA}$	398	°C/W
	(Note 6)		290	
Thermal Resistance, Junction to Case	(Note 5)	$R_{\theta JC}$	142	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	-30	—	—	V	V_{GS} = 0V, I_D = -1mA
Zero Gate Voltage Drain Current T_J = +25°C	I_{DSS}	—	—	-1	µA	V_{DS} = -30V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±10	µA	V_{GS} = ±16V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	-1.4	—	-2.4	V	V_{DS} = V_{GS}, I_D = -250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	—	2.4	Ω	V_{GS} = -10V, I_D = -0.5A
				4		V_{GS} = -4.5V, I_D = -0.3A
Forward Transfer Admittance	$\|Y_{fs}\|$	—	6	—	S	V_{DS} = -10V, I_D = -400mA
Diode Forward Voltage	V_{SD}	—	-0.8	-1.2	V	V_{GS} = 0V, I_S = -300mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	51.16	—	pF	V_{DS} = -15V, V_{GS} = 0V, f = 1.0MHz
Output Capacitance	C_{oss}	—	10.85	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	8.88	—	pF	
Gate Resistance	R_g	—	275	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge	Q_g	—	0.6	—	nC	V_{GS} = -4.5V; V_{DS} = -10V, I_D = -1A
Total Gate Charge	Q_g	—	1.2	—	nC	V_{GS} = -10V; V_{DS} = -10V, I_D = -1A
Gate-Source Charge	Q_{gs}	—	0.2	—	nC	V_{GS} = -10V; V_{DS} = -10V, I_D = -1A
Gate-Drain Charge	Q_{gd}	—	0.3	—	nC	V_{GS} = -10V; V_{DS} = -10V, I_D = -1A
Turn-On Delay Time	$t_{D(on)}$	—	9.86	—	ns	V_{DS} = -15V, I_D = -1A, V_{GS} = -10V, R_G = 6Ω
Turn-On Rise Time	t_r	—	11.5	—	ns	
Turn-Off Delay Time	$t_{D(off)}$	—	31.8	—	ns	
Turn-Off Fall Time	t_f	—	21.9	—	ns	

Notes:
5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to production testing.

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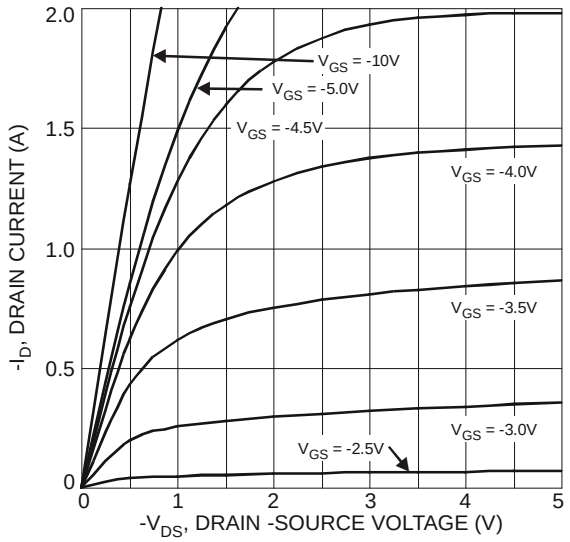


Figure 1 Typical Output Characteristics

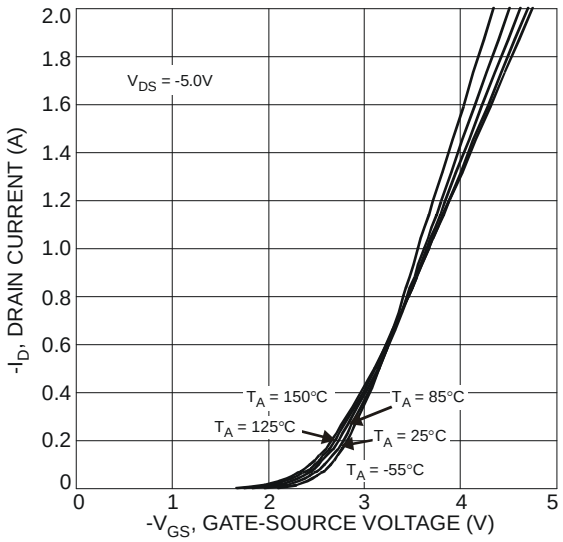


Figure 2 Typical Transfer Characteristics

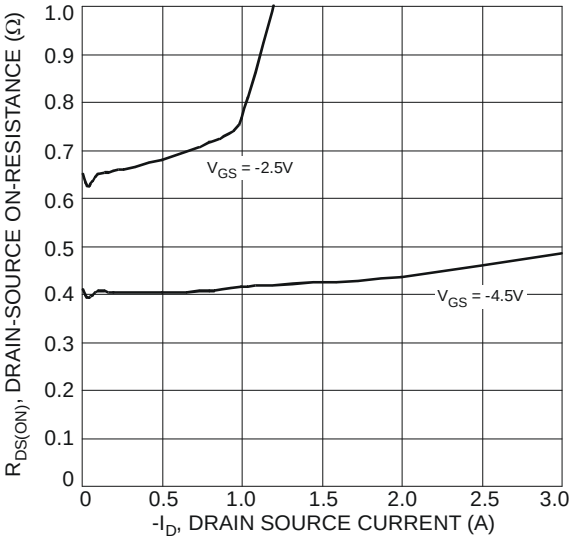


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

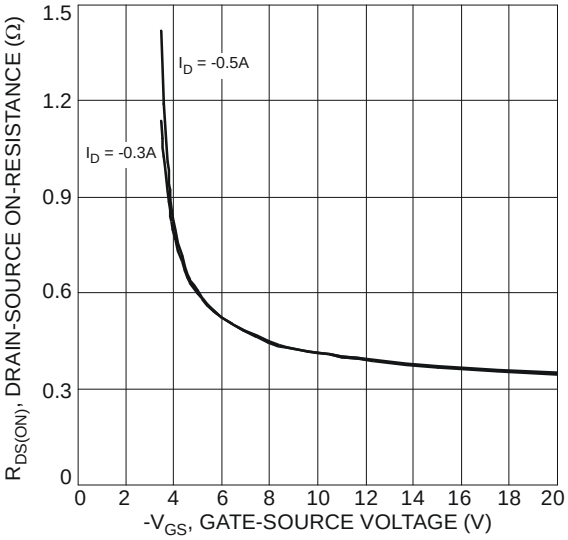


Figure 4 Typical Drain-Source On-Resistance vs. Gate-Source Voltage

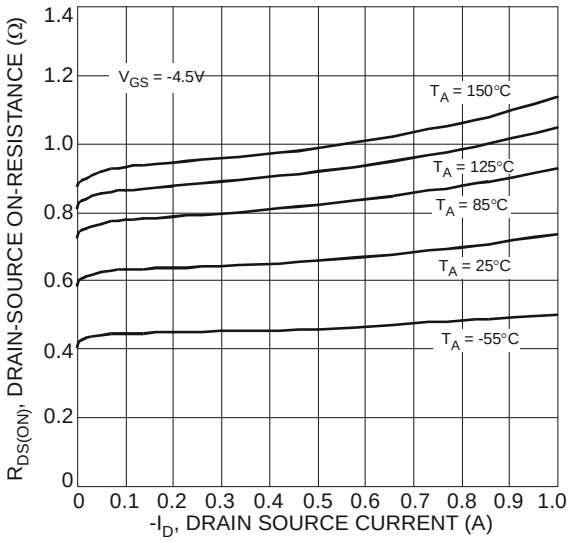


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

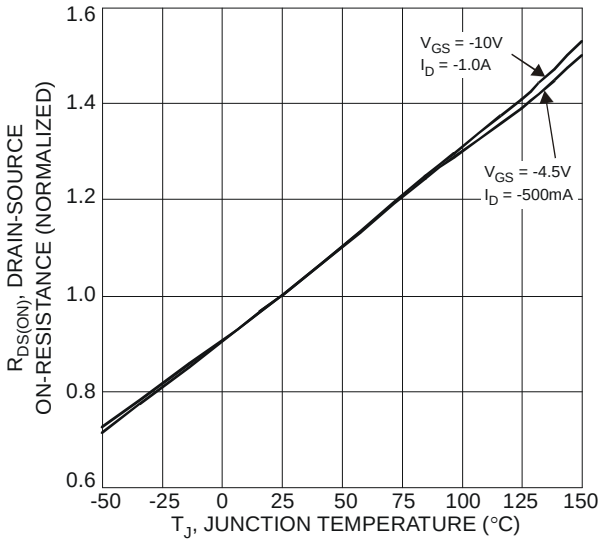


Figure 6 On-Resistance Variation with Temperature

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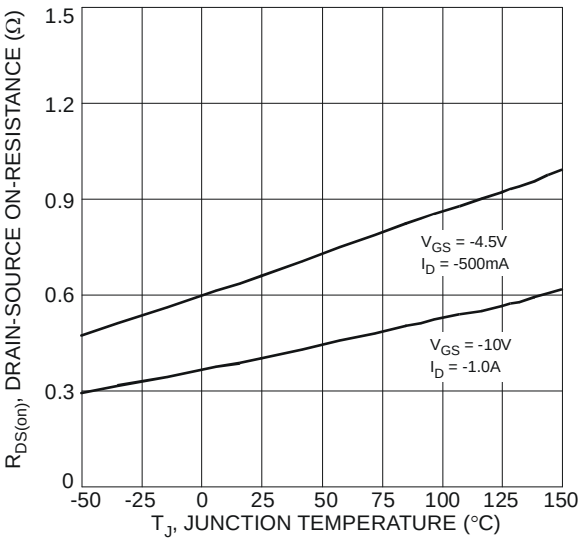


Figure 7 On-Resistance Variation with Temperature

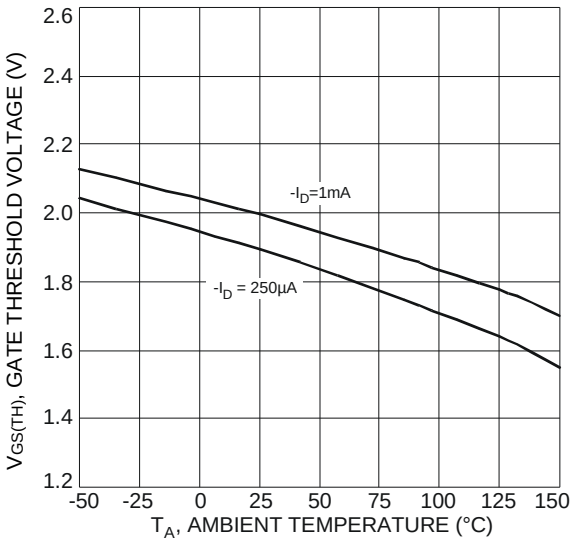


Figure 8 Gate Threshold Variation vs. Ambient Temperature

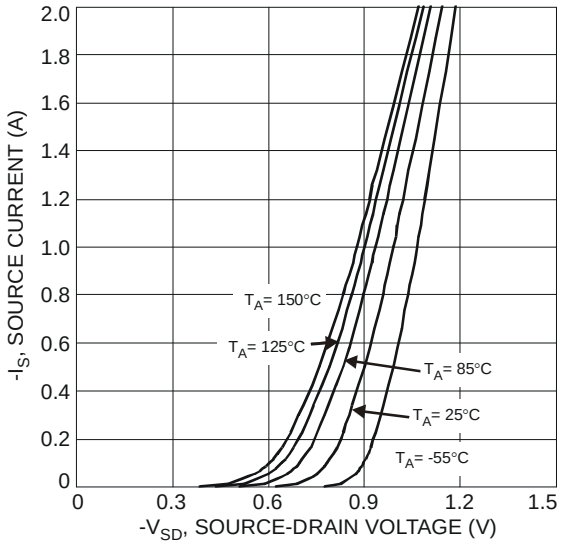


Figure 9 Diode Forward Voltage vs. Current

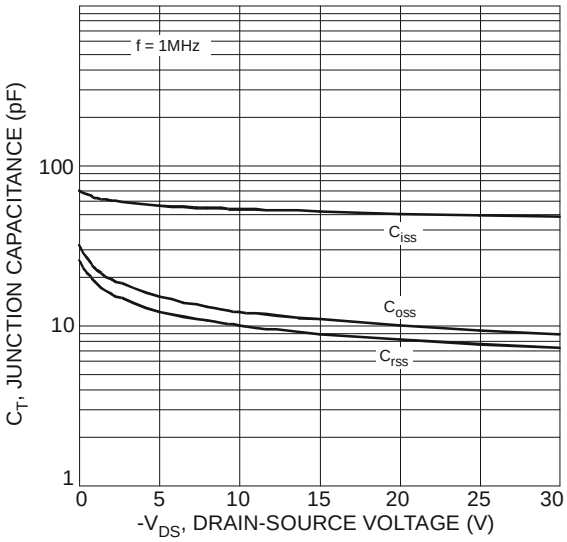


Figure 10 Typical Junction Capacitance

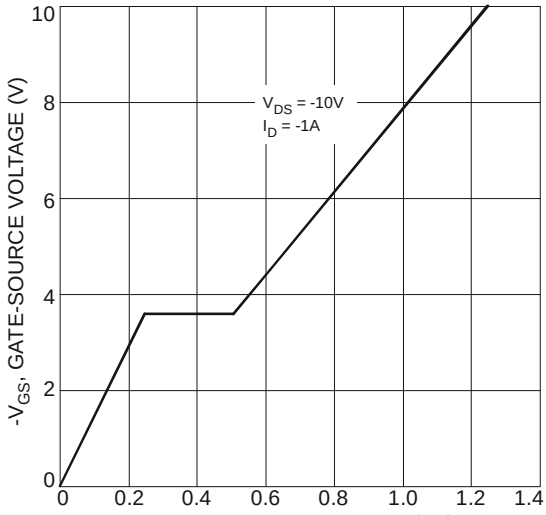


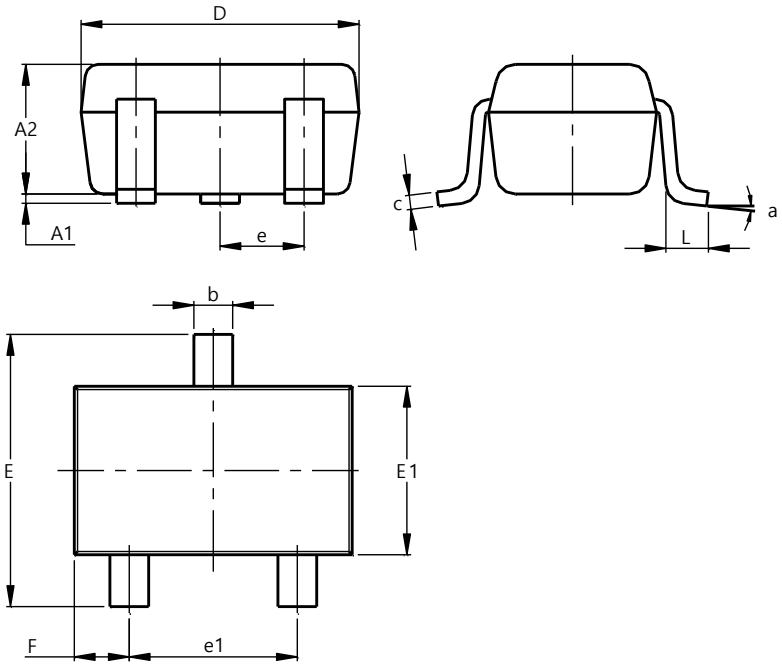
Figure 11 Gate-Charge Characteristics

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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323

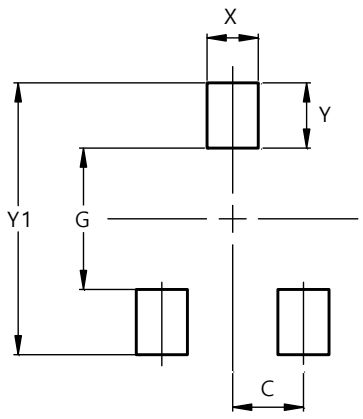


SOT323			
Dim	**Min**	**Max**	**Typ**
A1	0.00	0.10	0.05
A2	0.90	1.00	0.95
b	0.25	0.40	0.30
c	0.10	0.18	0.11
D	1.80	2.20	2.15
E	2.00	2.20	2.10
E1	1.15	1.35	1.30
e	0.650 BSC		
e1	1.20	1.40	1.30
F	0.375	0.475	0.425
L	0.25	0.40	0.30
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323



Dimensions	Value (in mm)
C	0.650
G	1.300
X	0.470
Y	0.600
Y1	2.500

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