40V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV_{DSS}	$R_{DS(ON)}$ Max	I_D Max T_A = +25°C
-40V	33mΩ @ V_{GS} = -10V	-6A
	50mΩ @ V_{GS} = -4.5V	-4.9A

Features

- 0.6mm Profile – Ideal for Low Profile Applications
- PCB Footprint of 4mm^2
- Low Gate Threshold Voltage
- Low On-Resistance
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.**
  https://www.diodes.com/quality/product-definitions/
- **An automotive-compliant part is available under separate datasheet (DMP4047LFDEQ)**

Description and Applications

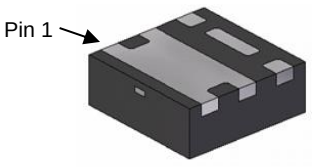
This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- General-purpose interfacing switches
- Load switching
- Battery-management applications
- Power-management functions

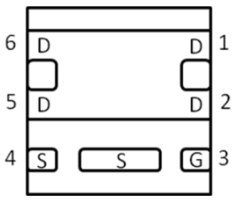
Mechanical Data

- Package: U-DFN2020-6
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 e4
- Weight: 0.0065 grams (Approximate)

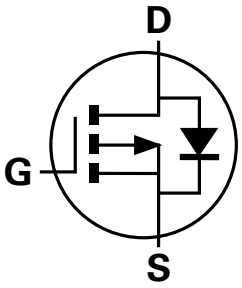
U-DFN2020-6 (Type E)



Bottom View



Pinout
Bottom View



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Package	Marking	Reel Size (inches)	Packing	
				Qty.	Carrier
DMP4047LFDE-7	U-DFN2020-6 (Type E)	PE	7	3,000	Reel
DMP4047LFDE-13	U-DFN2020-6 (Type E)	PE	13	10,000	Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

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Marking Information

Site 1:



PE = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: L = 2024)
M = Month (ex: 9 = September)

Date Code Key

Year	2012	-	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	Z	-	L	M	N	P	R	S	T	U	V	W

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Site 2 and Site 3:



PE = Product Type Marking Code
YWX = Date Code Marking
Y or Y = Year (ex: 4 = 2024)
W = Week (ex: a = week 27; z represents week 52 and 53)
X = Internal Code (ex: U = Monday)

Date Code Key

Year	2012	-	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	2	-	4	5	6	7	8	9	0	1	2	3

Week	1-26	27-52	53
Code	A-Z	a-z	z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	T	U	V	W	X	Y	Z

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-40	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (Note 5) V_{GS} = -10V	Steady State	T_A = +25°C T_A = +70°C	I_D	-3.3 -2.6	A
	t < 5s	T_A = +25°C T_A = +70°C	I_D	-5.3 -4.2	A
Continuous Drain Current (Note 6) V_{GS} = -10V	Steady State	T_A = +25°C T_A = +70°C	I_D	-6.0 -4.8	A
	t < 5s	T_A = +25°C T_A = +70°C	I_D	-9.5 -7.6	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I_{DM}	-40	A
Maximum Body Diode Continuous Current			I_S	-3	A

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T_A = +25°C	P_D	0.7	W
	T_A = +70°C		0.42	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	180	°C/W
	t < 5s		76	
Total Power Dissipation (Note 6)	T_A = +25°C	P_D	2.1	W
	T_A = +70°C		1.3	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	58	°C/W
	t < 5s		25	
Thermal Resistance, Junction to Case (Note 6)		$R_{\theta JC}$	10.2	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	-40	—	—	V	V_{GS} = 0V, I_D = -250µA
Zero Gate Voltage Drain Current T_J = +25°C	I_{DSS}	—	—	-1	µA	V_{DS} = -40V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±100	nA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	-1.0	—	-2.2	V	V_{DS} = V_{GS}, I_D = -250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	26	33	mΩ	V_{GS} = -10V, I_D = -4.4A
			36	50		V_{GS} = -4.5V, I_D = -3.7A
Forward Transfer Admittance	$\|Y_{fs}\|$	—	5.2	—	S	V_{DS} = -15V, I_D = -4.4A
Diode Forward Voltage	V_{SD}	—	-0.75	-1.2	V	V_{GS} = 0V, I_S = -3.9A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	1382	—	pF	V_{DS} = -20V, V_{GS} = 0V f = 1.0MHz
Output Capacitance	C_{oss}	—	103	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	81	—	pF	
Gate Resistance	R_g	—	7.7	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge (V_{GS} = -4.5V)	Q_g	—	11.2	—	nC	V_{DS} = -20V, I_D = -4.9A
Total Gate Charge (V_{GS} = -10V)	Q_g	—	23.2	—	nC	
Gate-Source Charge	Q_{gs}	—	3.3	—	nC	
Gate-Drain Charge	Q_{gd}	—	3.9	—	nC	
Turn-On Delay Time	$t_{D(ON)}$	—	18.4	—	ns	V_{DS} = -20V, I_D = -3.9A V_{GS} = -4.5V, R_g = 1Ω
Turn-On Rise Time	t_R	—	28.2	—	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	—	38.8	—	ns	
Turn-Off Fall Time	t_F	—	28.6	—	ns	
Reverse-Recovery Time	t_{RR}	—	15.4	—	ns	I_F = -3.9A, di/dt = 100A/µs
Reverse-Recovery Charge	Q_{RR}	—	5.4	—	nC	

Notes:
5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to production testing.

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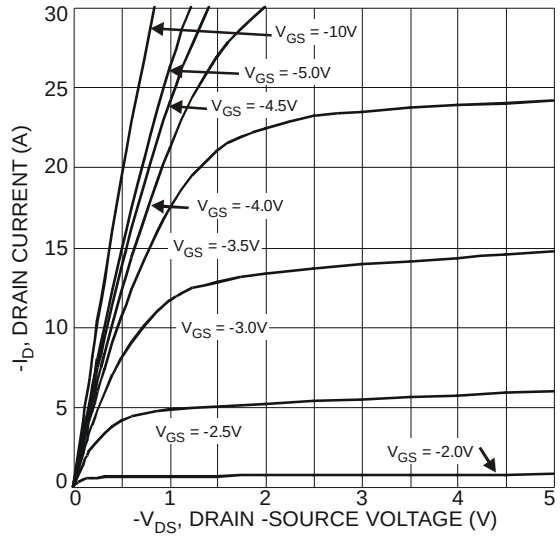


Fig. 1 Typical Output Characteristics

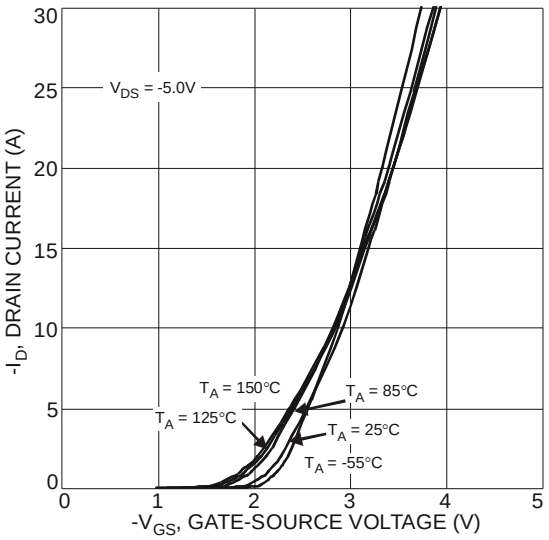


Fig. 2 Typical Transfer Characteristics

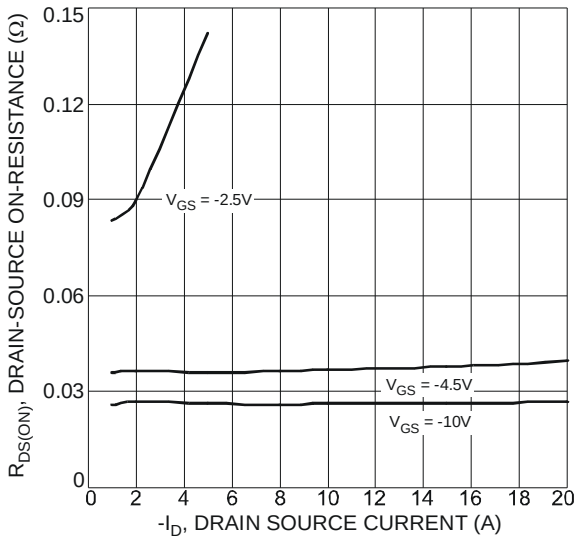


Fig. 3 Typical On-Resistance vs.
Drain Current and Gate Voltage

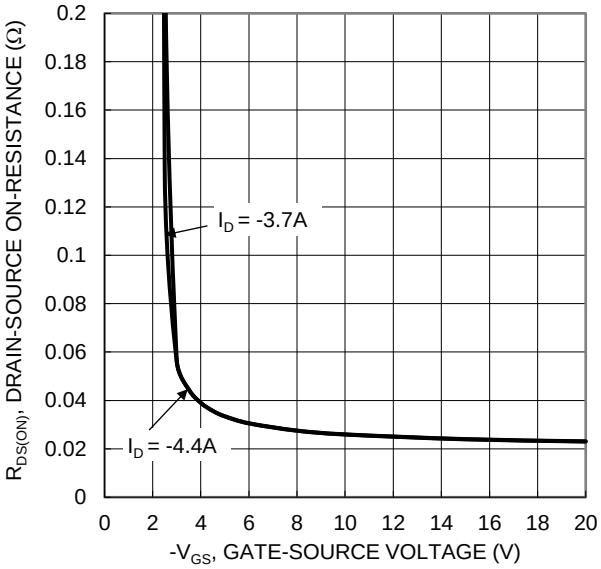


Fig. 4 Typical On-Resistance vs Gate Voltage

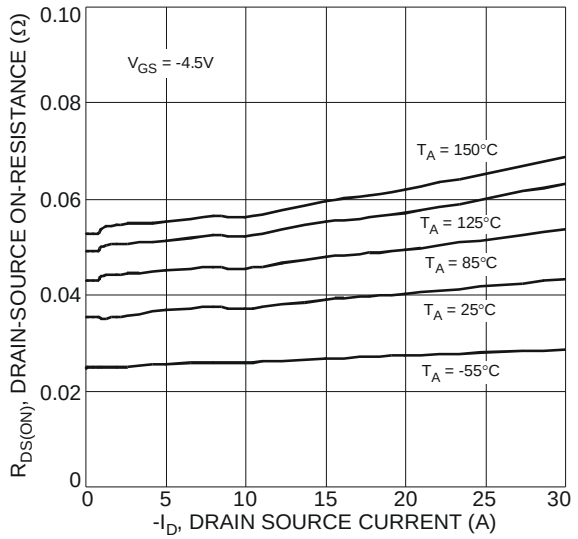


Fig. 5 Typical On-Resistance vs.
Drain Current and Temperature

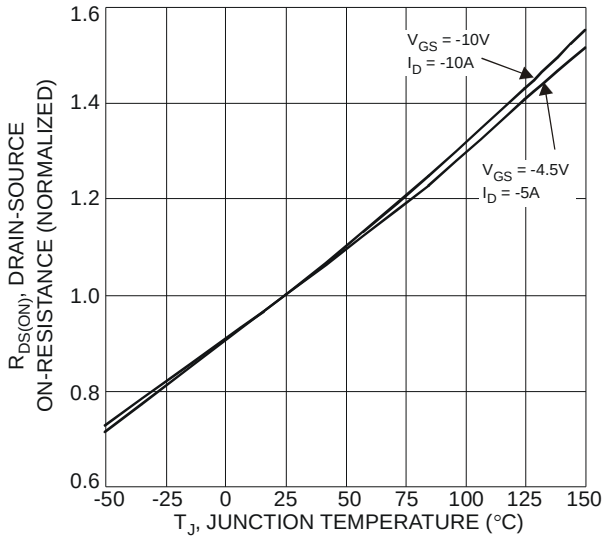


Fig. 6 On-Resistance Variation with Temperature

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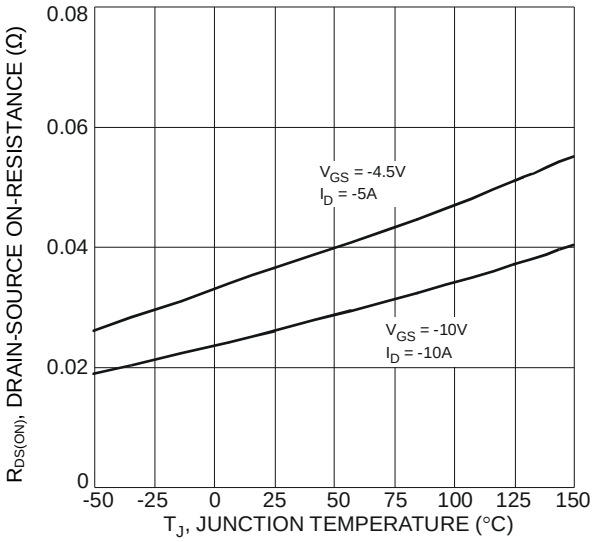


Fig. 7 On-Resistance Variation with Temperature

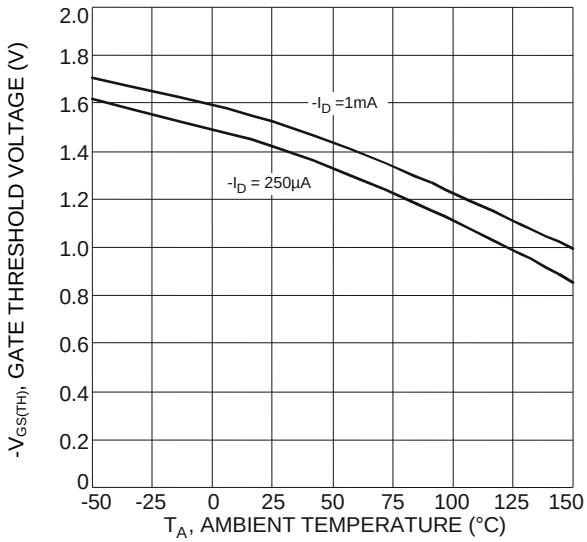


Fig. 8 Gate Threshold Variation vs. Ambient Temperature

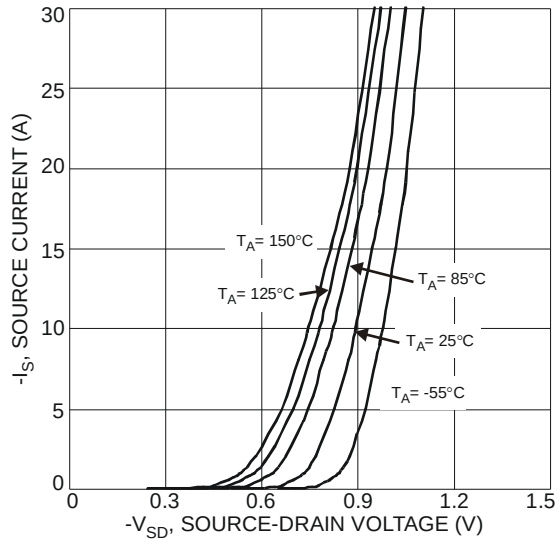


Fig. 9 Diode Forward Voltage vs. Current

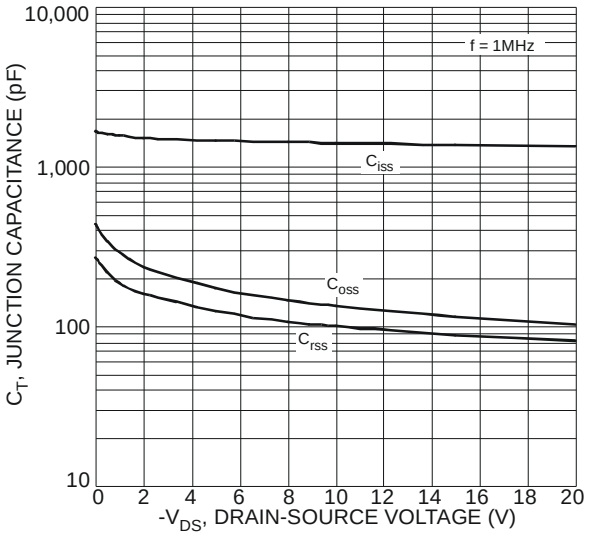


Fig. 10 Typical Junction Capacitance

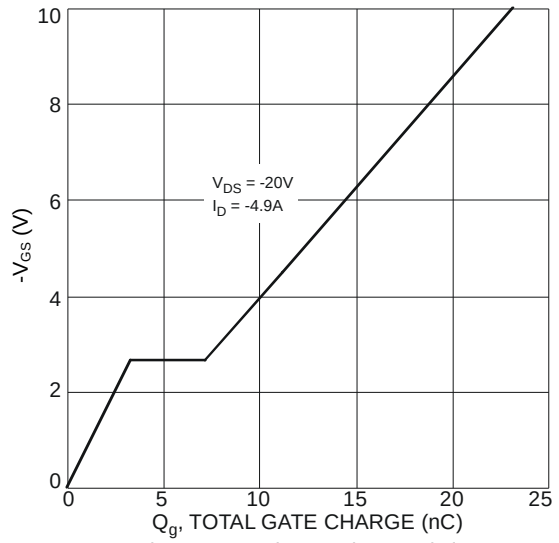


Fig. 11 Gate-Charge Characteristics

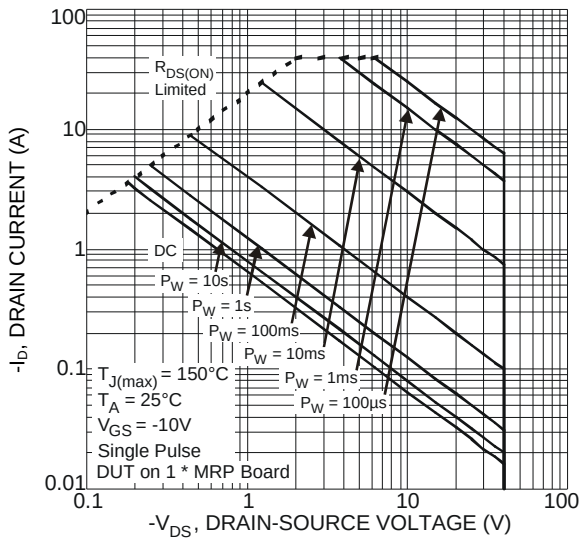


Fig. 12 SOA, Safe Operation Area

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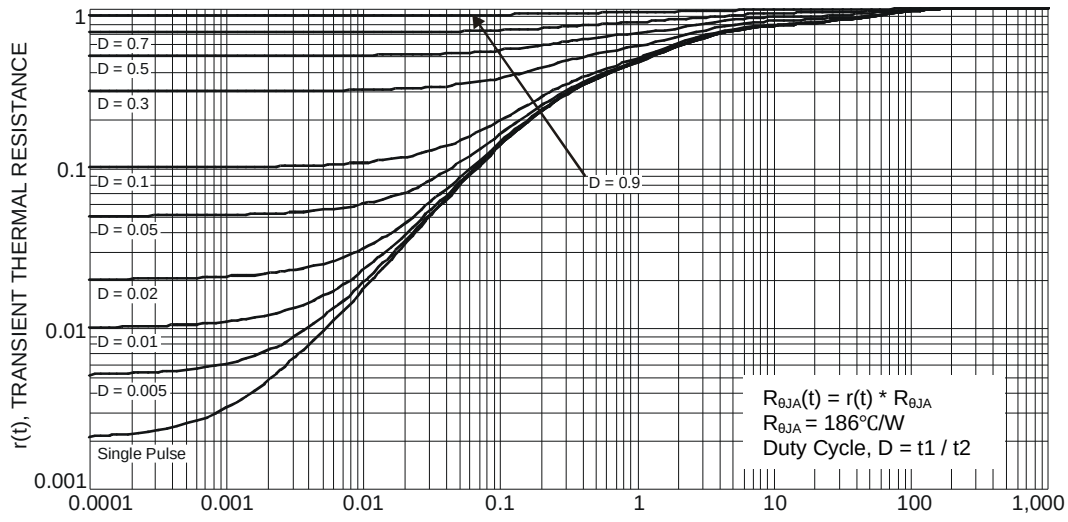


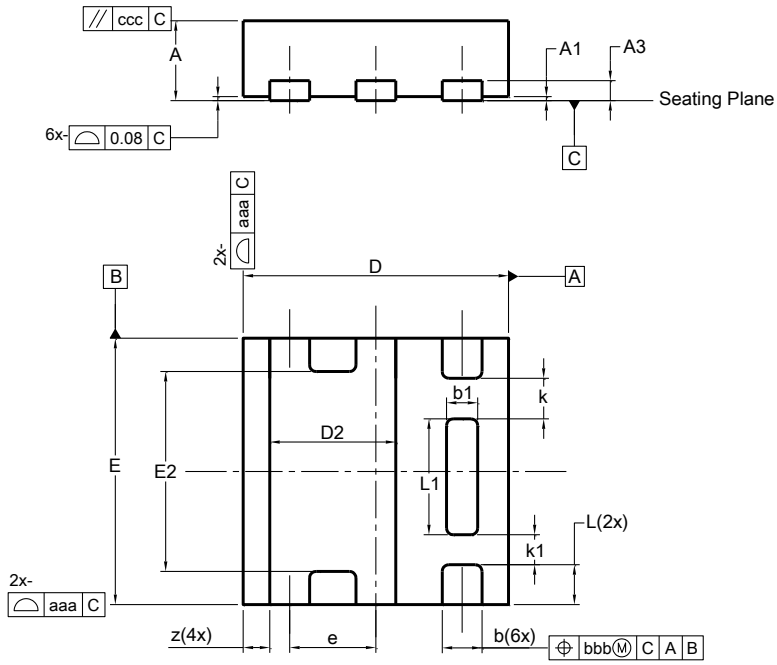
Fig. 13 Transient Thermal Resistance

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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type E)

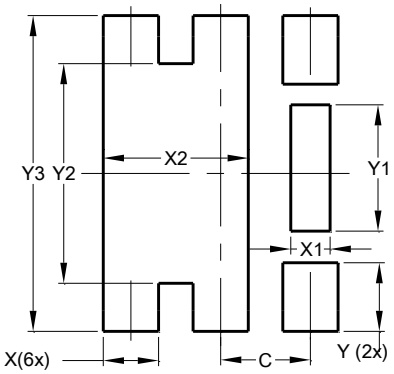


U-DFN2020-6 (Type E)			
Dim	**Min**	**Max**	**Typ**
A	0.57	0.63	0.60
A1	0.00	0.05	0.03
A3	–	–	0.15
b	0.25	0.35	0.30
b1	0.185	0.285	0.235
D	1.95	2.05	2.00
D2	0.85	1.05	0.95
E	1.95	2.05	2.00
E2	1.40	1.60	1.50
e	–	–	0.65
L	0.25	0.35	0.30
L1	0.82	0.92	0.87
k	–	–	0.305
k1	–	–	0.225
Z	–	–	0.20
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type E)



Dimensions	Value (in mm)
C	0.650
X	0.400
X1	0.285
X2	1.050
Y	0.500
Y1	0.920
Y2	1.600
Y3	2.300

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