60V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV_{DSS}	$R_{DS(ON)}$ max	I_D max T_A = +25°C
-60V	110mΩ @ V_{GS} = -10V	-4.5A
	130mΩ @ V_{GS} = -4.5V	-4.2A

Features and Benefits

- 100% Unclamped Inductive Switch (UIS) Test in Production
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/ Output Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The DMP6110SSSQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**
  https://www.diodes.com/quality/product-definitions/

Description and Applications

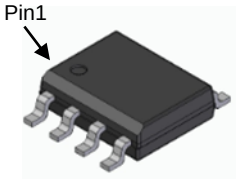
This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Backlighting
- Power-management functions
- DC-DC converters

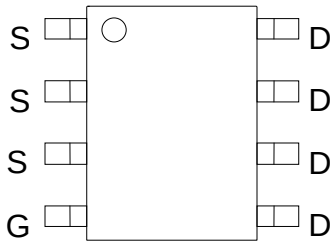
Mechanical Data

- Package: SO-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish - Matte Tin Annealed over Copper Lead Frame. Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.072g (Approximate)

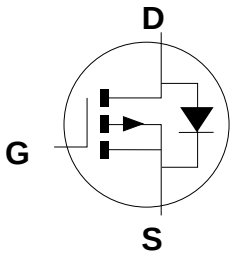
SO-8



Top View



Top View
Pin Configuration



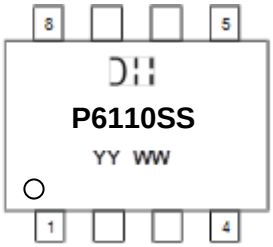
Equivalent Circuit

Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMP6110SSSQ-13	SO-8	2500	Tape & Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



= Manufacturer's Marking
P6110SS = Product Type Marking Code
YYWW = Date Code Marking
YY = Year Code (ex: 23 = 2023)
WW = Week Code (01 to 53)

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-60	V
Gate-Source Voltage			V_{GSS}	±20	V
Drain Current (Note 6) V_{GS} = -10V	t < 10s	T_A = +25°C T_A = +70°C	I_D	-4.5 -3.6	A
Maximum Body Diode Forward Current (Note 6)			I_S	-2.1	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I_{DM}	-19	A
Avalanche Current (Note 7) L = 0.1mH			I_{AS}	-17.6	A
Avalanche Energy (Note 7) L = 0.1mH			E_{AS}	15.4	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_D	1.5	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	80	°C/W
	t<10s		48	°C/W
Total Power Dissipation (Note 6)		P_D	2.0	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	61	°C/W
	t<10s		37	°C/W
Thermal Resistance, Junction to Case		$R_{\theta JC}$	6.4	°C/W
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV_{DSS}	-60	—	—	V	V_{GS} = 0V, I_D = -250µA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	-1	µA	V_{DS} = -48V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	100	nA	V_{GS} = ±16V, V_{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(TH)}$	-1	—	-3	V	V_{DS} = V_{GS}, I_D = -250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	86	110	mΩ	V_{GS} = -10V, I_D = -4.5A
		—	98	130		V_{GS} = -4.5V, I_D =-3.5A
Diode Forward Voltage	V_{SD}	—	-0.7	-1.2	V	V_{GS} = 0V, I_S = -1A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C_{iss}	—	1030	—	pF	V_{DS} = -30V, V_{GS} = 0V, f = 1.0MHz
Output Capacitance	C_{oss}	—	49.1	—		
Reverse Transfer Capacitance	C_{rss}	—	38.7	—		
Gate Resistance	R_G	—	13.6	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz
Total Gate Charge (V_{GS} = -4.5V)	Q_g	—	9.5	—	nC	V_{DS} = -30V, I_D = -5A
Total Gate Charge (V_{GS} = -10V)	Q_g	—	19.4	—		
Gate-Source Charge	Q_{gs}	—	2.3	—		
Gate-Drain Charge	Q_{gd}	—	3.6	—		
Turn-On Delay Time	$t_{D(ON)}$	—	3.7	—	ns	V_{GS} = -10V, V_{DS} = -30V, R_{GEN} = 6Ω, I_D = -5A
Turn-On Rise Time	t_R	—	6.3	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	58.7	—		
Turn-Off Fall Time	t_F	—	26.1	—		
Body Diode Reverse Recovery Time	t_{RR}	—	14.85	—	ns	I_S = -5A, dI/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q_{RR}	—	8.8	—	nC	I_S = -5A, dI/dt = 100A/µs

Notes:
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. UIS in production with L = 0.1mH, starting T_A = +25°C.
8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.

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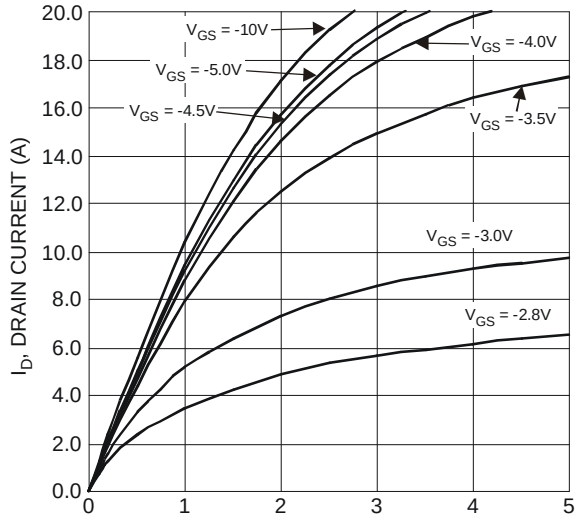


Figure 1 Typical Output Characteristics

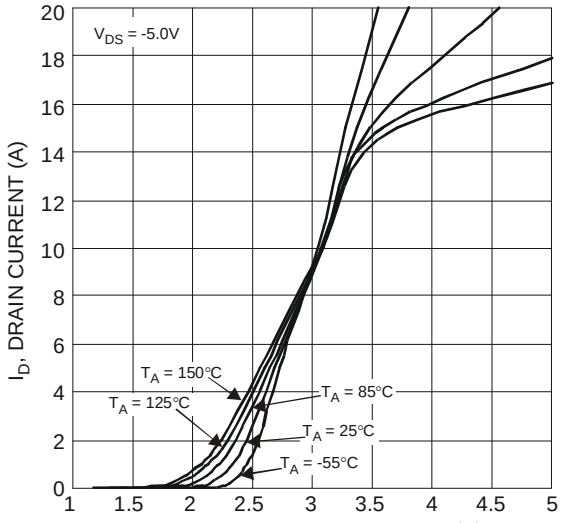


Figure 2 Typical Transfer Characteristics

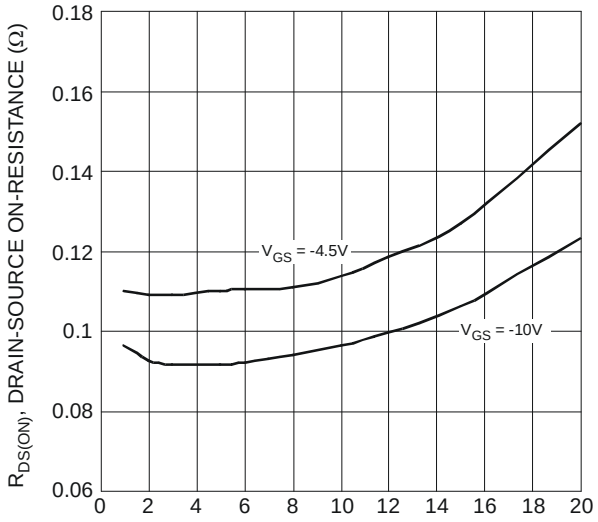


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

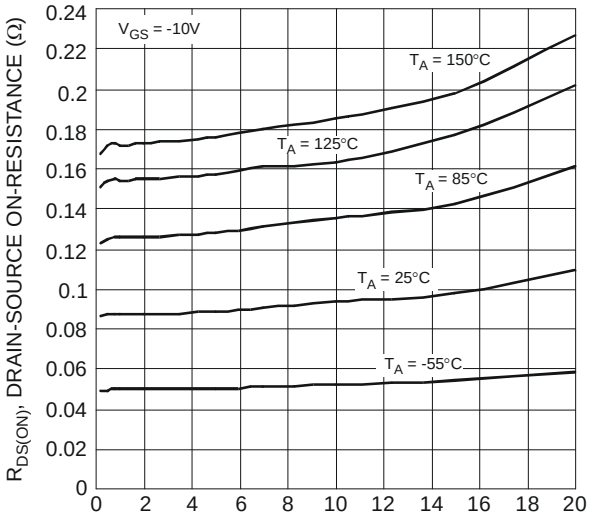


Figure 4 Typical On-Resistance vs. Drain Current and Temperature

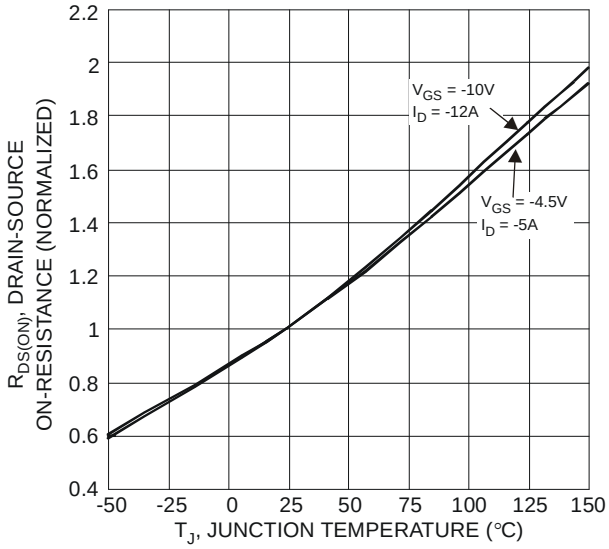


Figure 5 On-Resistance Variation with Temperature

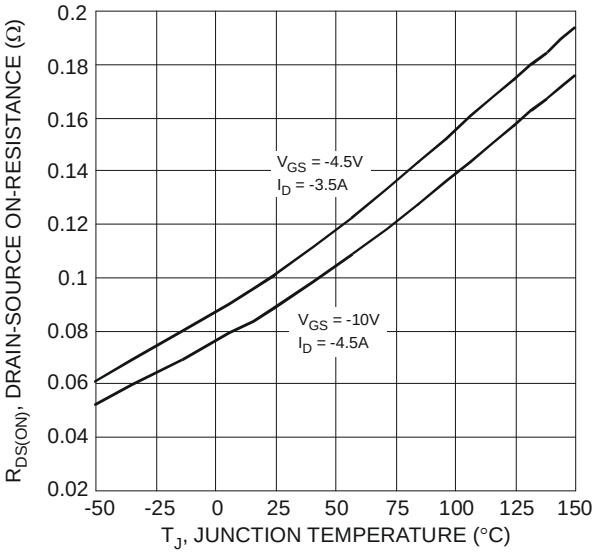


Figure 6 On-Resistance Variation with Temperature

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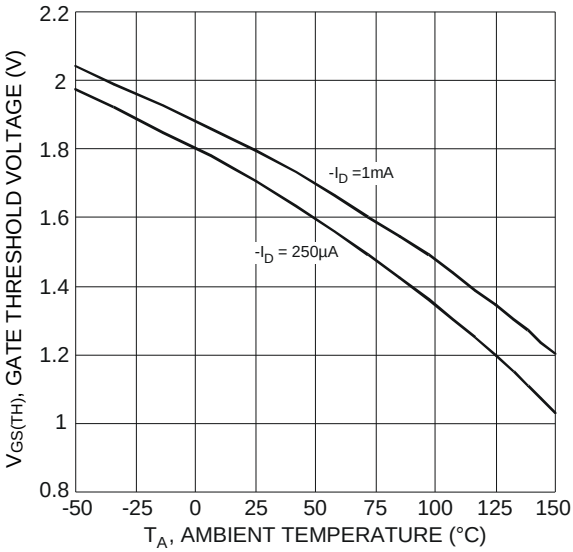


Figure 7 Gate Threshold Variation vs. Ambient Temperature

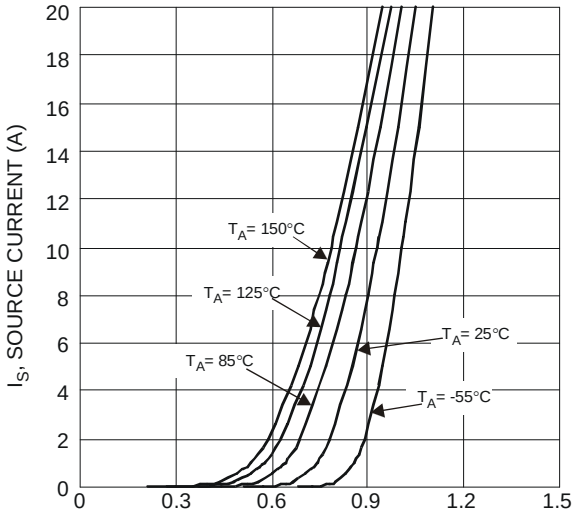


Figure 8 Diode Forward Voltage vs. Current

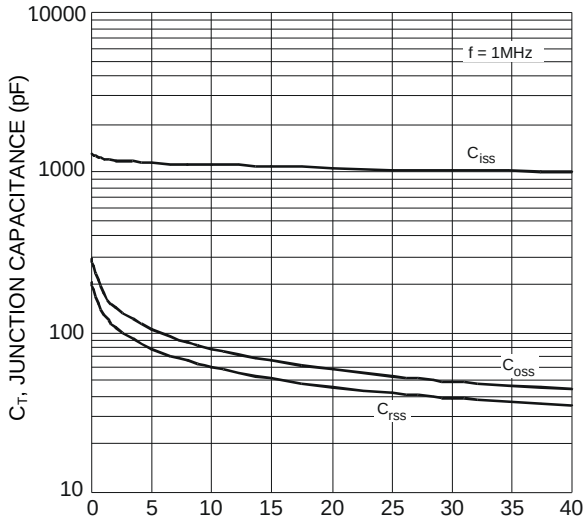


Figure 9 Typical Junction Capacitance

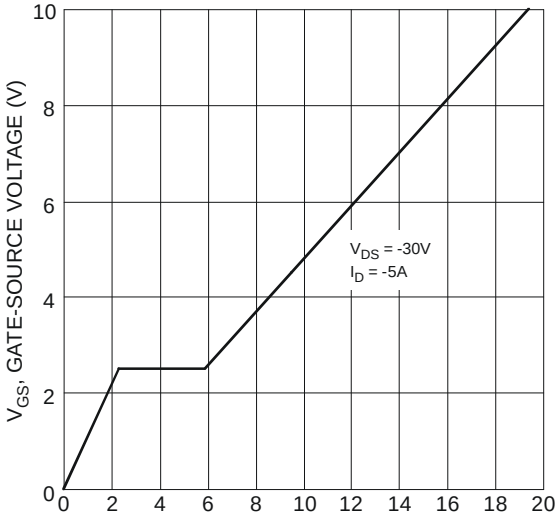


Figure 10 Gate-Charge Characteristics

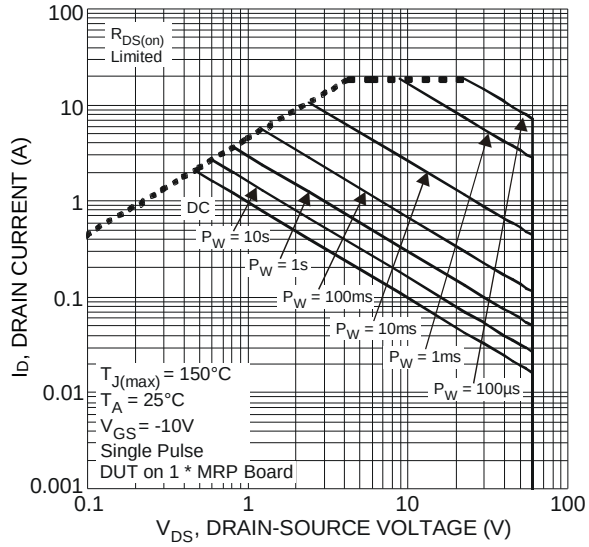


Figure 11 SOA, Safe Operation Area

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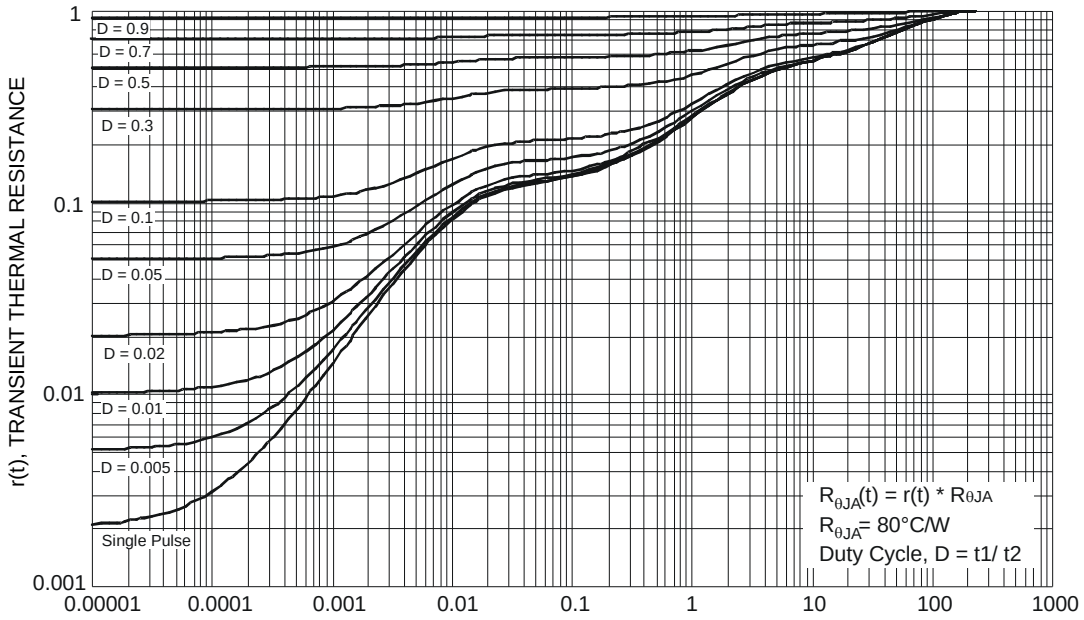


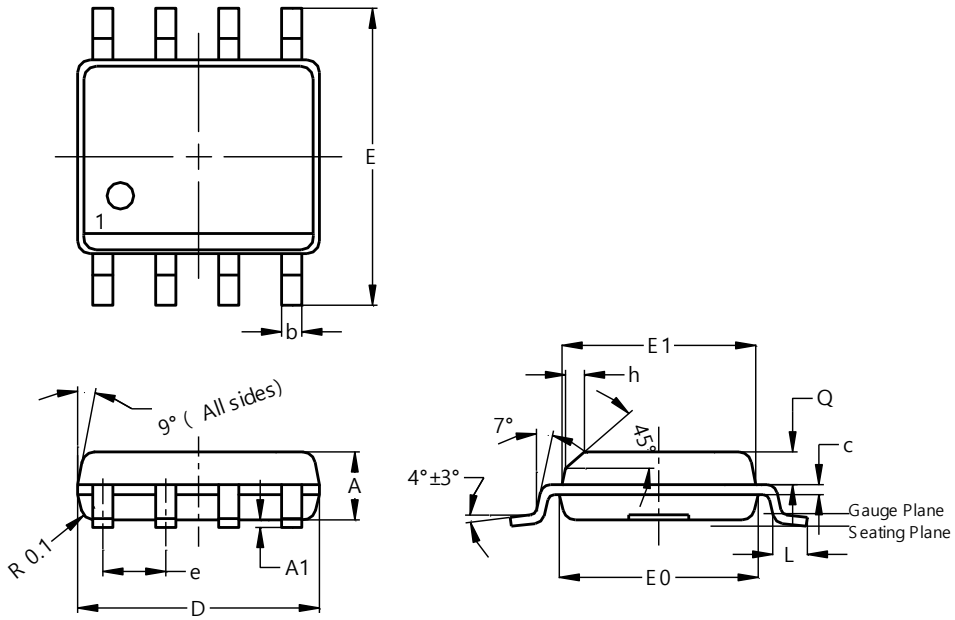
Figure 12 Transient Thermal Resistance

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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8

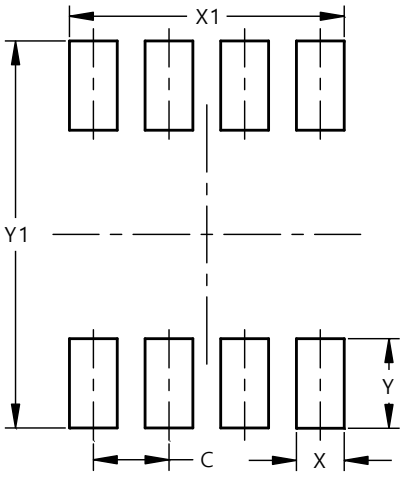


SO-8			
Dim	**Min**	**Max**	**Typ**
A	1.40	1.50	1.45
A1	0.10	0.20	0.15
b	0.30	0.50	0.40
c	0.15	0.25	0.20
D	4.85	4.95	4.90
E	5.90	6.10	6.00
E1	3.80	3.90	3.85
E0	3.85	3.95	3.90
e	--	--	1.27
h	--	--	0.35
L	0.62	0.82	0.72
Q	0.60	0.70	0.65
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8



Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50

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