300-mA Output, High PSRR, Low-Dropout Linear Regulator

Features

- Input Voltage Range: 2.4 V to 5.5 V
- Output Voltage Options:
  - Fixed Voltage: 1.2 V, 1.5 V, 1.8 V, 2.5 V, 2.8 V, 3 V, 3.3 V
  - Adjustable Voltage: 0.8 V to 5 V
- High Output Accuracy:
  - ±1% Typical Under Room Temperature
  - ±2% Through Operating Conditions
- Maximum Output Current: 300 mA
- Low Dropout Voltage: 200 mV at 300 mA
- Low Quiescent Current and Shutdown Current
- Foldback Current Limit and Thermal Protection
- Stable with 2.2-μF Ceramic Capacitor
- Inrush Input Current Limitation During Start-up
- Thermal Shutdown Protection
- Junction Temperature Range: −40°C to +125°C
- Package Options: SOT23-5, SOT353-5 (SC70-5), DFN1×1-4

Applications

- Handheld Devices with Battery Power Supply
- POS
- Video Surveillance
- Wireless and IoT modules

Description

The TPL730 series is a high-performance and low-dropout linear regulator. The TPL730 series supports a maximum 300-mA output current with a low-quiescent current and high PSRR. The TPL730 series of products is stable with ceramic output capacitors from 2.2 μF to 10 μF.

The TPL730 series of products has a high PSRR with 60 dB at 1 kHz. This feature makes the TPL730 series very suitable for power-sensitive applications with high noise from the previous stage power supply. A quiescent current of as low as 49 μA and an only 20-nA shutdown current make the TPL730 series an ideal choice for portable devices with a battery power supply. The current limit foldback and thermal overload protection circuits improve reliability under heavy load conditions.

The TPL730 series provides several output voltage version options including the fixed version and the adjustable version with ±2% output voltage accuracy over operating conditions. The TPL730 series is guaranteed over the junction temperature range from −40°C to +125°C.

Typical Application Circuit

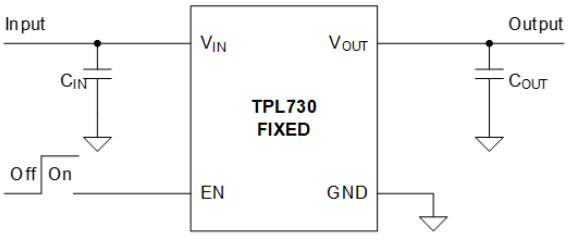


Figure 1. TPL730 Fixed Output Voltage

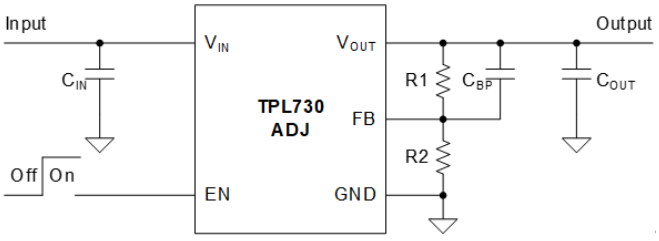


Figure 2. TPL730 Adjustable Output Voltage

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Product Family Table

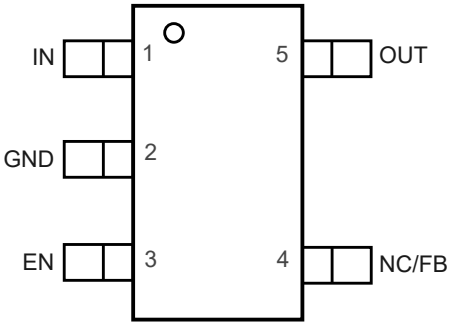
Order Number	Output Voltage (V)	Package
TPL730ADJ-5TR	Adjustable (0.8 V ~ 5 V)	SOT23-5
TPL730F12-5TR	Fixed 1.2 V	SOT23-5
TPL730F15-5TR	Fixed 1.5 V	SOT23-5
TPL730F18-5TR	Fixed 1.8 V	SOT23-5
TPL730F25-5TR	Fixed 2.5 V	SOT23-5
TPL730F28-5TR	Fixed 2.8 V	SOT23-5
TPL730F30-5TR	Fixed 3.0 V	SOT23-5
TPL730F33-5TR	Fixed 3.3 V	SOT23-5
TPL730ADJ-CR	Adjustable (0.8 V ~ 5 V)	SOT353-5 (SC70-5)
TPL730F18-CR	Fixed 1.8 V	SOT353-5 (SC70-5)
TPL730F28-CR	Fixed 2.8 V	SOT353-5 (SC70-5)
TPL730F30-CR	Fixed 3.0 V	SOT353-5 (SC70-5)
TPL730F33-CR	Fixed 3.3 V	SOT353-5 (SC70-5)
TPL730F12-FR	Fixed 1.2 V	DFN1×1-4
TPL730F15-FR	Fixed 1.5 V	DFN1×1-4
TPL730F18-FR	Fixed 1.8 V	DFN1×1-4
TPL730F25-FR	Fixed 2.5 V	DFN1×1-4
TPL730F28-FR	Fixed 2.8 V	DFN1×1-4
TPL730F30-FR	Fixed 3.0 V	DFN1×1-4
TPL730F33-FR	Fixed 3.3 V	DFN1×1-4

Revision History

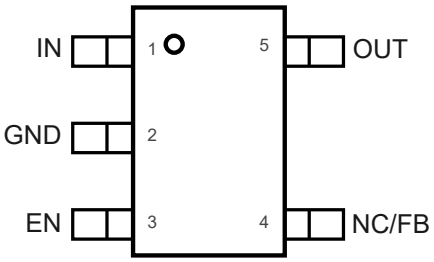
Date	Revision	Notes
2018-09-18	Rev.Pre.0	Preliminary version.
2018-11-26	Rev.A.0	Initial released version.
2019-02-11	Rev.A.1	1. Added the SC70-5 package. 2. Added voltage options of 1.5 V, 2.7 V, and 2.9 V. 3. Added the link to Figure 11 and Figure 12.
2020-08-15	Rev.A.2	1. Changed the “Soft-start Limits Input Current Surge During Enable” to “Inrush Input Current Limitation During Start-up”. 2. Added the power dissipation limitation. 3. Added the description of “Short-Circuit Protection”.
2021-03-09	Rev.A.3	1. Corrected the typical value of the current limit in the Feature Description. 2. Added Tape and Reel Information. 3. Updated Junction Temperature Range.
2022-05-08	Rev.A.4	Corrected the test condition in the Electrical Characteristics table.
2022-08-21	Rev.A.5	Corrected the pin number of the SC70-5 package.
2024-10-10	Rev.A.6	1. Removed voltage options of 2.7 V and 2.9 V. 2. Updated recommended C_{BP} range for adjustable output version

Pin Configuration and Functions

TPL730 Series
SOT23-5
Top View



TPL730 Series
SOT353-5 (SC70-5)
Top View



TPL730 Series
DFN1×1-4
Top View

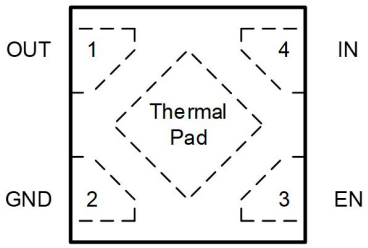


Table 1. Pin Functions

Pin No.			Name	I/O	Description
SOT23-5	SOT353-5 (SC70-5)	DFN1×1-4			
1	1	4	IN	I	The input voltage pin. Bypass IN to GND with a 1-μF or greater capacitor.
5	5	1	OUT	O	The regulated output voltage pin. Bypass OUT to GND with a 2.2-μF or greater capacitor.
3	3	3	EN	I	The regulator enable pin. Drive EN high to turn on the regulator; drive EN low to turn off the regulator. For the automatic startup, connect EN to IN directly.
2	2	2	GND	-	The ground reference pin. Connect the GND pin to the PCB ground plane directly.
4	4	-	NC	-	Not connected.
4	4	-	FB	I	The output feedback pin (adjustable version only). Connect to a resistor divider to adjust the output voltage. And connect C_{BP} with capacitance from 1nF 200nF between FB and OUT.

(1) The thermal pad must be connected to the PCB ground plane to maximize the thermal performance.

Specifications

Absolute Maximum Ratings

Parameter		Min	Max	Unit
V_{IN}, V_{EN}	Input Voltage	−0.3	6	V
V_{OUT}	Output Voltage	−0.3	6	V
V_{FB}	Feedback Voltage (Adjustable Version only)	−0.3	6	V
T_J	Maximum Operating Junction Temperature	−40	150	°C
T_{STG}	Storage Temperature Range	−65	150	°C
T_L	Lead Temperature (Soldering, 10 sec)		260	°C

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.
(2) All voltage values are with respect to GND.

ESD, Electrostatic Discharge Protection

Symbol	Parameter	Condition	Minimum Level	Unit
HBM	Human Body Model ESD	ANSI/ESDA/JEDEC JS-001 (1)	±8	kV
CDM	Charged Device Model ESD	ANSI/ESDA/JEDEC JS-002 (2)	±2	kV

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.
(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

Recommended Operating Conditions

Parameter		Min	Max	Unit
V_{IN}	Input Voltage	2.4	5.5	V
V_{EN}	Enable Voltage	0	V_{IN}	V
V_{OUT}	Output Voltage	0	5	V
V_{FB}	Feedback Voltage (Adjustable Version Only)	0	V_{OUT}	V
I_{OUT}	Output Current	0	300	mA
P_D	Power Dissipation (SOT23-5 Package)	0	300	mW
	Power Dissipation (SOT353-5 (SC70-5) Package)	0	300	mW
	Power Dissipation (DFN1×1-4 Package)	0	300	mW
C_{OUT}	Output Capacitor	2.2	10	μF
C_{BP}	Bypass Capacitor	1	200	nF
T_J	Operating Junction Temperature Range	−40	125	°C

Thermal Information

Package Type	θ_{JA}	θ_{JC}	Unit
SOT23-5	280	62	°C/W
SOT353-5 (SC70-5)	310	80	°C/W
DFN1×1-4	210	110	°C/W

Electrical Characteristics

All test conditions: V_{IN} = $V_{OUT\ (NOM)}$ + 0.5 V or 2.4 V, whichever is greater; C_{OUT} = 2.2 µF, T_A = +25°C, unless otherwise noted.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Supply Voltage and Current						
V_{IN}	Input Voltage Range		2.4		5.5	V
I_{GND}	Ground Pin Current	I_{OUT} = 0 mA		49		µA
		I_{OUT} = 100 mA		200		µA
I_{SHDN}	Shutdown Current	EN = GND		20		nA
UVLO	V_{IN} Under-Voltage Lock-out	V_{IN} Rising		1.9		V
		Hysteresis		200		mV
Enable Input Voltage and Current						
$V_{IH\ (EN)}$	EN Logic-Input High Level (Enable)		1.2		V_{IN}	V
$V_{IL\ (EN)}$	EN Logic-Input Low Level (Disable)		0		0.4	V
I_{EN}	EN Pin Leakage Current	EN = 5 V		1		µA
Regulated Output Voltage and Current						
V_{OUT}	Output Voltage Accuracy	T_J = +25°C		1%		
		−40°C ≤ T_J ≤ +125°C	−2%		2%	
V_{FB}	Feedback Pin Voltage	ADJ Version Only	0.784	0.8	0.816	V
$\triangle V_{OUT}$	Line Regulation	V_{IN} = 2.4 V or $V_{OUT\ (NOM)}$ + 0.5 V to 5.5 V, I_{OUT} = 1 mA		1	5	mV
	Load Regulation	I_{OUT} = 1 mA to 300 mA		20		mV
V_{DO} (1)	Dropout Voltage	V_{IN} = 0.98 × $V_{OUT\ (NOM)}$, I_{OUT} = 100 mA		75		mV
		V_{IN} = 0.98 × $V_{OUT\ (NOM)}$, I_{OUT} = 300 mA		200	250	mV
I_{OUT}	Output Current	V_{OUT} in Regulation	0		300	mA
I_{CL}	Output Current Limit	V_{OUT} = 0.9 × $V_{OUT\ (NOM)}$	350	1000	1400	mA
Regulated Output Voltage and Current						
PSRR	Power Supply Rejection Ratio (Fixed Version)	I_{OUT} = 100 mA, f = 1 kHz		60		dB
		I_{OUT} = 100 mA, f = 100 kHz		40		dB
		I_{OUT} = 100 mA, f = 1 MHz		40		dB
	Power Supply Rejection Ratio (ADJ Version)	I_{OUT} = 100 mA, f = 1 kHz, C_{BP} = 100 nF		65		dB
		I_{OUT} = 100 mA, f = 100 kHz, C_{BP} = 100 nF		60		dB
		I_{OUT} = 100 mA, f = 1 MHz, C_{BP} = 100 nF		45		dB

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_N	Output Noise Voltage (Fixed Version)	I_{OUT} = 100 mA, BW = 100 Hz to 80 kHz		130		μV_{RMS}
	Output Noise Voltage (ADJ Version)	I_{OUT} = 100 mA, BW = 100 Hz to 80 kHz, C_{BP} = 100 nF		40		μV_{RMS}
T_{STR} (2)	Start-up Time (Fixed Version)	I_{OUT} = 300 mA, C_{OUT} = 2.2 µF		150		µs
	Start-up Time (ADJ Version)	I_{OUT} = 300 mA, C_{OUT} = 2.2 µF, C_{BP} = 100 nF		15		ms
Temperature Range						
T_{SD}	Thermal Shutdown Temperature			170		ºC
	Thermal Shutdown Hysteresis			30		ºC

(1) Dropout voltage is the minimum input-to-output voltage differential needed to maintain regulation at a specified output current. In dropout, the output voltage will be equal to $V_{IN} - V_{DROPOUT}$.

(2) The start-up time from EN assertion to 0.98 × $V_{OUT\ (NOM)}$.

Typical Performance Characteristics

All test conditions: V_{IN} = $V_{OUT\ (NOM)}$ + 0.5 V or 2.4 V, whichever is greater; C_{OUT} = 2.2 µF, T_A = +25℃, unless otherwise noted.

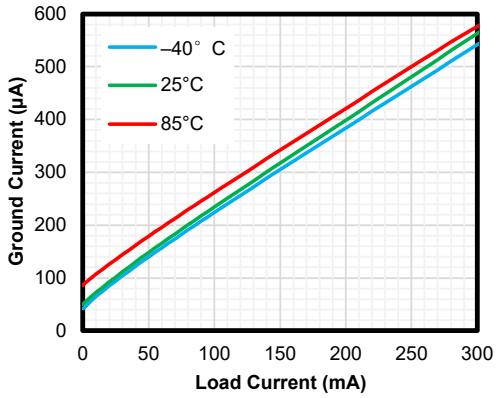


Figure 3. Quiescent Current vs. Output Current

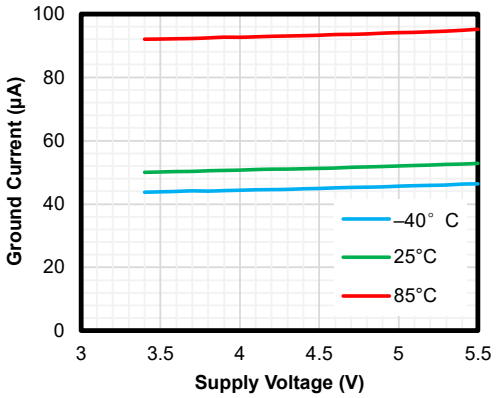


Figure 4. Quiescent Current vs. Input Voltage

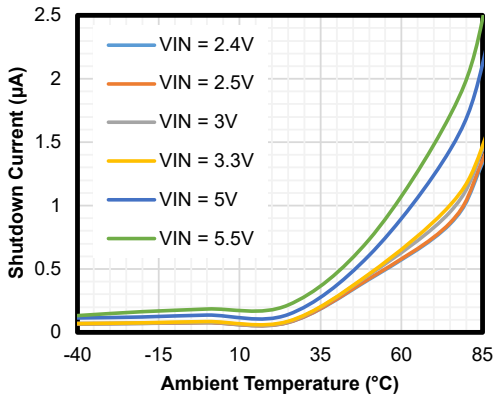


Figure 5. Shutdown Current vs. Ambient Temperature

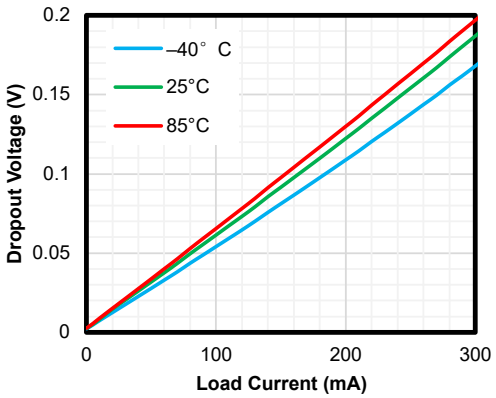


Figure 6. Dropout Voltage vs. Output Current

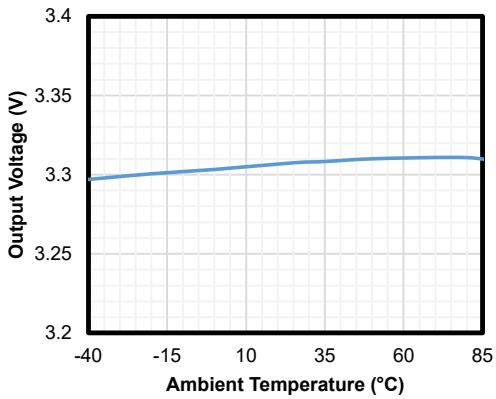


Figure 7. Output Accuracy vs. Ambient Temperature

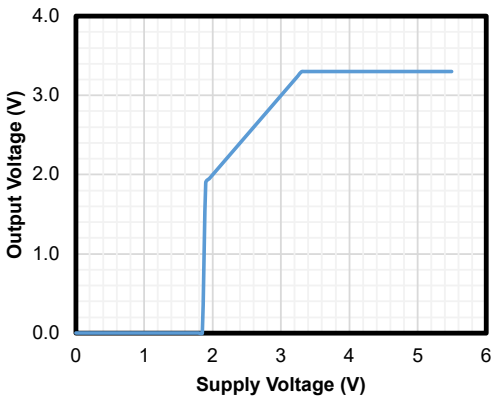


Figure 8. Output Voltage vs. Supply Voltage

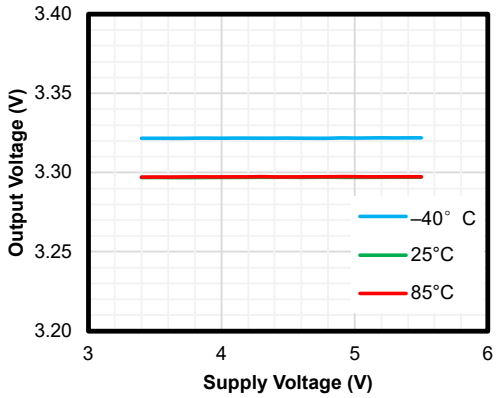


Figure 9. Line Regulation

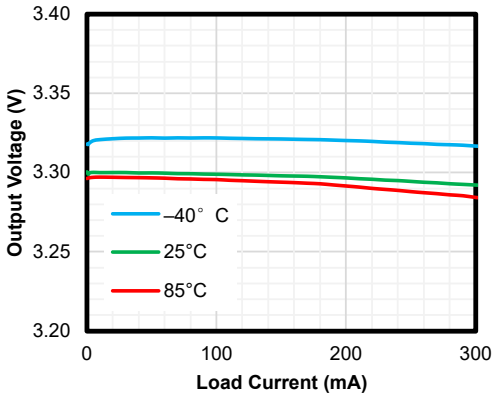


Figure 10. Load Regulation

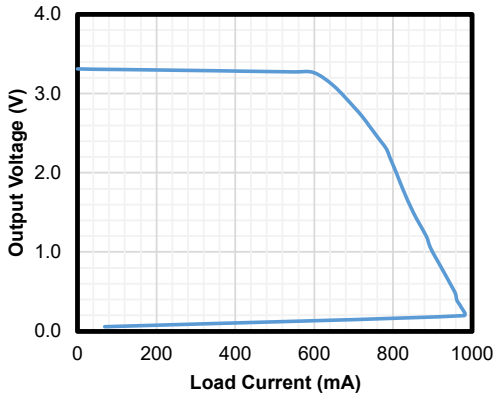


Figure 11. Foldback Current Limit

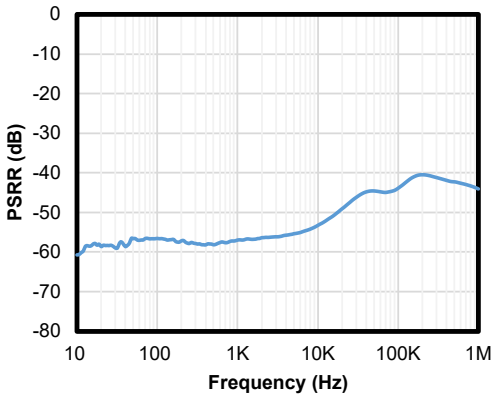


Figure 12. PSRR

Detailed Description

Overview

The TPL730 series of devices is a 300-mA high PSRR, low-dropout linear regulator with a very low quiescent current. It operates from 2.4 V to 5.5 V and consumes a quiescent current of 49 μA at no load and only 20 nA in shutdown mode.

The TPL730 series is available in fixed voltage versions of 1.2 V, 1.5 V, 1.8 V, 2.5 V, 2.8 V,3 V, and 3.3 V, and adjustable voltage versions of 0.8 V to 5 V with ±2% output voltage accuracy over operating conditions.

Functional Block Diagram

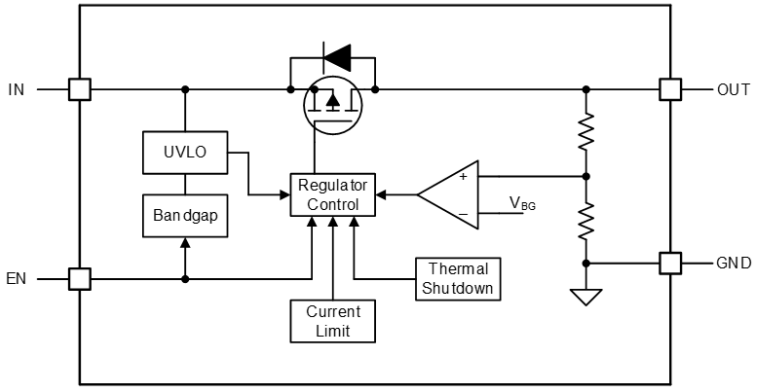


Figure 13. TPL730 Series Fixed Output Version

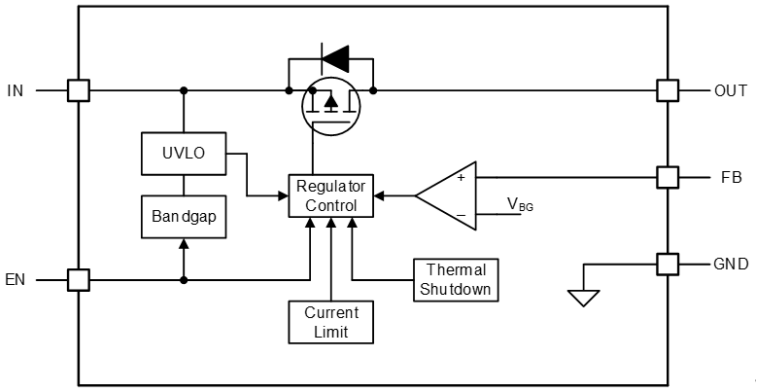


Figure 14. TPL730 Series Adjustable Output Version

Feature Description

Enable

The enable pin (EN) is active high. Connect this pin to the GPIO of an external processor or digital logic control circuit to enable and disable the device. Or connect this pin to the IN pin for self-bias applications.

Under-Voltage Lockout (UVLO)

The TPL730 series uses an under-voltage lockout circuit (UVLO = 1.9 V) to keep the output shut off until the internal circuitry operates properly.

Regulated Output Voltage

The TPL730 series is available in fixed voltage versions of 1.2 V, 1.5 V, 1.8 V, 2.5 V, 2.8 V, 3 V, and 3.3 V. When the input voltage is higher than $V_{OUT\ (NOM)}$ + V_{DO} or 2.4 V, the output pin is the regulated output based on the selected voltage version. When the input voltage falls below $V_{OUT\ (NOM)}$ + V_{DO} or 2.4 V, the output pin tracks the input voltage minus the dropout voltage based on the load current. When the input voltage drops below the UVLO threshold, the output keeps shut off.

Adjustable Output Voltage

The TPL730 series is also available in adjustable voltage versions of 0.8 V to 5 V by selecting suitable external resistor dividers. Use Equation 1 to calculate the output voltage (V_{FB} = 0.8 V). Selecting the resistor value of (R1 + R2) between 10 kΩ and 100 kΩ is suggested.

$$V_{OUT} = V_{FB} \times \left(1 + \frac{R1}{R2}\right) \quad (1)$$

Current Limit

The TPL730 series integrates an internal foldback current limit that helps to protect the regulator during fault conditions. When the output is shorted, the LDO supplies a typical current of 1000 mA. The output voltage is not regulated when the device is in current limit and is $V_{OUT} = I_{CL} \times R_{LOAD}$.

Short-Circuit Protection

The TPL730 series integrates the short-circuit protection. When the output pin is shorted to ground or forced to a voltage below 0.2 V, the output current of the TPL730 series is limited to a typical value of 150 mA.

Thermal Shutdown

During normal operation, the LDO junction temperature should not exceed 125°C. When the junction temperature exceeds the thermal shutdown threshold, the LDO shuts down the output immediately. Until when the junction temperature falls below the thermal shutdown threshold minus thermal shutdown hysteresis, the output turns on again.

Application and Implementation

> Note
>
> Information in the following application sections is not part of the 3PEAK's component specification and 3PEAK does not warrant its accuracy or completeness. 3PEAK's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

Application Information

The TPL730 series of devices is a 300-mA high PSRR, low-dropout linear regulator with a low quiescent current. The following application schematic shows a typical usage of the TPL730 series.

Typical Application

Figure 15 and Figure 16 show the typical application schematic of the TPL730 series.

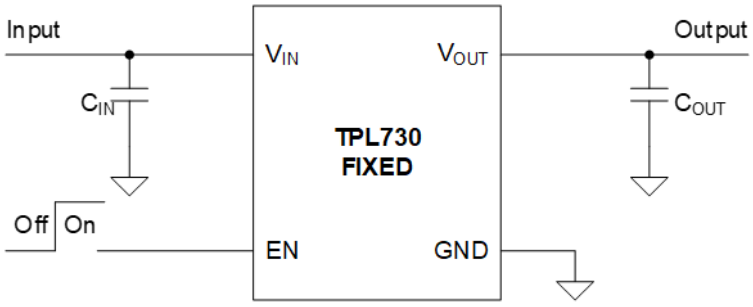


Figure 15. TPL730 Fixed Output Voltage

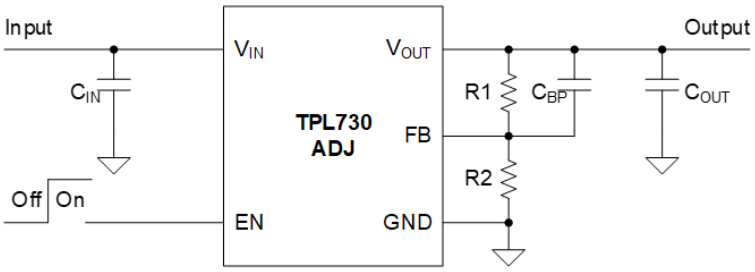


Figure 16. TPL730 Adjustable Output Voltage

Input Capacitor and Output Capacitor

3PEAK recommends adding a 1-μF or greater capacitor with a 0.1-μF capacitor in parallel at the IN pin to keep the input voltage stable. The voltage rating of the capacitors must be greater than the maximum input voltage.

To ensure loop stability, the TPL730 series requires an output capacitor with a minimum effective capacitance value of 2.2 μF. 3PEAK recommends selecting an X5R- or X7R-type ceramic capacitor with low ESR over temperature.

The TPL730 adjustable output version requires a bypass capacitor C_{BP} with a capacitance value from 1nF to 200nF to ensure loop stability.

All capacitors must be placed as close to the pins of the device as possible.

Power Dissipation

During normal operation, the LDO junction temperature should not exceed 125°C. Use the below equations to calculate the power dissipation and estimate the junction temperature.

The power dissipation can be calculated using Equation 2.

$$P_D = (V_{IN} - V_{OUT}) \times I_{OUT} + V_{IN} \times I_{GND} \quad (2)$$

The junction temperature can be estimated using Equation 3. θ_{JA} is the junction-to-ambient thermal resistance (See Thermal Information).

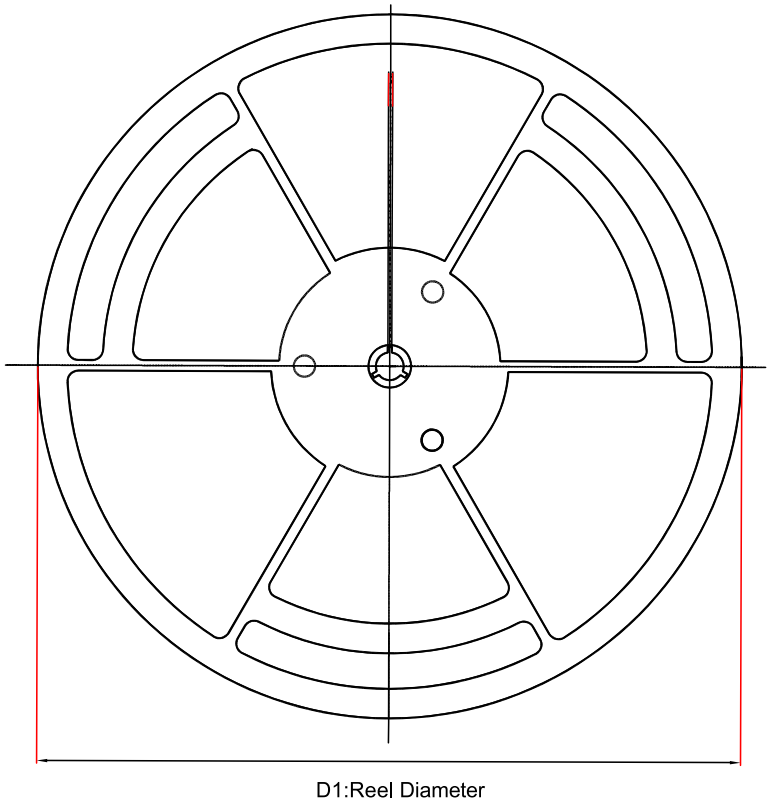
$$T_J = T_A + P_D \times \theta_{JA} \quad (3)$$

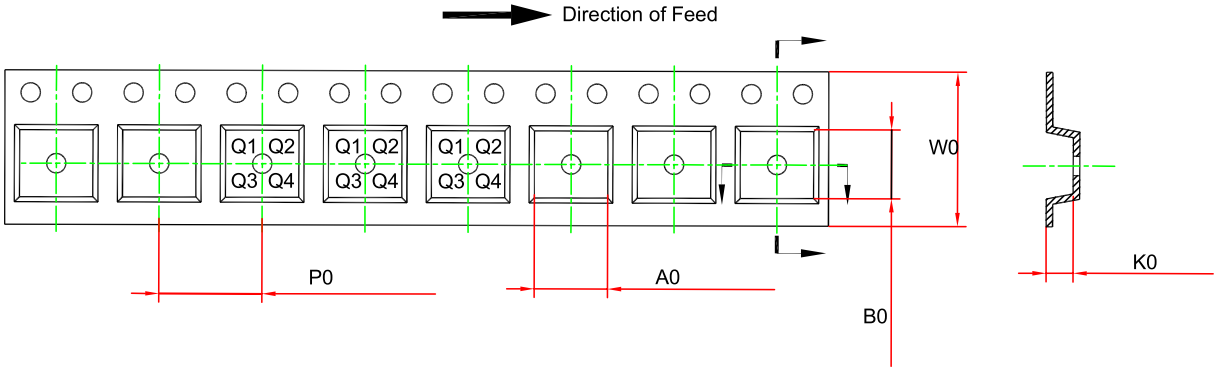
Layout

Layout Guideline

- Both input capacitors and output capacitors must be placed as close to the pins of the device as possible.
- It is recommended to bypass the input pin to ground with a 0.1-μF bypass capacitor. The loop area formed by the bypass capacitor connection, the IN pin, and the GND pin of the system must be as small as possible.
- It is recommended to use wide trace lengths or thick copper weight to minimize I×R drop and heat dissipation.

Tape and Reel Information





Order Number	Package	D1 (mm)	W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	W0 (mm)	Pin1 Quadrant
TPL730ADJ-5 TR	SOT23-5	180.0	13.1	3.2	3.2	1.4	4.0	8.0	Q3
TPL730Fxx-5T R	SOT23-5	180.0	13.1	3.2	3.2	1.4	4.0	8.0	Q3
TPL730ADJ-CR	SOT353-5 (SC70-5)	178.0	12.3	2.4	2.5	1.2	4.0	8.0	Q3

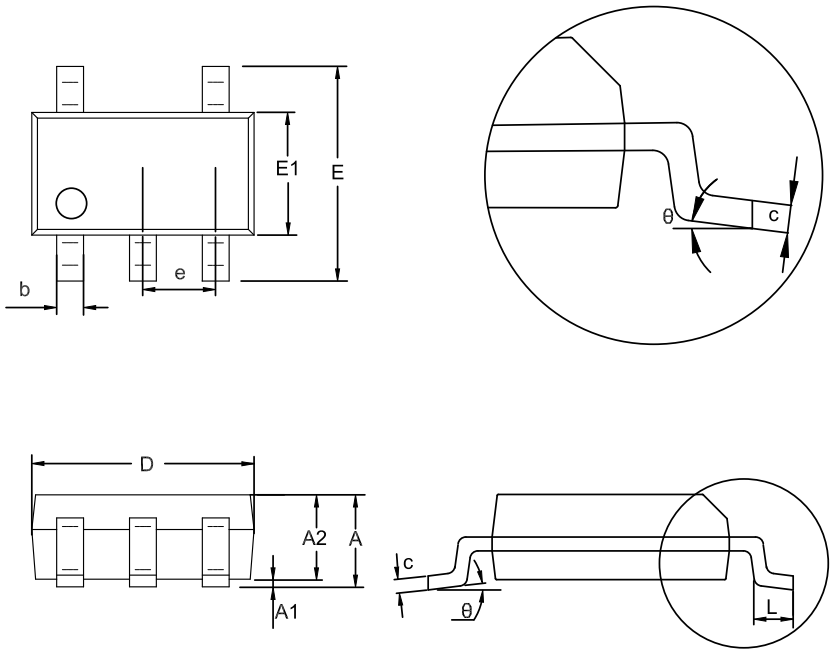
Order Number	Package	D1 (mm)	W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	W0 (mm)	Pin1 Quadrant
TPL730Fxx-CR	SOT353-5 (SC70-5)	178.0	12.3	2.4	2.5	1.2	4.0	8.0	Q3
TPL730Fxx-FR	DFN1×1-4	180.0	10.0	1.16	1.16	0.5	2.0	8.0	Q1

(1) The output voltage value, xx = 12 to 33, e.g., 33 means 3.3-V output voltage.

Package Outline Dimensions

SOT23-5

Package Outline Dimensions S5T(SOT23-5-A)



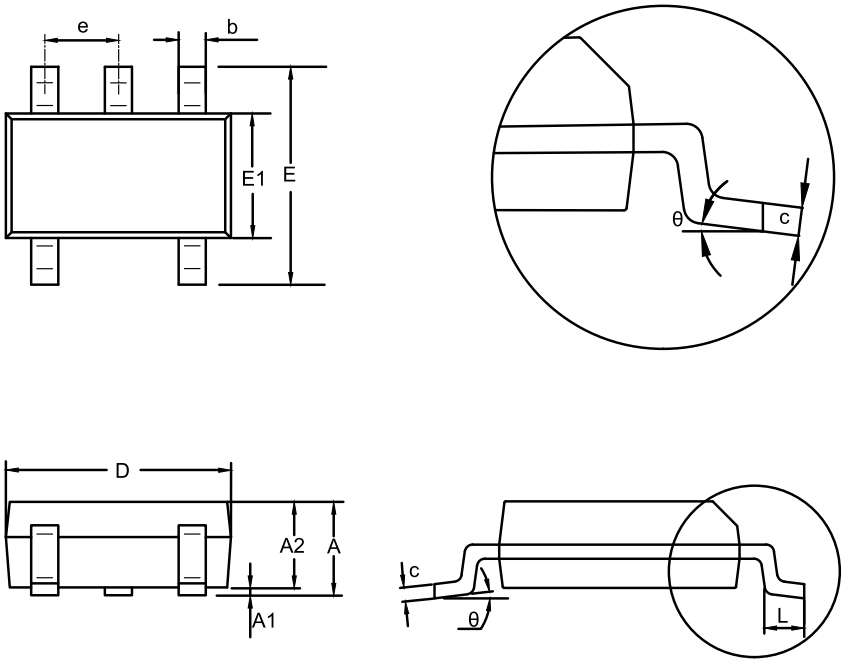
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.050	1.250	0.041	0.049
A1	0.000	0.150	0.000	0.006
A2	1.000	1.200	0.039	0.047
b	0.280	0.500	0.011	0.020
c	0.100	0.230	0.004	0.009
D	2.820	3.020	0.111	0.119
E	2.600	3.000	0.102	0.118
E1	1.500	1.720	0.059	0.068
e	0.950 BSC		0.037 BSC	
L	0.300	0.600	0.012	0.024
θ	0	8°	0	8°

NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

SOT353-5 (SC70-5)

Package Outline Dimensions SC5(SOT353-5-A)



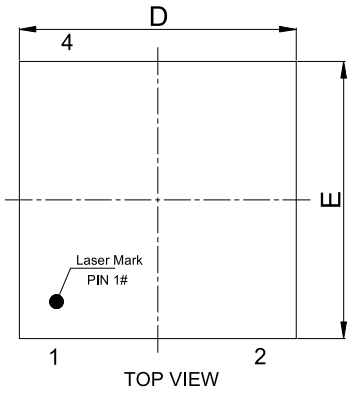
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.850	1.100	0.033	0.043
A1	0.000	0.100	0.000	0.004
A2	0.800	1.000	0.031	0.039
b	0.150	0.350	0.006	0.014
c	0.110	0.230	0.004	0.009
D	2.000	2.200	0.079	0.087
E	2.150	2.450	0.085	0.096
E1	1.150	1.350	0.045	0.053
e	0.650 BSC		0.026 BSC	
L	0.260	0.460	0.010	0.018
θ	0	8°	0	8°

NOTES

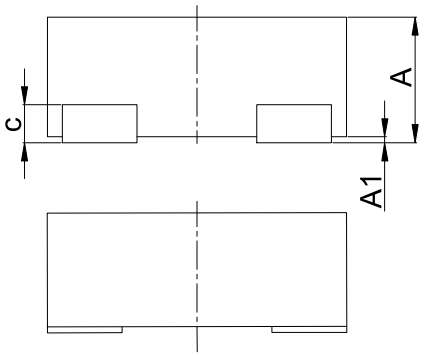
1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

DFN1×1-4

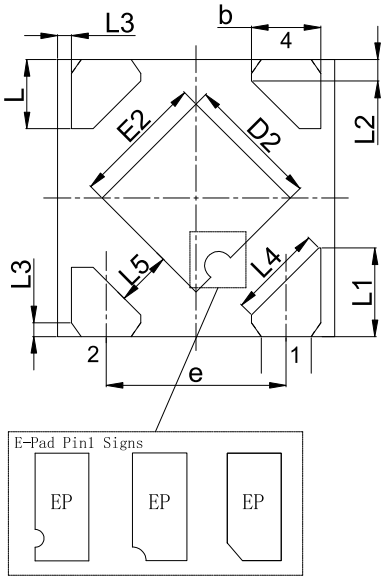
Package Outline Dimensions DF1(DFN1X1-4-A)



TOP VIEW



SIDE VIEW



BOTTOM VIEW

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.350	0.400	0.014	0.016
A1	0.000	0.050	0.000	0.002
b	0.200	0.300	0.008	0.012
c	0.070	0.170	0.003	0.007
D	0.950	1.050	0.037	0.041
D2	0.430	0.530	0.017	0.021
E	0.950	1.050	0.037	0.041
E2	0.430	0.530	0.017	0.021
e	0.650 BSC		0.026 BSC	
L	0.200	0.300	0.008	0.012
L1	0.270	0.370	0.011	0.015
L2	0.077 BSC		0.003 BSC	
L3	0.050 BSC		0.002 BSC	
L4	0.340 BSC		0.013 BSC	
L5	0.200 BSC		0.008 BSC	

NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.
3. The many types of E-pad Pin1 signs may appear in the product.

Order Information

Order Number	Operating Temperature Range	Package	Marking Information	MSL	Transport Media, Quantity	Eco Plan
TPL730ADJ-5TR	−40 to 125°C	SOT23-5	L6A	3	Tape and Reel, 3,000	Green
TPL730F12-5TR	−40 to 125°C	SOT23-5	L6D	3	Tape and Reel, 3,000	Green
TPL730F15-5TR	−40 to 125°C	SOT23-5	L6K	3	Tape and Reel, 3,000	Green
TPL730F18-5TR	−40 to 125°C	SOT23-5	L6F	3	Tape and Reel, 3,000	Green
TPL730F25-5TR	−40 to 125°C	SOT23-5	L6G	3	Tape and Reel, 3,000	Green
TPL730F28-5TR	−40 to 125°C	SOT23-5	L6H	3	Tape and Reel, 3,000	Green
TPL730F30-5TR	−40 to 125°C	SOT23-5	L6I	3	Tape and Reel, 3,000	Green
TPL730F33-5TR	−40 to 125°C	SOT23-5	L6J	3	Tape and Reel, 3,000	Green
TPL730ADJ-CR	−40 to 125°C	SOT353-5 (SC70-5)	L6A	3	Tape and Reel, 3,000	Green
TPL730F12-CR (1)	−40 to 125°C	SOT353-5 (SC70-5)	L6D	3	Tape and Reel, 3,000	Green
TPL730F15-CR (1)	−40 to 125°C	SOT353-5 (SC70-5)	L6K	3	Tape and Reel, 3,000	Green
TPL730F18-CR	−40 to 125°C	SOT353-5 (SC70-5)	L6F	3	Tape and Reel, 3,000	Green
TPL730F25-CR (1)	−40 to 125°C	SOT353-5 (SC70-5)	L6G	3	Tape and Reel, 3,000	Green
TPL730F28-CR	−40 to 125°C	SOT353-5 (SC70-5)	L6H	3	Tape and Reel, 3,000	Green
TPL730F30-CR	−40 to 125°C	SOT353-5 (SC70-5)	L6I	3	Tape and Reel, 3,000	Green
TPL730F33-CR	−40 to 125°C	SOT353-5 (SC70-5)	L6J	3	Tape and Reel, 3,000	Green
TPL730F12-FR	−40 to 125°C	DFN1x1-4	L6D	3	Tape and Reel, 12,000	Green
TPL730F15-FR	−40 to 125°C	DFN1x1-4	L6K	3	Tape and Reel, 12,000	Green
TPL730F18-FR	−40 to 125°C	DFN1x1-4	L6F	3	Tape and Reel, 12,000	Green
TPL730F25-FR	−40 to 125°C	DFN1x1-4	L6G	3	Tape and Reel, 12,000	Green

Order Number	Operating Temperature Range	Package	Marking Information	MSL	Transport Media, Quantity	Eco Plan
TPL730F28-FR	−40 to 125°C	DFN1x1-4	L6H	3	Tape and Reel, 12,000	Green
TPL730F30-FR	−40 to 125°C	DFN1x1-4	L6I	3	Tape and Reel, 12,000	Green
TPL730F33-FR	−40 to 125°C	DFN1x1-4	L6J	3	Tape and Reel, 12,000	Green

(1) For future products, contact the 3PEAK factory for more information and samples.

Green: 3PEAK defines "Green" to mean RoHS compatible and free of halogen substances.

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