IRGP4690DPbF
IRGP4690D-EPbF

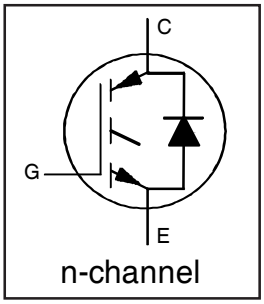
INSULATED GATE BIPOLAR TRANSISTOR WITH ULTRAFAST SOFT RECOVERY DIODE

V_{CES} = 600V

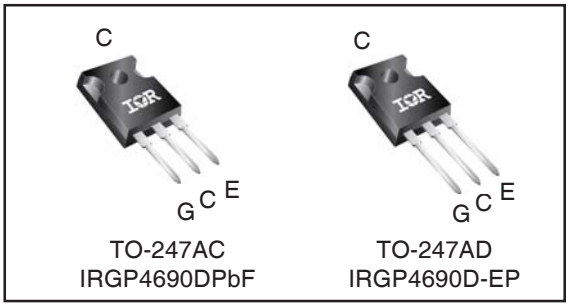
I_C = 90A, T_C = 100°C

$t_{SC} \geq$ 5µs, $T_{J(max)}$ = 175°C

$V_{CE(on)}$ typ. = 1.70V @ I_C = 75A



n-channel



TO-247AC
IRGP4690DPbF

TO-247AD
IRGP4690D-EP

G	C	E
Gate	Collector	Emitter

Applications
- Industrial Motor Drive
- Inverters
- UPS
- Welding

Features	→	Benefits
Low $V_{CE(ON)}$ and Switching Losses		High efficiency in a wide range of applications and switching frequencies
Square RBSOA and Maximum Junction Temperature 175°C		Improved reliability due to rugged hard switching performance and higher power capability
Positive $V_{CE\ (ON)}$ Temperature Coefficient		Excellent current sharing in parallel operation
5µs short circuit SOA		Enables short circuit protection scheme
Lead-Free, RoHS compliant		Environmentally friendly

Base part number	Package Type	Standard Pack		Orderable part number
		Form	Quantity	
IRGP4690DPbF	TO-247AC	Tube	25	IRGP4690DPbF
IRGP4690D-EPbF	TO-247AD	Tube	25	IRGP4690D-EPbF

Absolute Maximum Ratings

	Parameter	Max.	Units
V_{CES}	Collector-to-Emitter Voltage	600	V
I_C @ T_C = 25°C	Continuous Collector Current	140	A
I_C @ T_C = 100°C	Continuous Collector Current	90	
I_{CM}	Pulse Collector Current, V_{GE} = 15V	225	
I_{LM}	Clamped Inductive Load Current, V_{GE} = 20V ①	300	
I_F @ T_C = 25°C	Diode Continous Forward Current	70	
I_F @ T_C = 100°C	Diode Continous Forward Current	45	
I_{FM}	Diode Maximum Forward Current ④	300	
V_{GE}	Continuous Gate-to-Emitter Voltage	±20	V
	Transient Gate-to-Emitter Voltage	±30	
P_D @ T_C = 25°C	Maximum Power Dissipation	454	W
P_D @ T_C = 100°C	Maximum Power Dissipation	227	
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to +175	°C
	Soldering Temperature, for 10 sec.	300 (0.063 in. (1.6mm) from case)	
	Mounting Torque, 6-32 or M3 Screw	10 lbf·in (1.1 N·m)	

Thermal Resistance

	Parameter	Min.	Typ.	Max.	Units
$R_{\theta JC}$ (IGBT)	Junction-to-Case (IGBT) ②	—	—	0.33	°C/W
$R_{\theta JC}$ (Diode)	Junction-to-Case (Diode) ②	—	—	1.0	
$R_{\theta CS}$	Case-to-Sink (flat, greased surface)	—	0.24	—	
$R_{\theta JA}$	Junction-to-Ambient (typical socket mount)	—	—	40	

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Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)CES}$	Collector-to-Emitter Breakdown Voltage	600	—	—	V	V_{GE} = 0V, I_C = 100µA ③
$\Delta V_{(BR)CES}/\Delta T_J$	Temperature Coeff. of Breakdown Voltage	—	0.30	—	V/°C	V_{GE} = 0V, I_C = 2.0mA (25°C-175°C)
$V_{CE(on)}$	Collector-to-Emitter Saturation Voltage	—	1.70	2.10	V	I_C = 75A, V_{GE} = 15V, T_J = 25°C
		—	2.0	—		I_C = 75A, V_{GE} = 15V, T_J = 150°C
		—	2.1	—		I_C = 75A, V_{GE} = 15V, T_J = 175°C
$V_{GE(th)}$	Gate Threshold Voltage	4.0	—	6.5	V	V_{CE} = V_{GE}, I_C = 2.1mA
$\Delta V_{GE(th)}/\Delta T_J$	Threshold Voltage temp. coefficient	—	-21	—	mV/°C	V_{CE} = V_{GE}, I_C = 2.1mA (25°C - 175°C)
gfe	Forward Transconductance	—	50	—	S	V_{CE} = 50V, I_C = 75A, PW = 60µs
I_{CES}	Collector-to-Emitter Leakage Current	—	1.0	100	µA	V_{GE} = 0V, V_{CE} = 600V
		—	1040	—		V_{GE} = 0V, V_{CE} = 600V, T_J = 175°C
V_{FM}	Diode Forward Voltage Drop	—	2.23	3.0	V	I_F = 75A
		—	1.8	—		I_F = 75A, T_J = 175°C
I_{GES}	Gate-to-Emitter Leakage Current	—	—	±200	nA	V_{GE} = ±20V

Switching Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
Q_g	Total Gate Charge	—	150	—	nC	I_C = 75A
Q_{ge}	Gate-to-Emitter Charge	—	40	—		V_{GE} = 15V
Q_{gc}	Gate-to-Collector Charge	—	60	—		V_{CC} = 400V
E_{on}	Turn-On Switching Loss	—	2465	—	µJ	I_C = 75A, V_{CC} = 400V, V_{GE} = 15V R_G = 10Ω, L = 200µH, T_J = 25°C Energy losses include tail & diode reverse recovery ⑤
E_{off}	Turn-Off Switching Loss	—	2155	—		
E_{total}	Total Switching Loss	—	4620	—		
$t_{d(on)}$	Turn-On delay time	—	50	—	ns	
t_r	Rise time	—	70	—		
$t_{d(off)}$	Turn-Off delay time	—	200	—		
t_f	Fall time	—	60	—		
E_{on}	Turn-On Switching Loss	—	3870	—	µJ	I_C = 75A, V_{CC} = 400V, V_{GE}=15V R_G=10Ω, L=200µH,T_J = 175°C Energy losses include tail & diode reverse recovery ⑤
E_{off}	Turn-Off Switching Loss	—	2815	—		
E_{total}	Total Switching Loss	—	6685	—		
$t_{d(on)}$	Turn-On delay time	—	50	—	ns	
t_r	Rise time	—	70	—		
$t_{d(off)}$	Turn-Off delay time	—	240	—		
t_f	Fall time	—	70	—		
C_{ies}	Input Capacitance	—	4440	—	pF	V_{GE} = 0V
C_{oes}	Output Capacitance	—	245	—		V_{CC} = 30V
C_{res}	Reverse Transfer Capacitance	—	130	—		f = 1.0Mhz
RBSOA	Reverse Bias Safe Operating Area	FULL SQUARE				T_J = 175°C, I_C = 300A V_{CC} = 480V, Vp ≤ 600V Rg = 10Ω, V_{GE} = +20V to 0V
SCSOA	Short Circuit Safe Operating Area	5	—	—	µs	V_{CC} = 400V, Vp ≤ 600V Rg = 10Ω, V_{GE} = +15V to 0V
Erec	Reverse Recovery Energy of the Diode	—	470	—	µJ	T_J = 175°C
t_{rr}	Diode Reverse Recovery Time	—	155	—	ns	V_{CC} = 400V, I_F = 75A
I_{rr}	Peak Reverse Recovery Current	—	27	—	A	V_{GE} = 15V, Rg = 10Ω, L =60µH

Notes:

① V_{CC} = 80% (V_{CES}), V_{GE} = 20V, L = 10µH, R_G = 10Ω.
② R_θ is measured at T_J of approximately 90°C.
③ Refer to AN-1086 for guidelines for measuring $V_{(BR)CES}$ safely.
④ Pulse width limited by max. junction temperature.
⑤ Values influenced by parasitic L and C in measurement.

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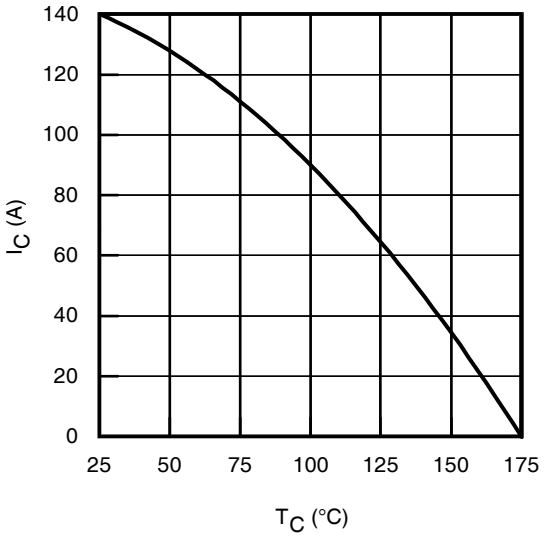


Fig. 1 - Maximum DC Collector Current vs. Case Temperature

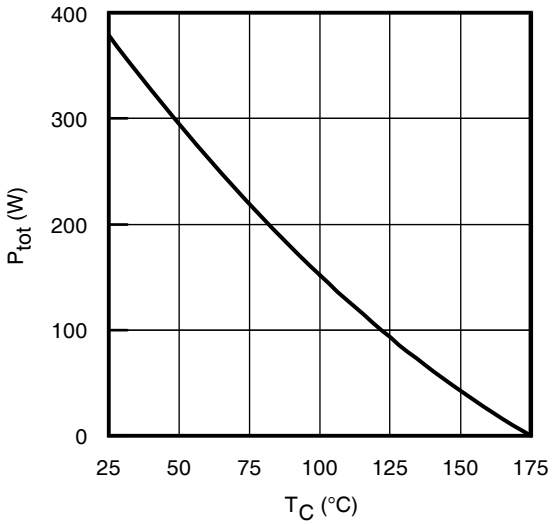


Fig. 2 - Power Dissipation vs. Case Temperature

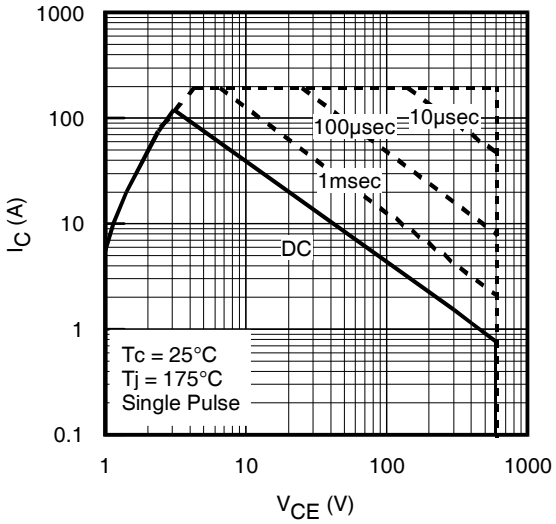


Fig. 3 - Forward SOA
T_C = 25°C, $T_J \leq$ 175°C; V_{GE} =15V

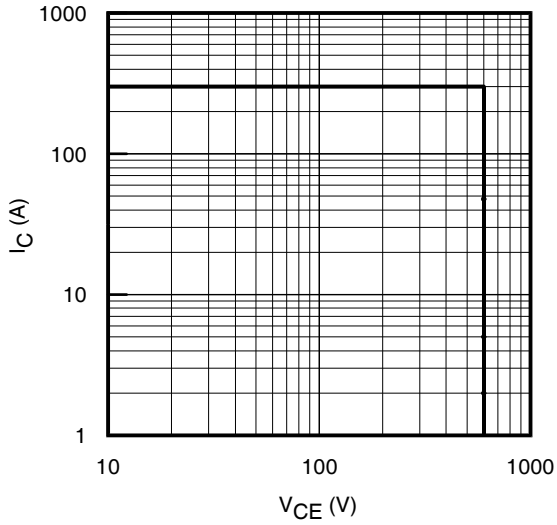


Fig. 4 - Reverse Bias SOA
T_J = 175°C; V_{GE} =20V

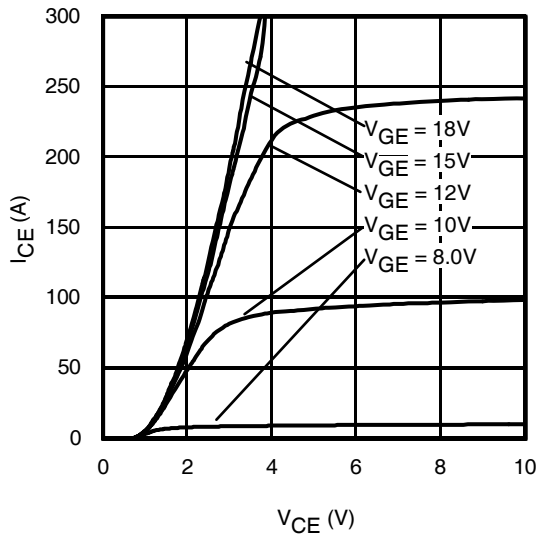


Fig. 5 - Typ. IGBT Output Characteristics
T_J = -40°C; tp = ≤60µs

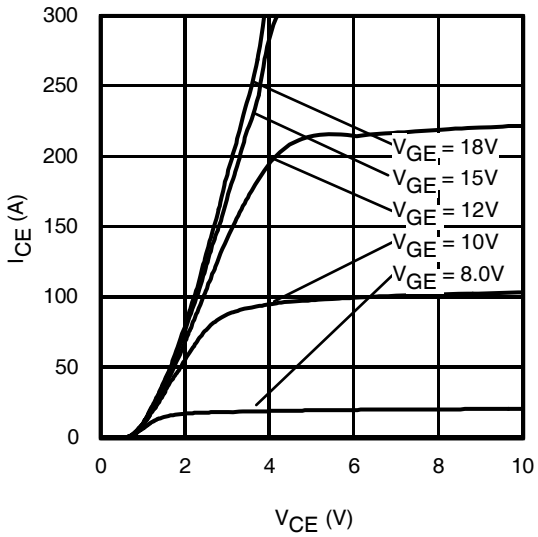


Fig. 6 - Typ. IGBT Output Characteristics
T_J = 25°C; tp = ≤60µs

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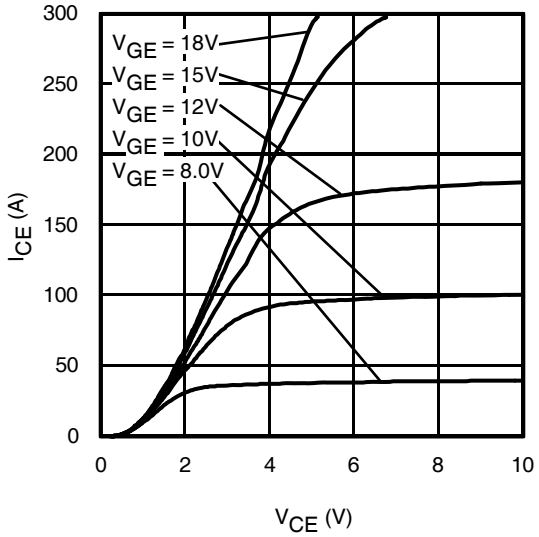


Fig. 7 - Typ. IGBT Output Characteristics
T_J = 175°C; tp = ≤60µs

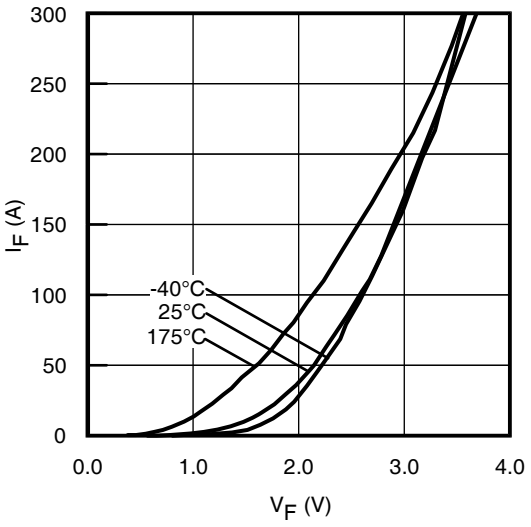


Fig. 8 - Typ. Diode Forward Characteristics
tp = 80µs

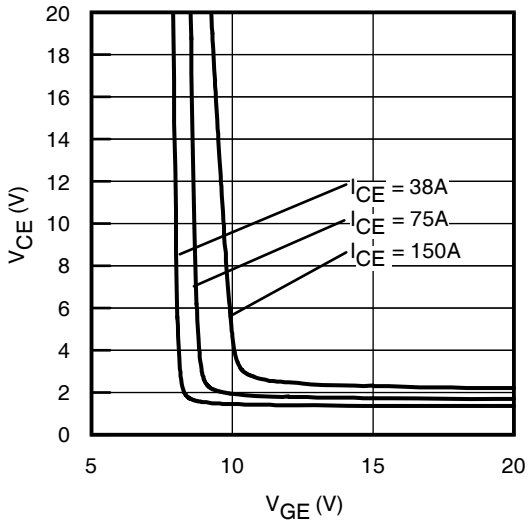


Fig. 9 - Typical V_{CE} vs. V_{GE}
T_J = -40°C

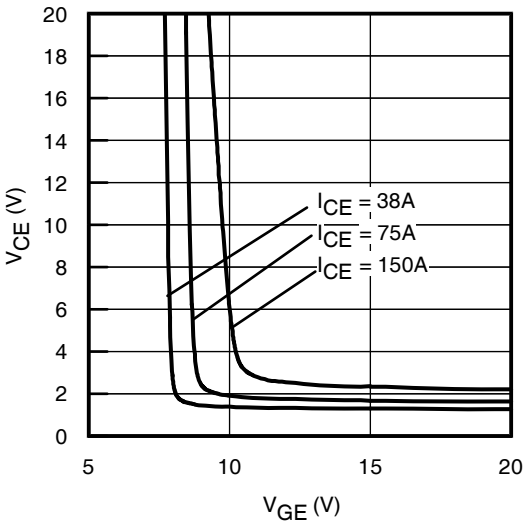


Fig. 10 - Typical V_{CE} vs. V_{GE}
T_J = 25°C

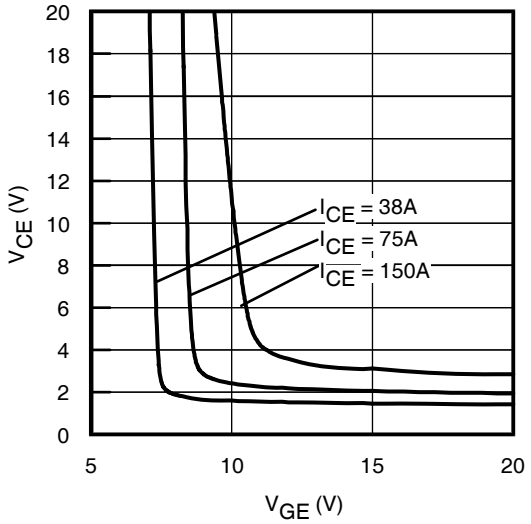


Fig. 11 - Typical V_{CE} vs. V_{GE}
T_J = 175°C

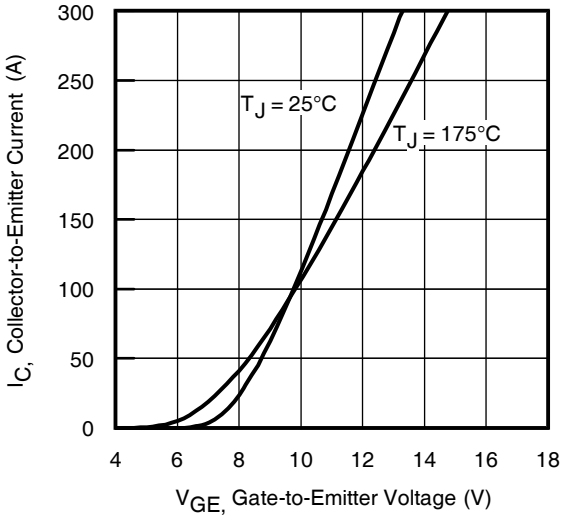


Fig. 12 - Typ. Transfer Characteristics
V_{CE} = 50V; tp = 60µs

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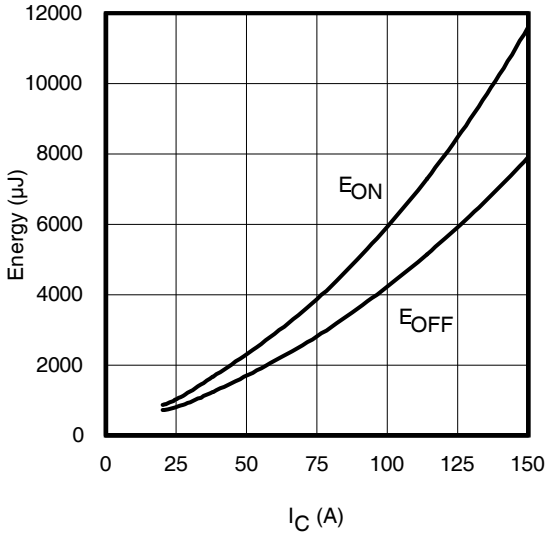


Fig. 13 - Typ. Energy Loss vs. I_C
T_J = 175°C; L = 200µH; V_{CE} = 400V, R_G = 10Ω; V_{GE} = 15V

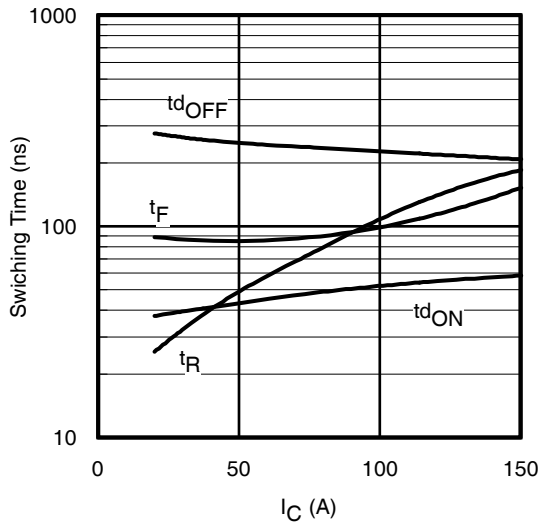


Fig. 14 - Typ. Switching Time vs. I_C
T_J = 175°C; L = 200µH; V_{CE} = 400V, R_G = 10Ω; V_{GE} = 15V

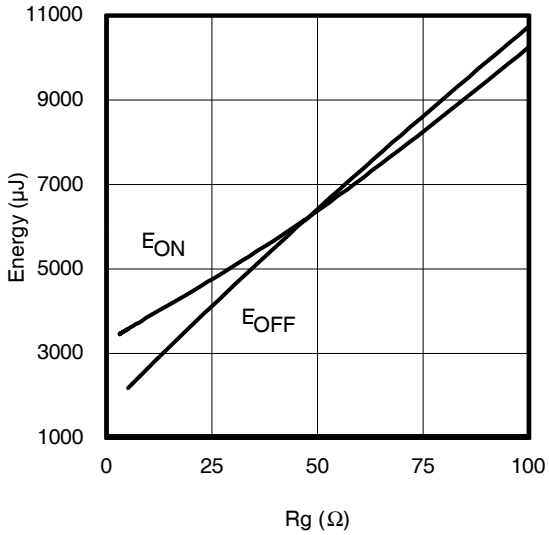


Fig. 15 - Typ. Energy Loss vs. R_G
T_J = 175°C; L = 200µH; V_{CE} = 400V, I_{CE} = 75A; V_{GE} = 15V

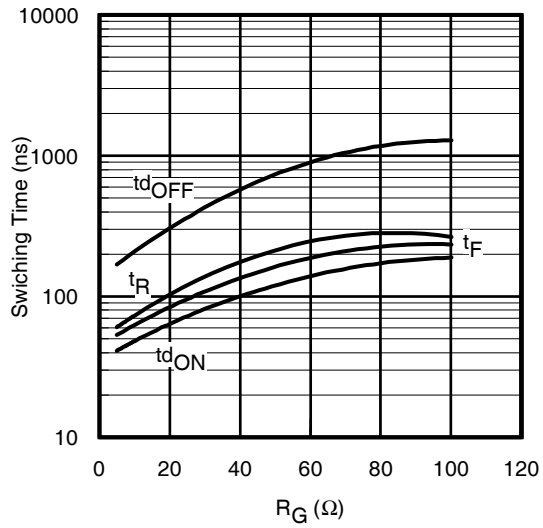


Fig. 16 - Typ. Switching Time vs. R_G
T_J = 175°C; L = 200µH; V_{CE} = 400V, I_{CE} = 75A; V_{GE} = 15V

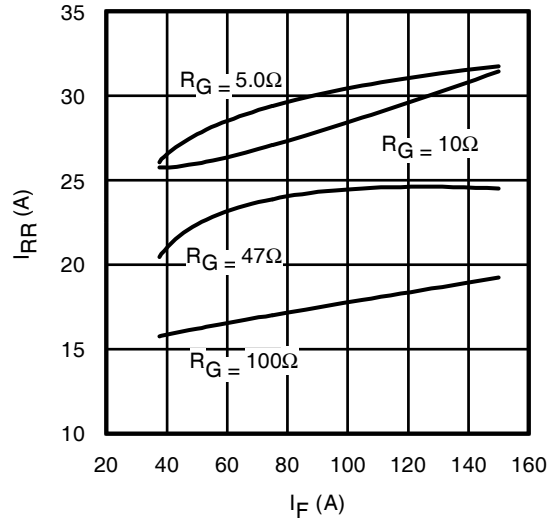


Fig. 17 - Typ. Diode I_{RR} vs. I_F
T_J = 175°C

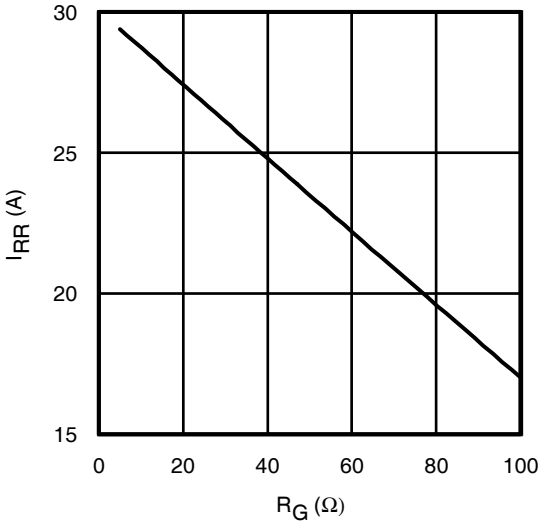


Fig. 18 - Typ. Diode I_{RR} vs. R_G
T_J = 175°C

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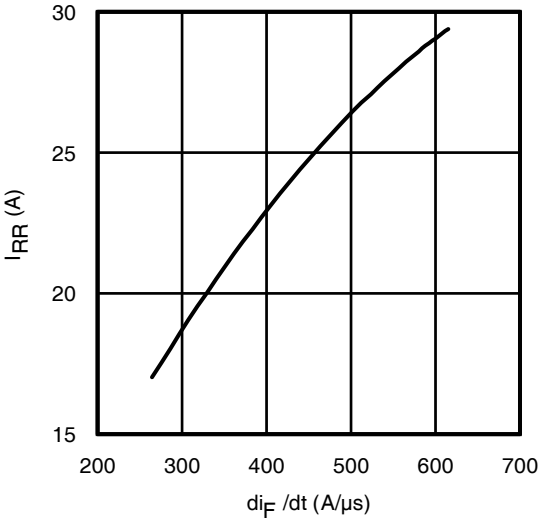


Fig. 19 - Typ. Diode I_{RR} vs. di_F/dt
V_{CC} = 400V; V_{GE} = 15V; I_F = 75A; T_J = 175°C

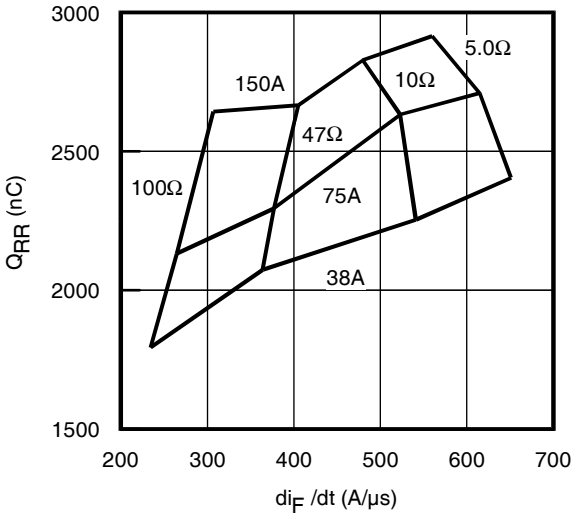


Fig. 20 - Typ. Diode Q_{RR} vs. di_F/dt
V_{CC} = 400V; V_{GE} = 15V; T_J = 175°C

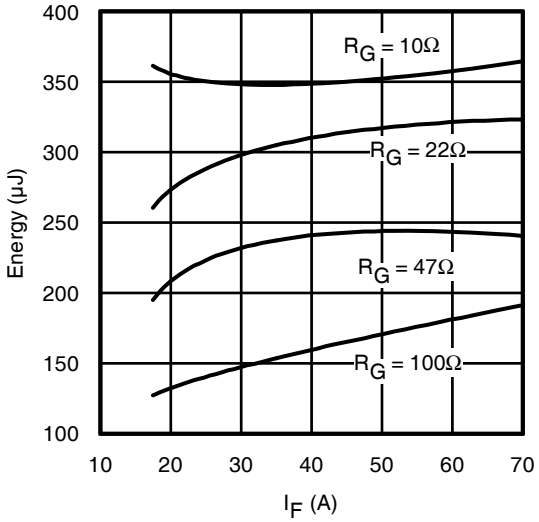


Fig. 21 - Typ. Diode E_{RR} vs. I_F
T_J = 175°C

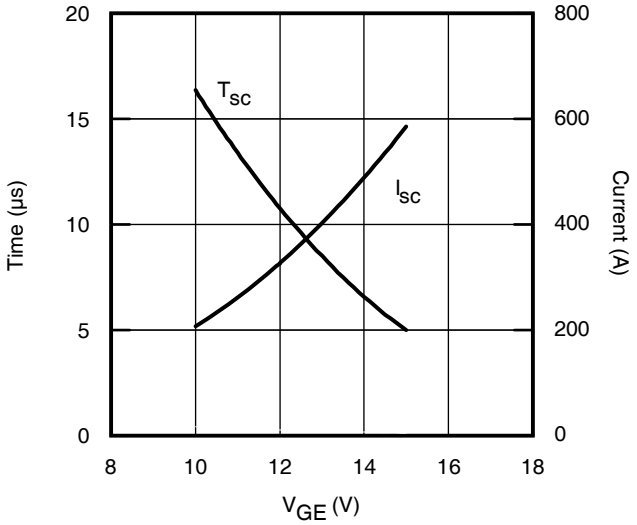


Fig. 22 - V_{GE} vs. Short Circuit Time
V_{CC} = 400V; T_C = 25°C

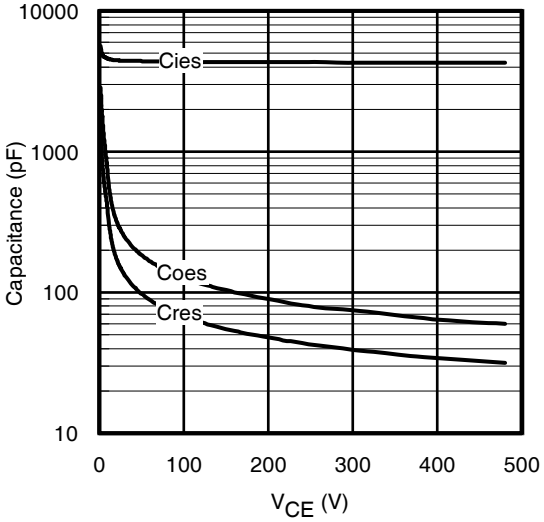


Fig. 23 - Typ. Capacitance vs. V_{CE}
V_{GE}= 0V; f = 1MHz

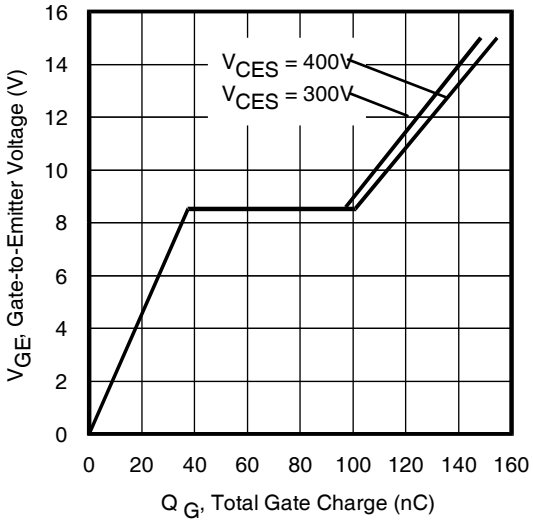


Fig. 24 - Typical Gate Charge vs. V_{GE}
I_{CE} = 75A; L = 485µH

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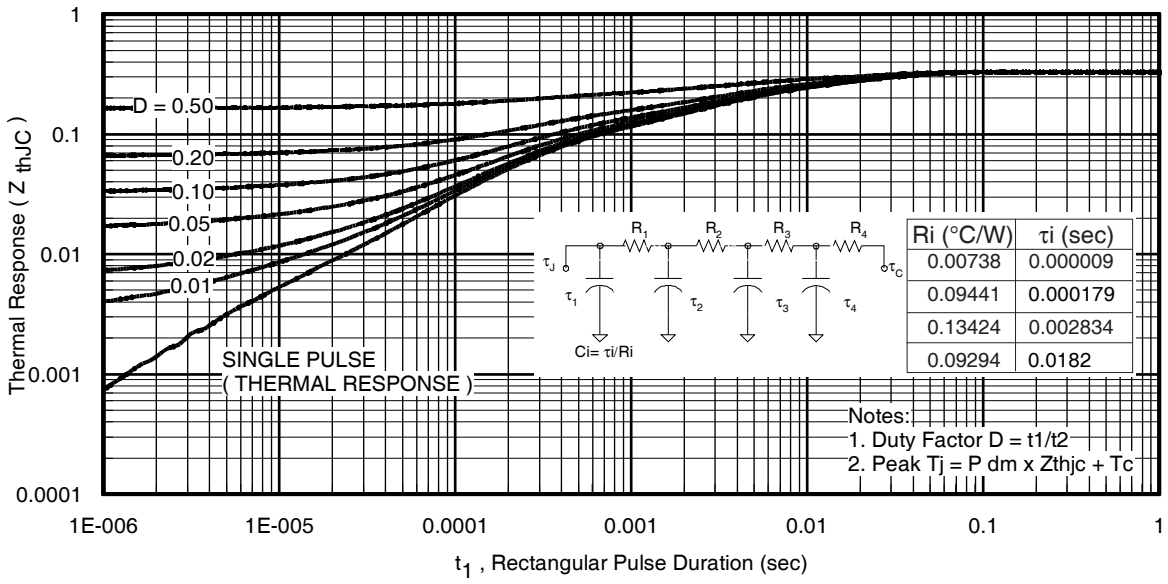


Fig 25. Maximum Transient Thermal Impedance, Junction-to-Case (IGBT)

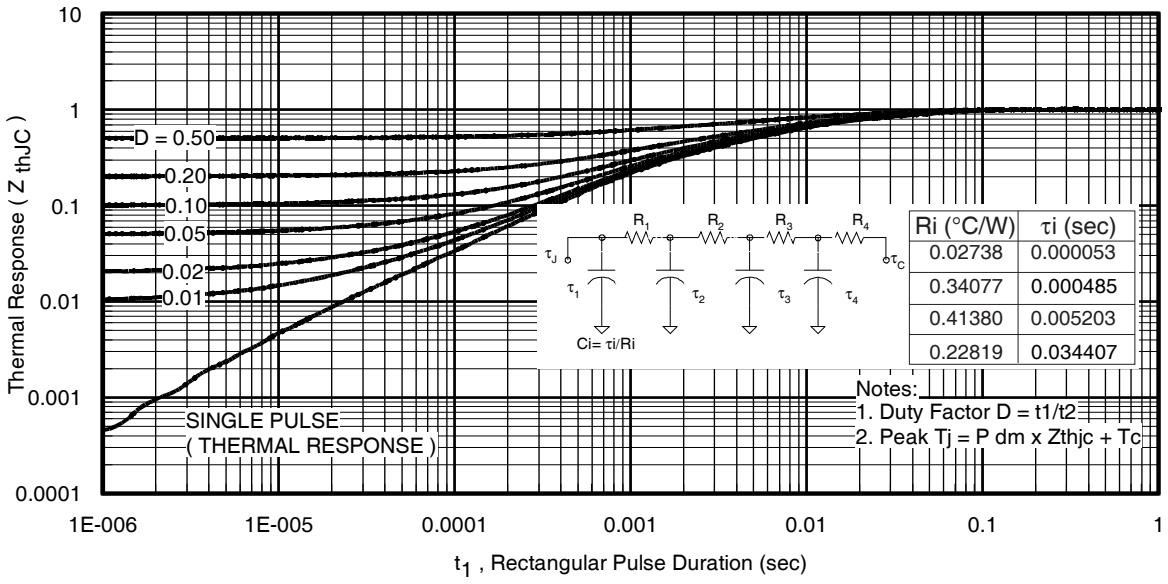


Fig. 26. Maximum Transient Thermal Impedance, Junction-to-Case (DIODE)

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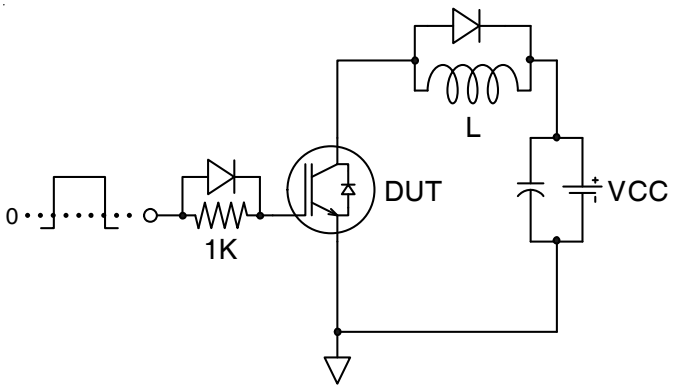


Fig.C.T.1 - Gate Charge Circuit (turn-off)

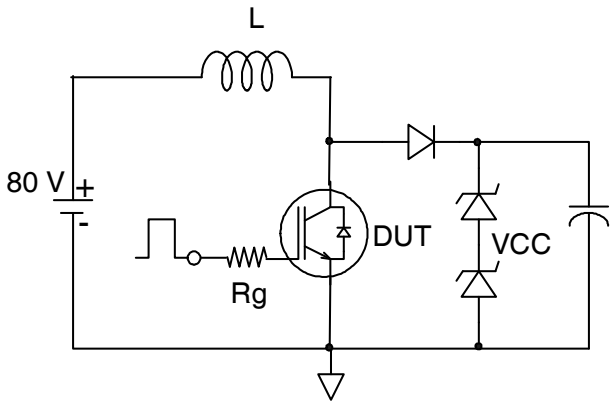
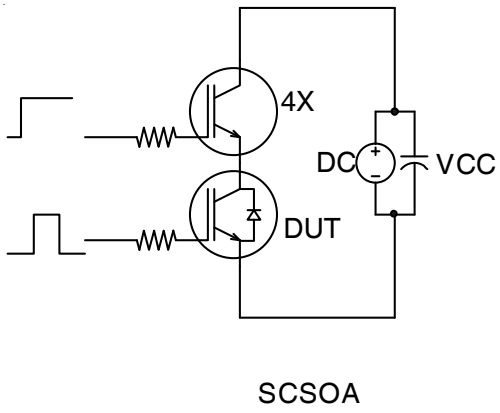


Fig.C.T.2 - RBSOA Circuit



SCSOA

Fig.C.T.3 - S.C. SOA Circuit

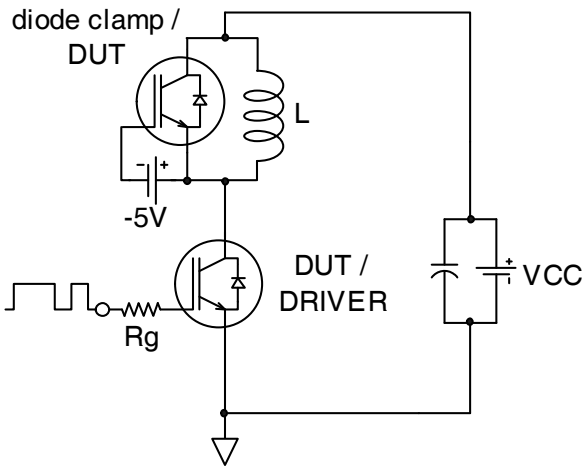


Fig.C.T.4 - Switching Loss Circuit

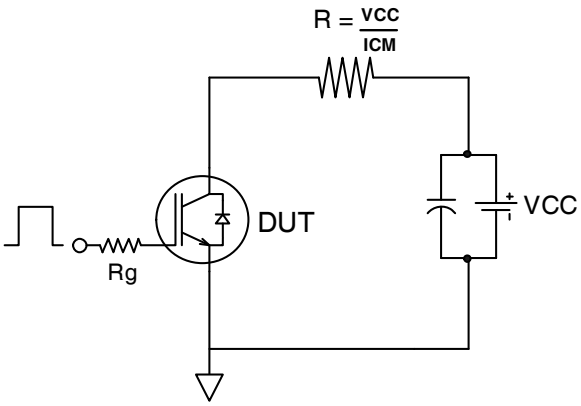


Fig.C.T.5 - Resistive Load Circuit

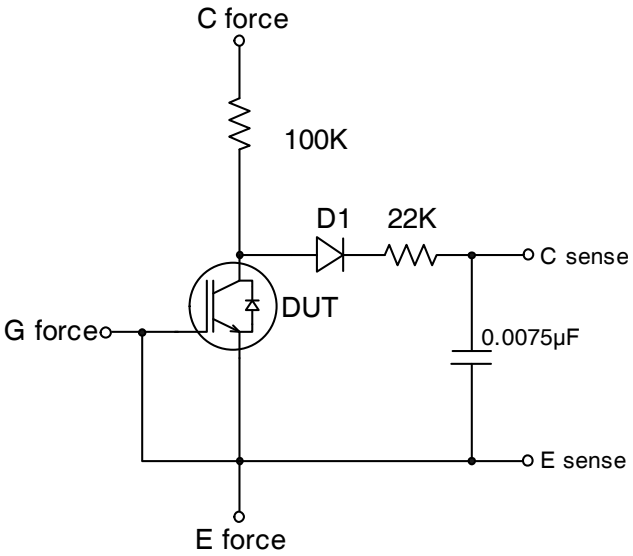


Fig.C.T.6 - BVCES Filter Circuit

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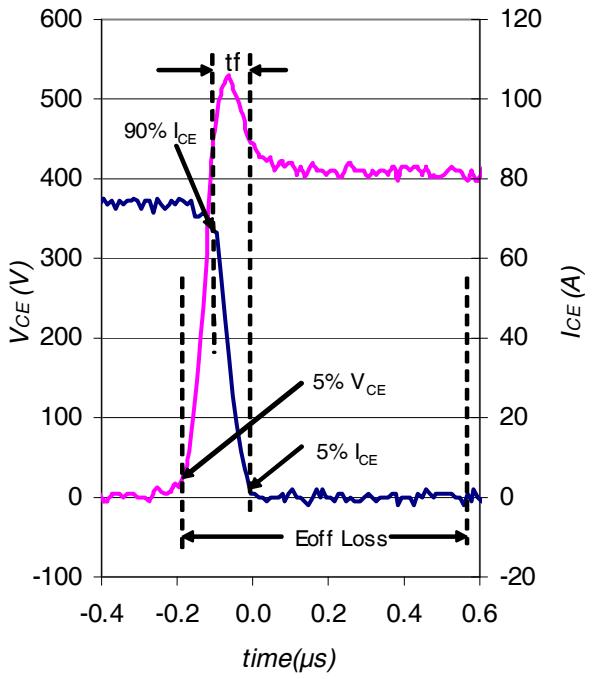


Fig. WF1 - Typ. Turn-off Loss Waveform
@ T_J = 175°C using Fig. CT.4

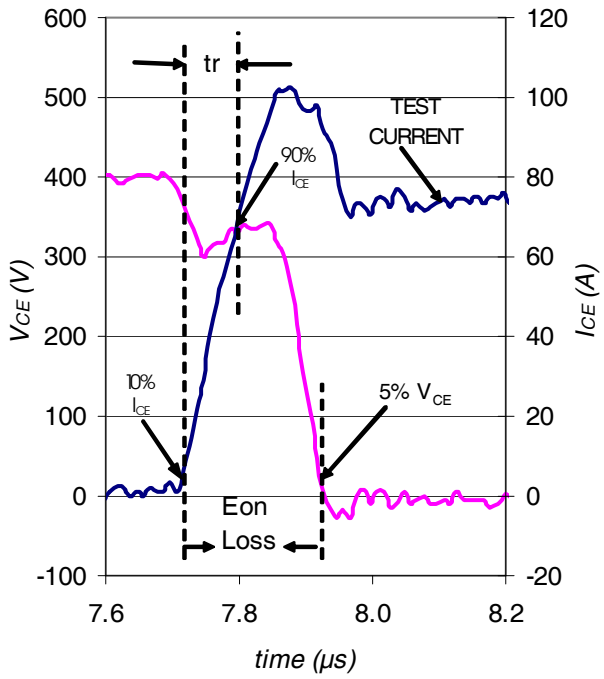


Fig. WF2 - Typ. Turn-on Loss Waveform
@ T_J = 175°C using Fig. CT.4

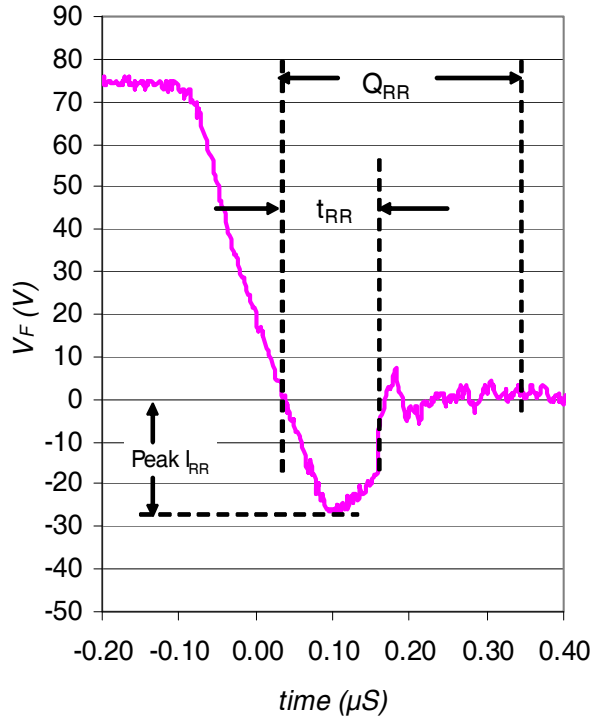


Fig. WF3 - Typ. Diode Recovery Waveform
@ T_J = 175°C using Fig. CT.4

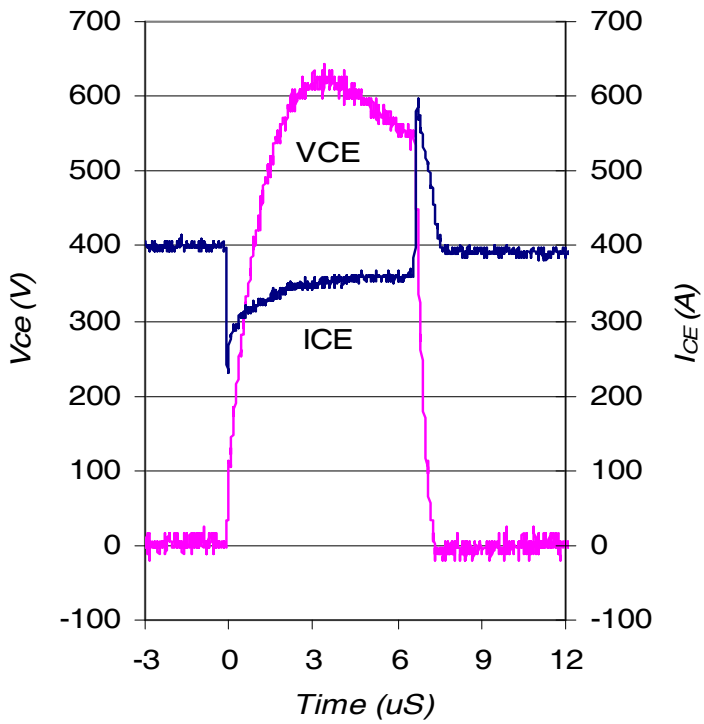
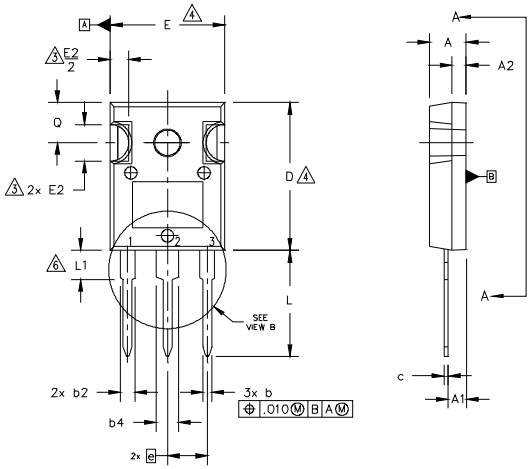


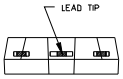
Fig. WF4 - Typ. S.C. Waveform
@ T_J = 25°C using Fig. CT.3

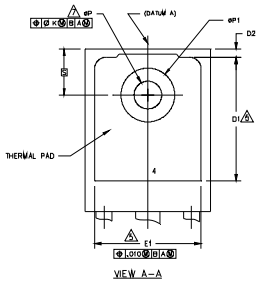
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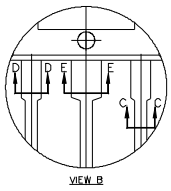
TO-247AC Package Outline

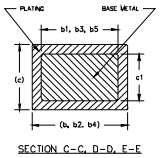
Dimensions are shown in millimeters (inches)











NOTES:

1. DIMENSIONING AND TOLERANCING AS PER ASME Y14.5M 1994.
2. DIMENSIONS ARE SHOWN IN INCHES.
3. CONTOUR OF SLOT OPTIONAL.
4. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
5. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS D1 & E1.
6. LEAD FINISH UNCONTROLLED IN L1.
7. øP TO HAVE A MAXIMUM DRAFT ANGLE OF 1.5 ° TO THE TOP OF THE PART WITH A MAXIMUM HOLE DIAMETER OF .154 INCH.
8. OUTLINE CONFORMS TO JEDEC OUTLINE TO-247AC .

SYMBOL	DIMENSIONS				NOTES
	INCHES		MILLIMETERS		
	MIN.	MAX.	MIN.	MAX.	
A	.183	.209	4.65	5.31	
A1	.087	.102	2.21	2.59	
A2	.059	.098	1.50	2.49	
b	.039	.055	0.99	1.40	
b1	.039	.053	0.99	1.35	
b2	.065	.094	1.65	2.39	
b3	.065	.092	1.65	2.34	
b4	.102	.135	2.59	3.43	
b5	.102	.133	2.59	3.38	
c	.015	.035	0.38	0.89	
c1	.015	.033	0.38	0.84	
D	.776	.815	19.71	20.70	4
D1	.515	–	13.08	–	5
D2	.020	.053	0.51	1.35	
E	.602	.625	15.29	15.87	4
E1	.530	–	13.46	–	
E2	.178	.216	4.52	5.49	
e	.215 BSC		5.46 BSC		
øk	.010		0.25		
L	.559	.634	14.20	16.10	
L1	.146	.169	3.71	4.29	
øP	.140	.144	3.56	3.66	
øP1	–	.291	–	7.39	
Q	.209	.224	5.31	5.69	
S	.217 BSC		5.51 BSC		

LEAD ASSIGNMENTS

HEXFET

1.– GATE
2.– DRAIN
3.– SOURCE
4.– DRAIN

IGBTs, CoPACK

1.– GATE
2.– COLLECTOR
3.– EMITTER
4.– COLLECTOR

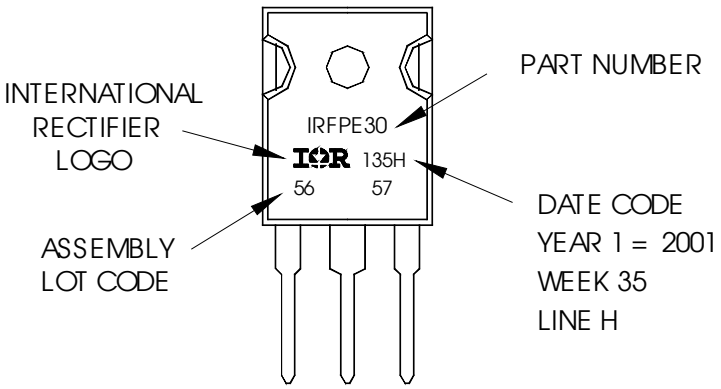
DIODES

1.– ANODE/OPEN
2.– CATHODE
3.– ANODE

TO-247AC Part Marking Information

EXAMPLE: THIS IS AN IRFPE30
WITH ASSEMBLY
LOT CODE 5657
ASSEMBLED ON WW 35, 2001
IN THE ASSEMBLY LINE "H"

Note: "P" in assembly line position
indicates "Lead-Free"



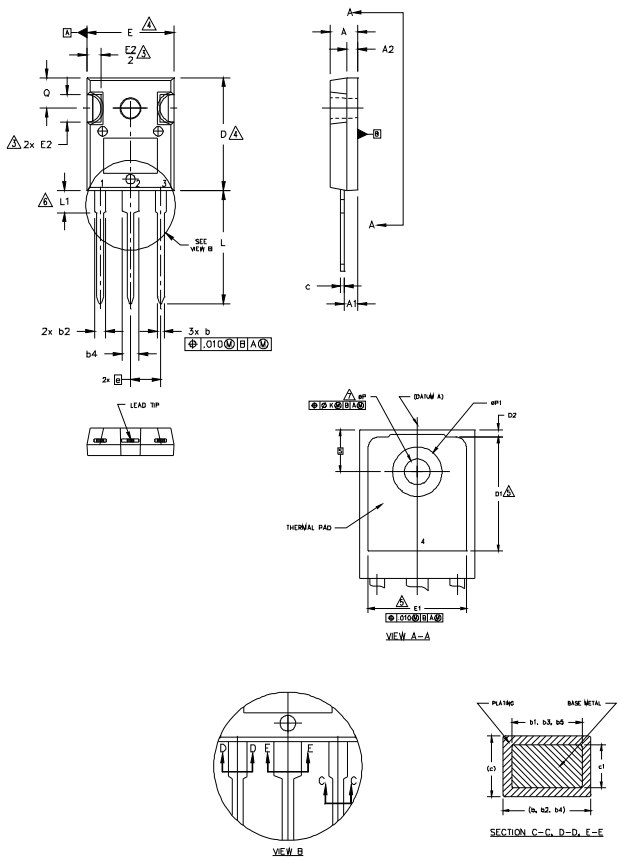
TO-247AC package is not recommended for Surface Mount Application.

Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

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TO-247AD Package Outline

Dimensions are shown in millimeters (inches)



NOTES:

1. DIMENSIONING AND TOLERANCING AS PER ASME Y14.5M 1994.
2. DIMENSIONS ARE SHOWN IN INCHES.
3. CONTOUR OF SLOT OPTIONAL.
4. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
5. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS D1 & E1.
6. LEAD FINISH UNCONTROLLED IN L1.
7. øP TO HAVE A MAXIMUM DRAFT ANGLE OF 1.5 ° TO THE TOP OF THE PART WITH A MAXIMUM HOLE DIAMETER OF .154 INCH.
8. OUTLINE CONFORMS TO JEDEC OUTLINE TO-247AD.

SYMBOL	DIMENSIONS				NOTES
	INCHES		MILLIMETERS		
	MIN.	MAX.	MIN.	MAX.	
A	.183	.209	4.65	5.31	
A1	.087	.102	2.21	2.59	
A2	.059	.098	1.50	2.49	
b	.039	.055	0.99	1.40	
b1	.039	.053	0.99	1.35	
b2	.065	.094	1.65	2.39	
b3	.065	.092	1.65	2.34	
b4	.102	.135	2.59	3.43	
b5	.102	.133	2.59	3.38	
c	.015	.035	0.38	0.89	
c1	.015	.033	0.38	0.84	
D	.776	.815	19.71	20.70	4
D1	.515	–	13.08	–	5
D2	.020	.053	0.51	1.35	
E	.602	.625	15.29	15.87	4
E1	.530	–	13.46	–	
E2	.178	.216	4.52	5.49	
e	.215 BSC		5.46 BSC		
øk	.010		0.25		
L	.780	.827	19.57	21.00	
L1	.146	.169	3.71	4.29	
øP	.140	.144	3.56	3.66	
øP1	–	.291	–	7.39	
Q	.209	.224	5.31	5.69	
S	.217 BSC		5.51 BSC		

LEAD ASSIGNMENTS

HEXFET

1.– GATE
2.– DRAIN
3.– SOURCE
4.– DRAIN

IGBTs, CoPACK

1.– GATE
2.– COLLECTOR
3.– EMITTER
4.– COLLECTOR

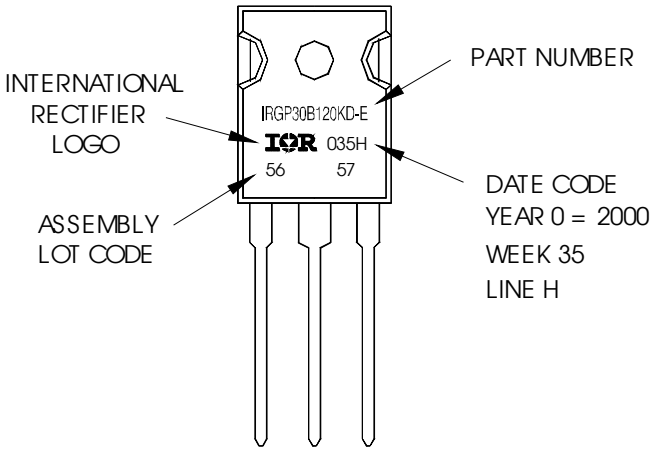
DIODES

1.– ANODE/OPEN
2.– CATHODE
3.– ANODE

TO-247AD Part Marking Information

EXAMPLE: THIS IS AN IRGP30B120KD-E
WITH ASSEMBLY
LOT CODE 5657
ASSEMBLED ON WW 35, 2000
IN THE ASSEMBLY LINE "H"

Note: "P" in assembly line position
indicates "Lead-Free"



TO-247AD package is not recommended for Surface Mount Application.

Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

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Qualification Information†

Qualification Level		Industrial (per International Rectifier's internal guidelines)	
Moisture Sensitivity Level		TO-247AC	N/A
		TO-247AD	N/A
ESD	Human Body Model	Class 3A (+/- 4000V)†† (per JEDEC JESD22-A114)	
	Charged Device Model	Class C5 (+/- 1125V)†† (per JEDEC JESD22-C101)	
RoHS Compliant		Yes	

† Qualification standards can be found at International Rectifier's web site: http://www.irf.com/product-info/reliability

†† Highest passing voltage.

Revision History

Date	Comments
11/14/2014	• Added note ④ to I_{FM} Diode Maximum Forward Current on page 1. • Added note ⑤ to switching losses test condition on page 2.

International
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