S-1142C/D Series

HIGH-WITHSTAND VOLTAGE LOW CURRENT CONSUMPTION LOW DROPOUT CMOS VOLTAGE REGULATOR

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Rev.1.3_00

The S-1142C/D Series, developed by using high-withstand voltage CMOS technology, is a positive voltage regulator with a high-withstand voltage, low current consumption, and high-accuracy output voltage.
The S-1142C/D Series operates at a high maximum operating voltage of 50 V and a low current consumption of 4.0 μA typ. In addition to a built-in low on-resistance transistor which provides a very small dropout voltage and a large output current, this voltage regulator also has a built-in ON / OFF circuit.
An overcurrent protection circuit prevents the load current from exceeding the current capacity of the output transistor, and a built-in thermal shutdown circuit prevents damage caused by heat.
A high heat radiation HSOP-6 package enables high-density mounting.

■ Features

- Output voltage: 2.0 V to 15.0 V, selectable in 0.1 V step
- Input voltage: 3.0 V to 50 V
- Output voltage accuracy: ±1.0% (Tj = +25°C)
  ±3.0% (Tj = –40°C to +105°C)
- Current consumption: During operation: 4.0 μA typ., 9.0 μA max. (Ta = –40°C to +85°C)
  During power-off: 0.1 μA typ., 1.0 μA max. (Ta = –40°C to +85°C)
- Output current: Possible to output 200 mA ($V_{IN} \geq V_{OUT(S)} + 2.0$ V)*1
- Input and output capacitors: A ceramic capacitor of 0.1 μF or more can be used.
- Built-in overcurrent protection circuit: Limits overcurrent of output transistor.
- Built-in thermal shutdown circuit: Prevents damage caused by heat.
- Built-in ON / OFF circuit: Ensures long battery life.
- Operation temperature range: Ta = –40°C to +85°C
- Lead-free (Sn 100%), halogen-free

***1.** Attention should be paid to the power dissipation of the package when the output current is large.

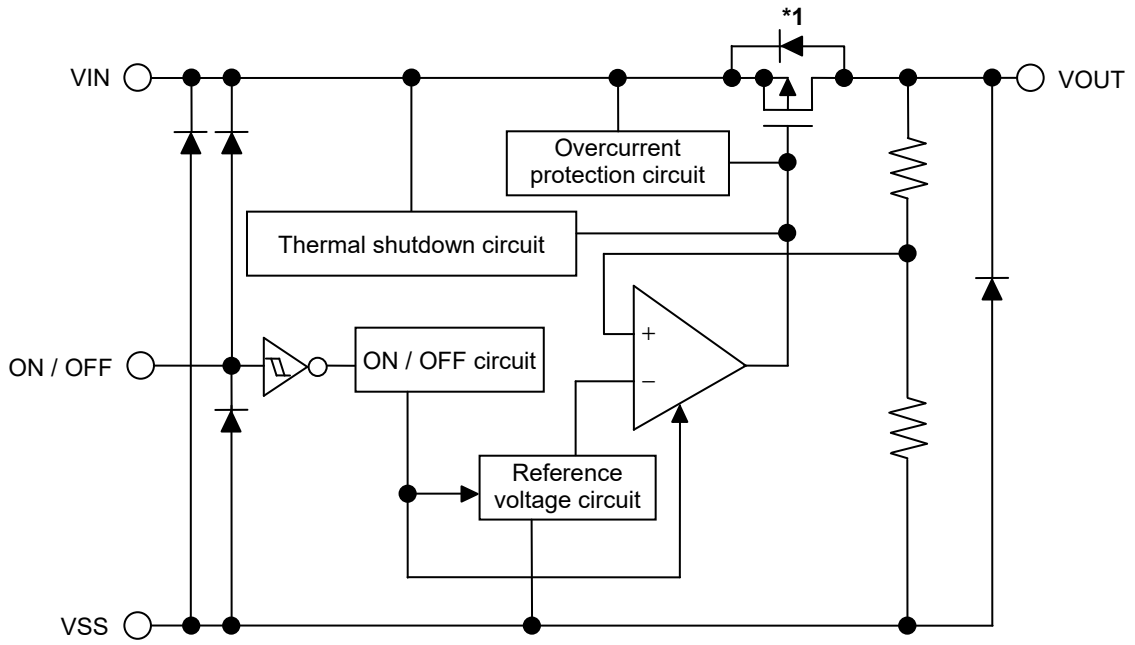
■ Application

- Constant-voltage power supply for home electric appliance

■ Package

- HSOP-6

■ Block Diagram



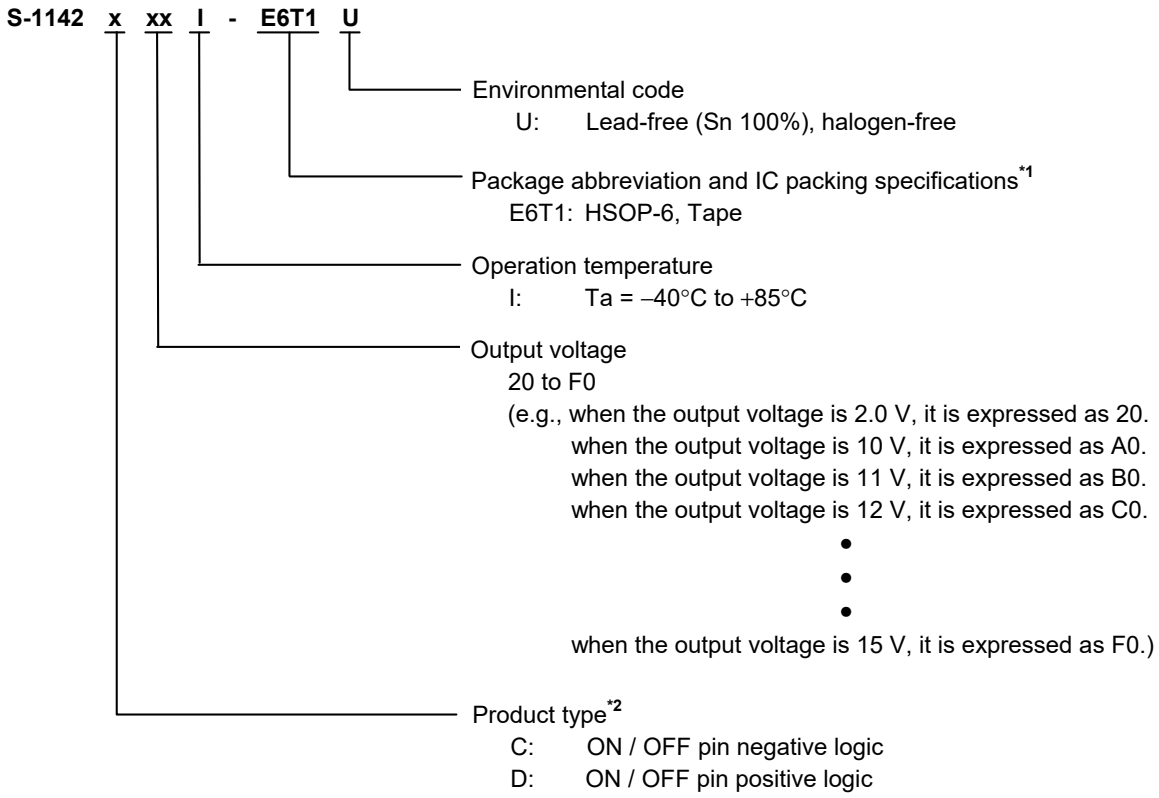
***1.** Parasitic diode

Figure 1

■ Product Name Structure

Users can select the product type and output voltage for the S-1142C/D Series. Refer to "**1. Product name**" regarding the contents of product name, "**2. Package**" regarding the package drawings and "**3. Product name list**" for details of product names.

1. Product name



***1.** Refer to the tape drawing.
***2.** Refer to "**3. ON / OFF pin**" in "**■ Operation**".

2. Package

Table 1 Package Drawing Codes

Package Name	Dimension	Tape	Reel	Land
HSOP-6	FH006-A-P-SD	FH006-A-C-SD	FH006-A-R-SD	FH006-A-L-SD

3. Product name list

Table 2

Output Voltage	HSOP-6
2.0 V ± 1.0%	S-1142D20I-E6T1U
2.5 V ± 1.0%	S-1142D25I-E6T1U
2.7 V ± 1.0%	S-1142D27I-E6T1U
2.8 V ± 1.0%	S-1142D28I-E6T1U
2.85 V ± 1.0%	S-1142D2JI-E6T1U
3.0 V ± 1.0%	S-1142D30I-E6T1U
3.2 V ± 1.0%	S-1142D32I-E6T1U
3.3 V ± 1.0%	S-1142D33I-E6T1U
3.5 V ± 1.0%	S-1142D35I-E6T1U
3.7 V ± 1.0%	S-1142D37I-E6T1U
3.9 V ± 1.0%	S-1142D39I-E6T1U
4.0 V ± 1.0%	S-1142D40I-E6T1U
5.0 V ± 1.0%	S-1142D50I-E6T1U
8.0 V ± 1.0%	S-1142D80I-E6T1U
11.5 V ± 1.0%	S-1142DB5I-E6T1U
12.5 V ± 1.0%	S-1142DC5I-E6T1U
15.0 V ± 1.0%	S-1142DF0I-E6T1U

Remark Please contact our sales office for products with an output voltage other than those listed above or type C products.

■ Pin Configuration

1. HSOP-6

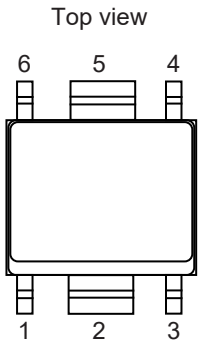


Figure 2

Table 3

Pin No.	Symbol	Description
1	VOUT	Output voltage pin
2	VSS	GND pin
3	ON / OFF	ON / OFF pin
4	NC*1	No connection
5	VSS	GND pin
6	VIN	Input voltage pin

***1.** The NC pin is electrically open.
The NC pin can be connected to the VIN pin or the VSS pin.

■ Absolute Maximum Ratings

Table 4

(Ta = +25°C unless otherwise specified)

Item	Symbol	Absolute Maximum Rating	Unit
Input voltage	V_{IN}	V_{SS} – 0.3 to V_{SS} + 60	V
	$V_{ON / OFF}$	V_{SS} – 0.3 to V_{IN} + 0.3	V
Output voltage	V_{OUT}	V_{SS} – 0.3 to V_{IN} + 0.3	V
Power dissipation	P_D	1900*1	mW
Junction temperature	Tj	–40 to +125	°C
Operation ambient temperature	T_{opr}	–40 to +85	°C
Storage temperature	T_{stg}	–40 to +125	°C

***1.** When mounted on board

[Mounted board]

(1) Board size: 50 mm × 50 mm × t1.6 mm
(2) Board material: Glass epoxy resin (two layers)
(3) Wiring ratio: 50%
(4) Test conditions: When mounted on board (wind speed: 0 m/s)
(5) Land pattern: Refer to the recommended land pattern (drawing code: FH006-A-L-SD)

Caution The absolute maximum ratings are rated values exceeding which the product could suffer physical damage. These values must therefore not be exceeded under any conditions.

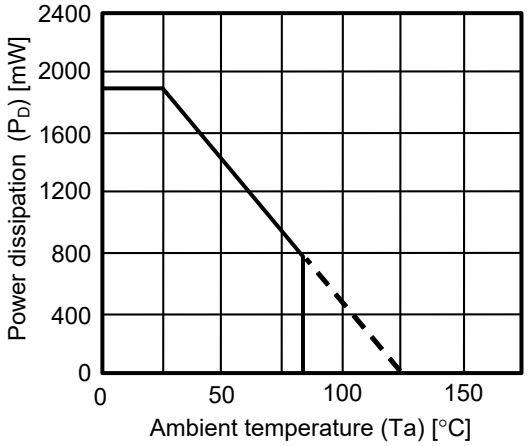


Figure 3 Power Dissipation of Package (When Mounted on Board)

Table 5

Condition	Power Dissipation	Thermal Resistance Value (θj – a)
HSOP-6 (When mounted on board)	1900 mW	53°C/W

Power dissipation of HSOP-6 (reference)

Package power dissipation differs depending on the mounting conditions.

The power dissipation characteristics under the following test conditions should be taken as reference values only.

[Mounted board]

(1) Board size: 50 mm × 50 mm × t1.6 mm
(2) Board material: Glass epoxy resin (two layers)
(3) Wiring ratio: 90%
(4) Test conditions: When mounted on board (wind speed: 0 m/s)
(5) Land pattern: Refer to the recommended land pattern (drawing code: FH006-A-L-SD)

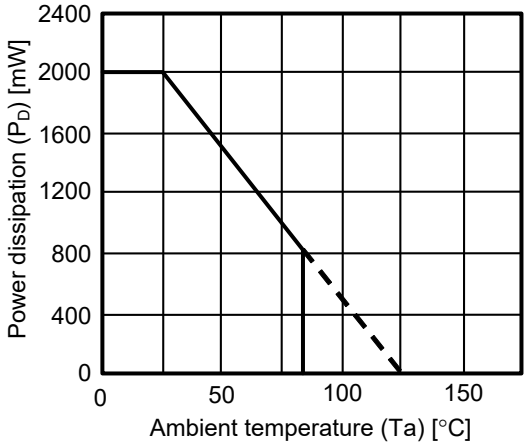


Figure 4 Power Dissipation of Package (When Mounted on Board)

Table 6

Condition	Power Dissipation (Reference)	Thermal Resistance Value (θj – a)
HSOP-6 (When mounted on board)	2000 mW	50°C/W

■ Electrical Characteristics

Table 7

(Tj = –40°C to +125°C, Ta = –40°C to +85°C unless otherwise specified)

Item	Symbol	Condition		Min.	Typ.	Max.	Unit	Test Circuit
Output voltage*1	$V_{OUT(E)}$	$V_{IN} = V_{OUT(S)} + 1.0$ V, I_{OUT} = 30 mA, –40°C ≤ Tj ≤ +105°C		$V_{OUT(S)} \times 0.97$	$V_{OUT(S)}$	$V_{OUT(S)} \times 1.03$	V	1
Output current*2	I_{OUT}	$V_{IN} \geq V_{OUT(S)} + 2.0$ V		200*4	–	–	mA	3
Dropout voltage*3	V_{drop}	I_{OUT} = 100 mA, Ta = +25°C	2.0 V ≤ $V_{OUT(S)}$ < 2.2 V	–	1.0	–	V	1
			2.2 V ≤ $V_{OUT(S)}$ < 2.4 V	–	0.8	–	V	1
			2.4 V ≤ $V_{OUT(S)}$ < 2.6 V	–	0.6	–	V	1
			2.6 V ≤ $V_{OUT(S)}$ < 3.0 V	–	0.45	–	V	1
			3.0 V ≤ $V_{OUT(S)}$ < 3.5 V	–	0.35	–	V	1
			3.5 V ≤ $V_{OUT(S)}$ < 4.0 V	–	0.3	–	V	1
			4.0 V ≤ $V_{OUT(S)}$ < 5.0 V	–	0.27	–	V	1
			5.0 V ≤ $V_{OUT(S)}$ < 7.0 V	–	0.23	–	V	1
			7.0 V ≤ $V_{OUT(S)}$ < 9.0 V	–	0.2	–	V	1
			9.0 V ≤ $V_{OUT(S)}$ ≤ 15.0 V	–	0.18	–	V	1
		I_{OUT} = 200 mA, Ta = +25°C	2.0 V ≤ $V_{OUT(S)}$ < 2.2 V	–	1.12	–	V	1
			2.2 V ≤ $V_{OUT(S)}$ < 2.4 V	–	1.02	–	V	1
			2.4 V ≤ $V_{OUT(S)}$ < 2.6 V	–	0.92	–	V	1
			2.6 V ≤ $V_{OUT(S)}$ < 3.0 V	–	0.82	–	V	1
			3.0 V ≤ $V_{OUT(S)}$ < 3.5 V	–	0.72	–	V	1
			3.5 V ≤ $V_{OUT(S)}$ < 4.0 V	–	0.62	–	V	1
			4.0 V ≤ $V_{OUT(S)}$ < 5.0 V	–	0.55	–	V	1
			5.0 V ≤ $V_{OUT(S)}$ < 7.0 V	–	0.5	–	V	1
			7.0 V ≤ $V_{OUT(S)}$ < 9.0 V	–	0.45	–	V	1
			9.0 V ≤ $V_{OUT(S)}$ ≤ 15.0 V	–	0.4	–	V	1
Line regulation	$\frac{\Delta V_{OUT1}}{\Delta V_{IN} \bullet V_{OUT}}$	$V_{OUT(S)} + 1.0$ V ≤ V_{IN} ≤ 30 V, I_{OUT} = 30 mA		–	0.05	0.3	%/V	1
Load regulation	ΔV_{OUT2}	$V_{IN} = V_{OUT(S)} + 1.0$ V, 2.0 V ≤ $V_{OUT(S)}$ < 5.1 V, 0.1 mA ≤ I_{OUT} ≤ 40 mA		–	20	40	mV	1
		$V_{IN} = V_{OUT(S)} + 1.0$ V, 5.1 V ≤ $V_{OUT(S)}$ < 12.1 V, 0.1 mA ≤ I_{OUT} ≤ 40 mA		–	20	60	mV	1
		$V_{IN} = V_{OUT(S)} + 1.0$ V, 12.1 V≤ $V_{OUT(S)}$ ≤ 15.0 V, 0.1 mA ≤ I_{OUT} ≤ 40 mA		–	20	80	mV	1
Current consumption during operation	I_{SS1}	$V_{IN} = V_{OUT(S)} + 1.0$ V, ON / OFF pin = ON, no load		–	4.0	9.0	µA	2
Current consumption during power-off	I_{SS2}	$V_{IN} = V_{OUT(S)} + 1.0$ V, ON / OFF pin = OFF, no load		–	0.1	1.0	µA	2
Input voltage	V_{IN}	–		3.0	–	50	V	–
ON / OFF pin input voltage "H"	V_{SH}	$V_{IN} = V_{OUT(S)} + 1.0$ V, R_L = 1.0 kΩ, determined by V_{OUT} output level		1.5	–	–	V	4
ON / OFF pin input voltage "L"	V_{SL}	$V_{IN} = V_{OUT(S)} + 1.0$ V, R_L = 1.0 kΩ, determined by V_{OUT} output level		–	–	0.3	V	4
ON / OFF pin input current "H"	I_{SH}	$V_{IN} = V_{OUT(S)} + 1.0$ V, $V_{ON / OFF} = V_{OUT(S)} + 1.0$ V		–0.1	–	0.1	µA	4
ON / OFF pin input current "L"	I_{SL}	$V_{IN} = V_{OUT(S)} + 1.0$ V, $V_{ON / OFF}$ = 0 V		–0.1	–	0.1	µA	4
Ripple rejection	|RR|	$V_{IN} = V_{OUT(S)} + 1.0$ V, f = 100 Hz, ΔV_{rip} = 0.5 Vrms, I_{OUT} = 30 mA, Ta = +25°C	2.0 V ≤ $V_{OUT(S)}$ < 2.3 V	–	50	–	dB	5
			2.3 V ≤ $V_{OUT(S)}$ < 3.6 V	–	45	–	dB	5
			3.6 V ≤ $V_{OUT(S)}$ < 6.1 V	–	40	–	dB	5
			6.1 V ≤ $V_{OUT(S)}$ < 10.1 V	–	35	–	dB	5
			10.1 V ≤ $V_{OUT(S)}$ ≤ 15.0 V	–	30	–	dB	5
Short-circuit current	I_{short}	$V_{IN} = V_{OUT(S)} + 1.0$ V, ON / OFF pin = ON, V_{OUT} = 0 V, Ta = +25°C		–	80	–	mA	3
Thermal shutdown detection temperature	T_{SD}	Junction temperature		–	150	–	°C	–
Thermal shutdown release temperature	T_{SR}	Junction temperature		–	125	–	°C	–

***1.** $V_{OUT(S)}$: Set output voltage
$V_{OUT(E)}$: Actual output voltage
The output voltage when fixing I_{OUT} (= 30 mA) and inputting $V_{OUT(S)}$ + 1.0 V

***2.** The output current at which the output voltage becomes 95% of $V_{OUT(E)}$ after gradually increasing the output current.

***3.** $V_{drop} = V_{IN1} - (V_{OUT3} \times 0.98)$
V_{OUT3} is the output voltage when $V_{IN} = V_{OUT(S)} + 2.0$ V, and I_{OUT} = 100 mA or 200 mA.
V_{IN1} is the input voltage at which the output voltage becomes 98% of V_{OUT3} after gradually decreasing the input voltage.

***4.** The output current can be at least this value.
Due to limitation of the package power dissipation, this value may not be satisfied. Attention should be paid to the power dissipation of the package when the output current is large.
This specification is guaranteed by design.

■ Test Circuits

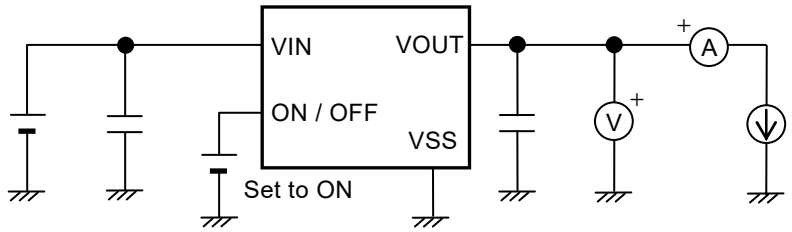


Figure 5 Test Circuit 1

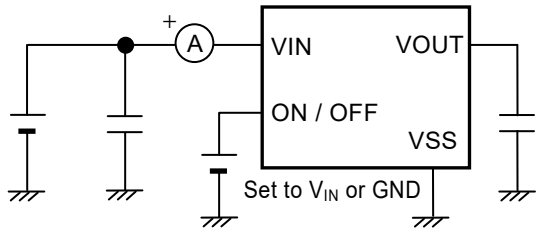


Figure 6 Test Circuit 2

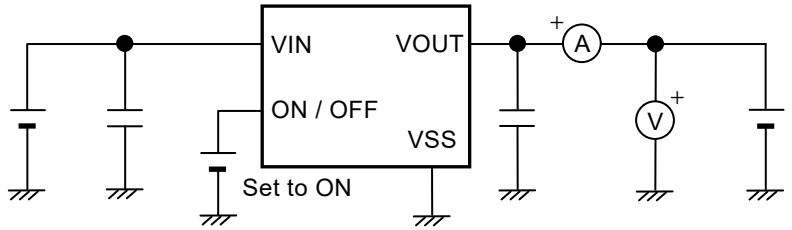


Figure 7 Test Circuit 3

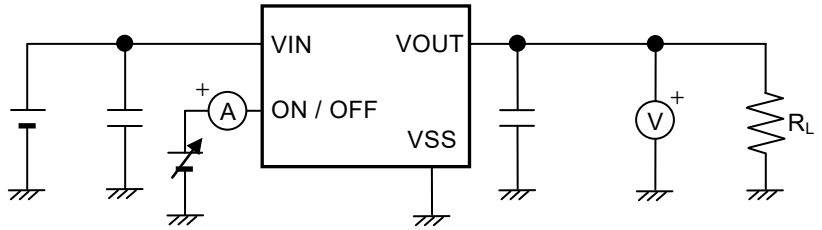


Figure 8 Test Circuit 4

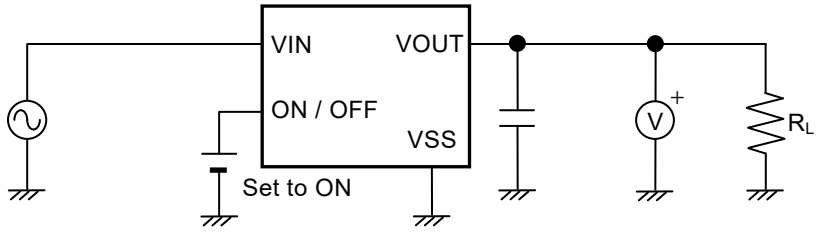
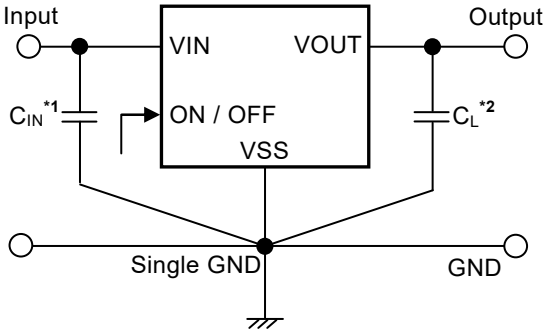


Figure 9 Test Circuit 5

■ Standard Circuit



***1.** C_{IN} is a capacitor for stabilizing the input.
***2.** A ceramic capacitor of 0.1 μF or more can be used as C_L.

Figure 10

Caution The above connection diagram and constants will not guarantee successful operation. Perform thorough evaluation using an actual application to set the constants.

■ Condition of Application

Input capacitor (C_{IN}): 0.1 μF or more
Output capacitor (C_L): 0.1 μF or more

Caution Generally a series regulator may cause oscillation, depending on the selection of external parts. Confirm that no oscillation occurs in the application for which the above capacitors are used.

■ Selection of Input and Output Capacitors (C_{IN}, C_L)

The S-1142C/D Series requires an output capacitor between the VOUT pin and the VSS pin for phase compensation. Operation is stabilized by a ceramic capacitor with an output capacitance of 0.1 μF or more over the entire temperature range. When using an OS capacitor, a tantalum capacitor, or an aluminum electrolytic capacitor, the capacitance must be 0.1 μF or more.
The values of output overshoot and undershoot, which are transient response characteristics, vary depending on the value of the output capacitor.
The required value of capacitance for the input capacitor differs depending on the application.
Set the value for input capacitor (C_{IN}) and output capacitor (C_L) as follows.

$C_{IN} \geq 0.1$ μF
$C_L \geq 0.1$ μF

Caution Define the capacitance of C_{IN} and C_L by sufficient evaluation including the temperature characteristics under the actual usage conditions.

■ Explanation of Terms

1. Low dropout voltage regulator

This voltage regulator has the low dropout voltage due to its built-in low on-resistance transistor.

2. Output voltage (V_{OUT})

The accuracy of the output voltage is ensured at ±3.0% under specified conditions of fixed input voltage*1, fixed output current, and fixed temperature.

***1.** Differs depending on the product.

Caution If the above conditions change, the output voltage value may vary and exceed the accuracy range of the output voltage. Refer to "■ Electrical Characteristics" and "■ Characteristics (Typical Data)" for details.

3. Line regulation $\left(\frac{\Delta V_{OUT1}}{\Delta V_{IN} \bullet V_{OUT}}\right)$

Indicates the dependency of the output voltage against the input voltage. That is, the value shows how much the output voltage changes due to a change in the input voltage after fixing output current constant.

4. Load regulation (ΔV_{OUT2})

Indicates the dependency of the output voltage against the output current. That is, the value shows how much the output voltage changes due to a change in the output current after fixing input voltage constant.

5. Dropout voltage (V_{drop})

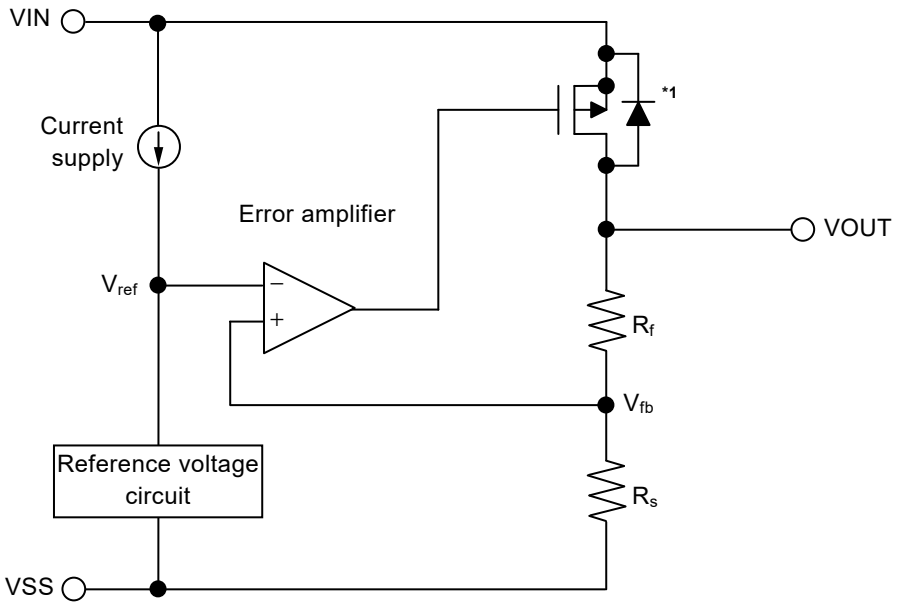
Indicates the difference between input voltage (V_{IN1}) and the output voltage when; decreasing input voltage (V_{IN}) gradually until the output voltage has dropped out to the value of 98% of output voltage (V_{OUT3}), which is at V_{IN} = $V_{OUT(S)}$ + 2.0 V.

$V_{drop} = V_{IN1} - (V_{OUT3} \times 0.98)$

■ Operation

1. Basic operation

Figure 11 shows the block diagram of the S-1142C/D Series.
The error amplifier compares the reference voltage (V_{ref}) with feedback voltage (V_{fb}), which is the output voltage resistance-divided by feedback resistors (R_s and R_f). It supplies the gate voltage necessary to maintain the constant output voltage which is not influenced by the input voltage and temperature change, to the output transistor.



***1.** Parasitic diode

Figure 11

2. Output transistor

In the S-1142C/D Series, a low on-resistance P-channel MOS FET is used as the output transistor.
Be sure that V_{OUT} does not exceed $V_{IN} + 0.3$ V to prevent the voltage regulator from being damaged due to reverse current flowing from the VOUT pin through a parasitic diode to the VIN pin, when the potential of V_{OUT} became higher than V_{IN}.

3. ON / OFF pin

This pin starts and stops the regulator.

When the ON / OFF pin is set to OFF level, the entire internal circuit stops operating, and the built-in P-channel MOS FET output transistor between the VIN pin and the VOUT pin is turned off, reducing current consumption significantly. The VOUT pin is set to the V_{SS} level by the internal dividing resistor of several MΩ between the VOUT pin and the VSS pin.
Note that the current consumption increases when a voltage of 0.3 V to V_{IN} – 0.3 V is applied to the ON / OFF pin.
The ON / OFF pin is configured as shown in **Figure 12**. Since the ON / OFF pin is neither pulled down nor pulled up internally, do not use it in the floating status. When not using the ON / OFF pin, connect it to the VSS pin in the product C type, and connect it to the VIN pin in the D type.

Table 8

Product Type	ON / OFF Pin	Internal Circuit	VOUT Pin Voltage	Current Consumption
C	"L": ON	Operate	Set value	I_{SS1}
C	"H": OFF	Stop	V_{SS} level	I_{SS2}
D	"L": OFF	Stop	V_{SS} level	I_{SS2}
D	"H": ON	Operate	Set value	I_{SS1}

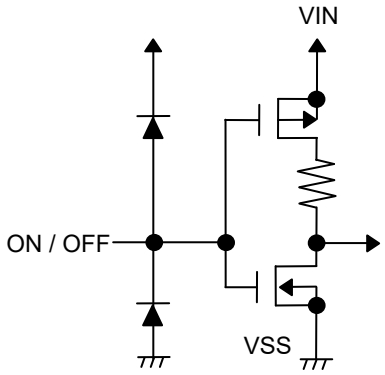


Figure 12

4. Overcurrent protection circuit

The S-1142C/D Series includes an overcurrent protection circuit having the characteristics shown in "**1. Output voltage vs. Output current (When load current increases) (Ta = +25°C)"** in "**■ Characteristics (Typical Data)**", in order to protect the output transistor against an excessive output current and short circuiting between the VOUT pin and the VSS pin. The current when the output pin is short-circuited (I_{short}) is internally set at approx. 80 mA typ., and the normal value is restored for the output voltage, if releasing a short circuit once.

Caution This overcurrent protection circuit does not work as for thermal protection. If this IC long keeps short circuiting inside, pay attention to the conditions of input voltage and load current so that, under the usage conditions including short circuit, the loss of the IC will not exceed power dissipation of the package.

5. Thermal shutdown circuit

The S-1142C/D Series has a thermal shutdown circuit to protect the device from damage due to overheat. When the junction temperature rises to 150°C typ., the thermal shutdown circuit operates to stop regulating. When the junction temperature drops to 125°C typ., the thermal shutdown circuit is released to restart regulating.
Due to self-heating of the S-1142C/D Series, if the thermal shutdown circuit starts operating, it stops regulating so that the output voltage drops. When regulation stops, the S-1142C/D Series does not itself generate heat and the IC's temperature drops. When the temperature drops, the thermal shutdown circuit is released to restart regulating, thus this IC generates heat again. Repeating this procedure makes the waveform of the output voltage into a pulse-like form. Stop or restart of regulation continues unless decreasing either or both of the input voltage and the output current in order to reduce the internal power consumption, or decreasing the ambient temperature.

Table 9

Thermal Shutdown Circuit	VOUT Pin Voltage
Operate: 150°C typ.*1	V_{SS} level
Release: 125°C typ.*1	Set value

***1.** Junction temperature

6. Overshoot of output voltage

Overshoot of output voltage occurs depending on the condition such as the rising speed of input voltage (V_{IN}). Overshoot voltage is the difference between the maximum value of output voltage generated by the fluctuation of V_{IN} and the actual output voltage ($V_{OUT(E)}$) value.

6. 1 At normal operation

As shown in **Figure 13**, V_{gs} is the voltage difference between V_{IN} and gate voltage of output driver.
The error amplifier controls V_{gs} in order to keep the output voltage constant depending on the fluctuation of V_{IN} and the output load.

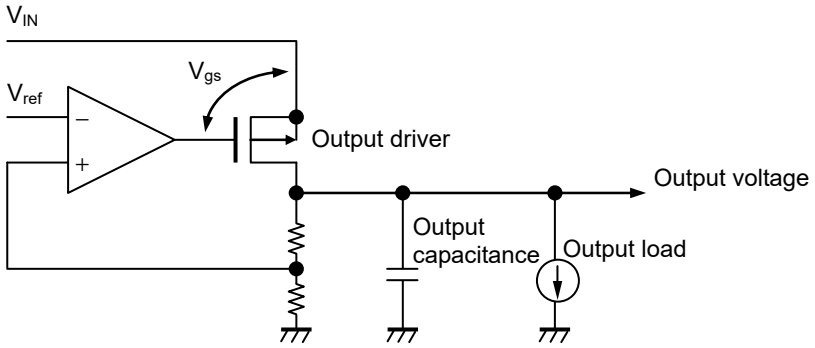


Figure 13 Circuit Diagram

6. 2 Occurrence of overshoot

If V_{IN} voltage rises at a fast speed, V_{gs} may become large when gate voltage of output driver can not follow the speed of V_{IN}. When V_{gs} becomes large, the current supplied from output driver is increased transiently. Thereby, output voltage rises, and then overshoot occurs.

Note that overshoot voltage is greatly affected by the following use conditions or temperature, etc.

- When V_{IN} rises in the range of 2.0 V to $V_{OUT(E)}$.
- When the rising speed of V_{IN} is fast.
- When the output capacitance is small.
- When the output load is small.

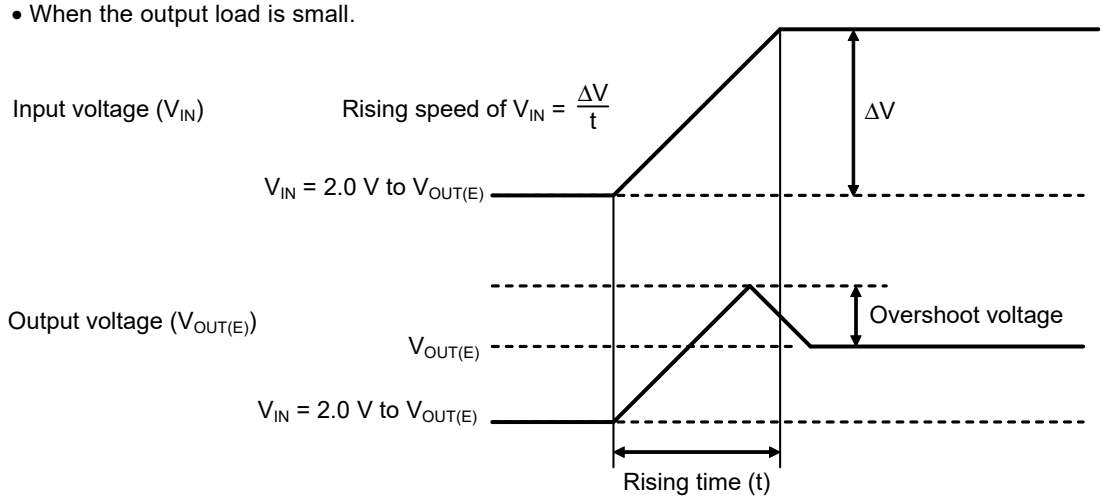


Figure 14 V_{IN} and Overshoot Voltage

Caution Under the following conditions, overshoot voltage tends to become larger especially.
- **When V_{IN} rises from around 98% of $V_{OUT(E)}$.**
- **When the rising speed of V_{IN} is 200 mV/µs or more.**

■ Precautions

- Wiring patterns for the VIN pin, the VOUT pin and GND should be designed so that the impedance is low. When mounting an output capacitor between the VOUT pin and the VSS pin (C_L) and a capacitor for stabilizing the input between the VIN pin and the VSS pin (C_{IN}), the distance from the capacitors to these pins should be as short as possible.
- Note that generally the output voltage may increase when a series regulator is used at low load current (0.1 mA or less).
- Note that generally the output voltage may increase due to the leakage current from an output driver when a series regulator is used at high temperature.
- Note that the output voltage may increase due to the leakage current from an output driver even if the ON / OFF pin is at OFF level when a series regulator is used at high temperature.
- Generally a series regulator may cause oscillation, depending on the selection of external parts. The following conditions are recommended for the S-1142C/D Series. However, be sure to perform sufficient evaluation under the actual usage conditions for selection, including evaluation of temperature characteristics. Refer to "**6. Example of equivalent series resistance vs. Output current characteristics (Ta = +25°C)**" in "**■ Reference Data**" for the equivalent series resistance (R_{ESR}) of the output capacitor.

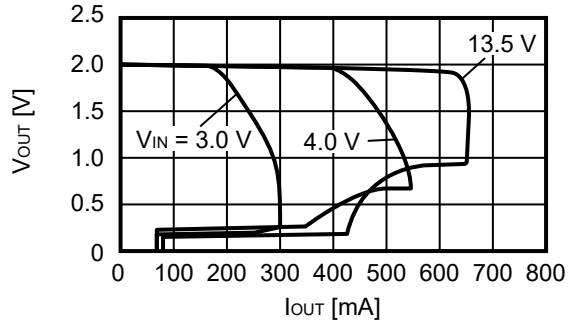
  Input capacitor (C_{IN}): 0.1 μF or more
  Output capacitor (C_L): 0.1 μF or more

- The voltage regulator may oscillate when the impedance of the power supply is high and the input capacitance is small or an input capacitor is not connected.
- Sufficiently evaluate the output voltage fluctuations caused by the power supply or the load fluctuations with the actual device.
- Overshoot may occur in the output voltage momentarily if the voltage is rapidly raised at power-on or when the power supply fluctuates. Sufficiently evaluate the output voltage at power-on with the actual device.
- The application conditions for the input voltage, the output voltage, and the load current should not exceed the package power dissipation.
- Do not apply an electrostatic discharge to this IC that exceeds the performance ratings of the built-in electrostatic protection circuit.
- In determining the output current, attention should be paid to the output current value specified in **Table 7** in "**■ Electrical Characteristics**" and footnote ***4** of the table.
- ABLIC Inc. claims no responsibility for any disputes arising out of or in connection with any infringement by products including this IC of patents owned by a third party.

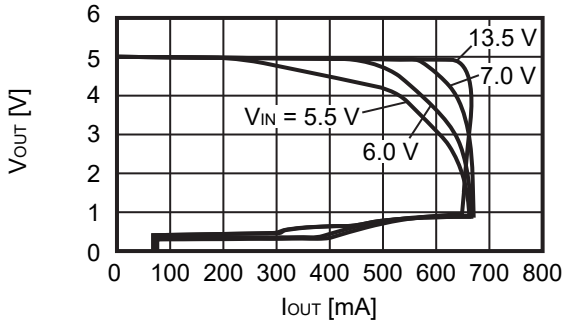
■ Characteristics (Typical Data)

1. Output voltage vs. Output current (When load current increases) (Ta = +25°C)

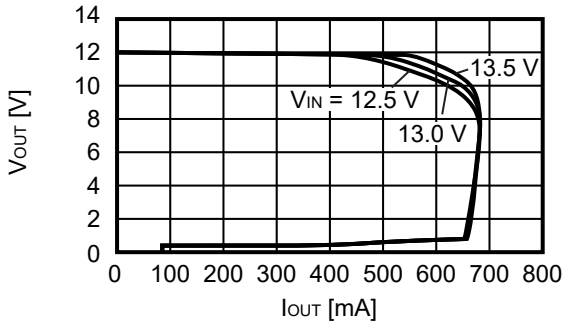
1. 1 V_{OUT} = 2.0 V



1. 2 V_{OUT} = 5.0 V



1. 3 V_{OUT} = 12.0 V

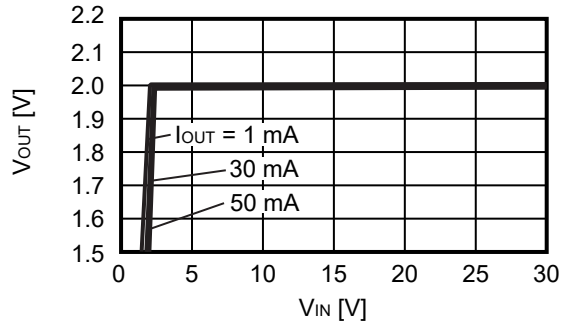


Remark In determining the output current, attention should be paid to the following.

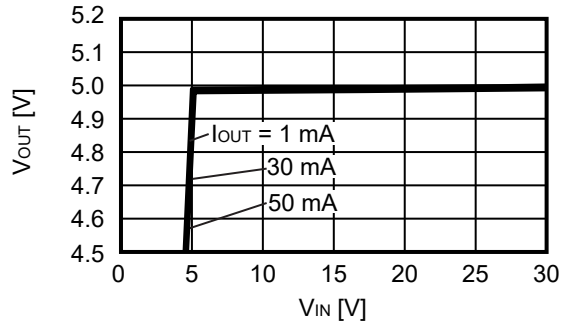
1. The minimum output current value and footnote ***4** of **Table 7** in "■ **Electrical Characteristics**"
2. Power dissipation of the package

2. Output voltage vs. Input voltage (Ta = +25°C)

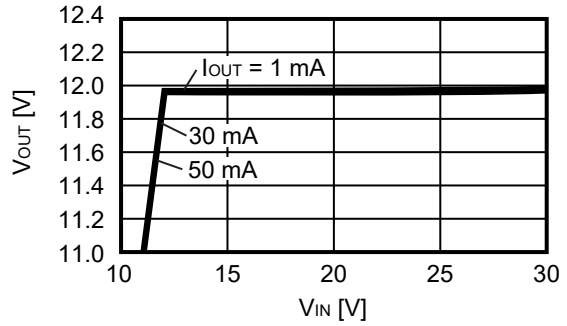
2. 1 V_{OUT} = 2.0 V



2. 2 V_{OUT} = 5.0 V

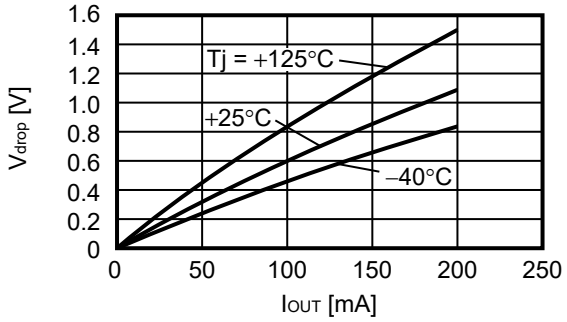


2. 3 V_{OUT} = 12.0 V

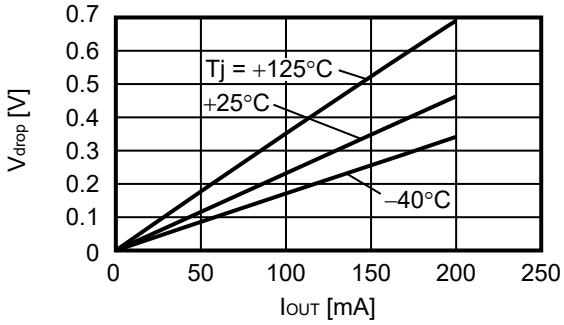


3. Dropout voltage vs. Output current

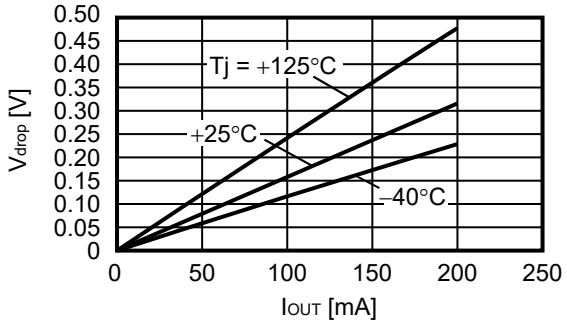
3. 1 V_{OUT} = 2.0 V



3. 2 V_{OUT} = 5.0 V

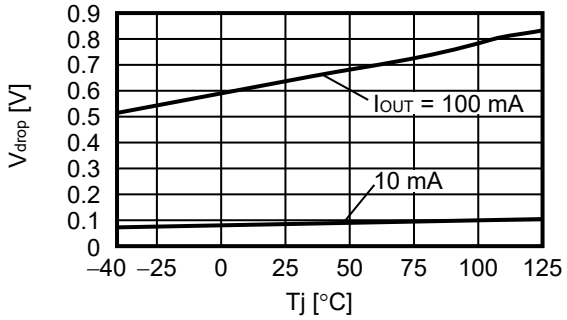


3. 3 V_{OUT} = 12.0 V

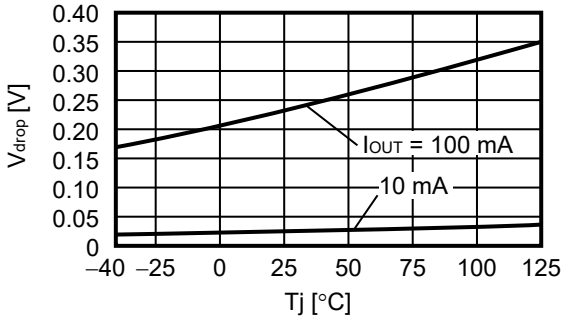


4. Dropout voltage vs. Temperature

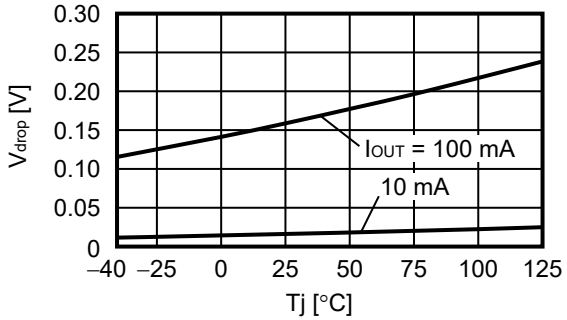
4. 1 V_{OUT} = 2.0 V



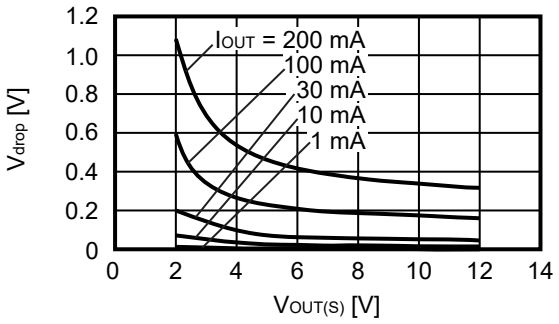
4. 2 V_{OUT} = 5.0 V



4. 3 V_{OUT} = 12.0 V

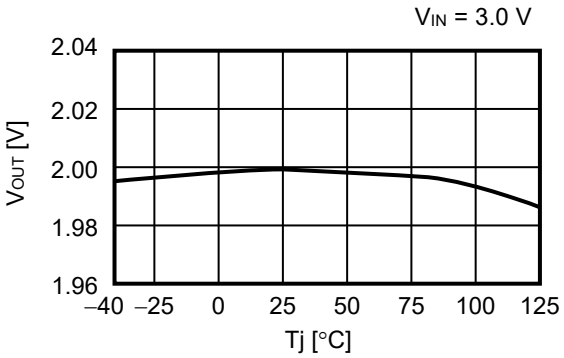


5. Dropout voltage vs. Set output voltage (Tj = +25°C)

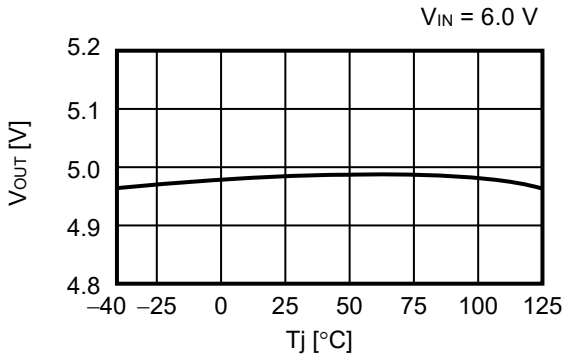


6. Output voltage vs. Temperature

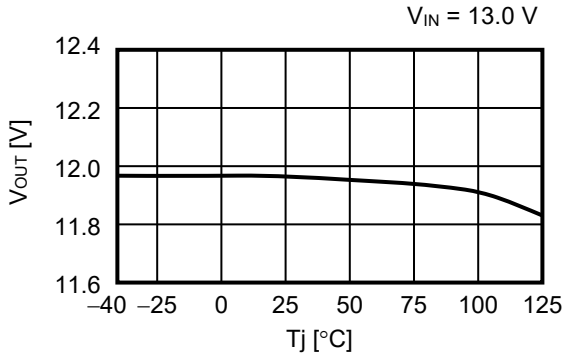
6. 1 V_{OUT} = 2.0 V



6. 2 V_{OUT} = 5.0 V

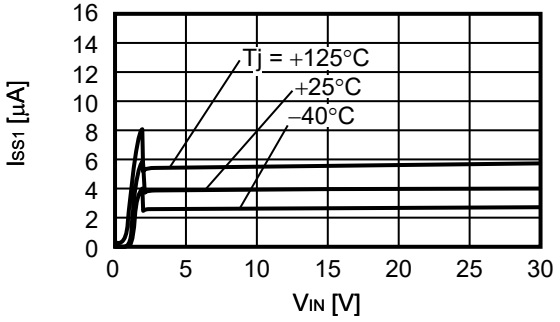


6. 3 V_{OUT} = 12.0 V

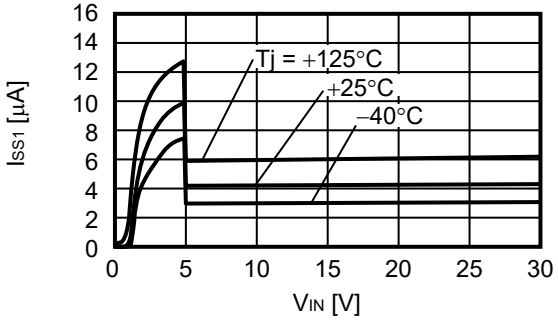


7. Current consumption during operation vs. Input voltage (When ON / OFF pin is ON, no load)

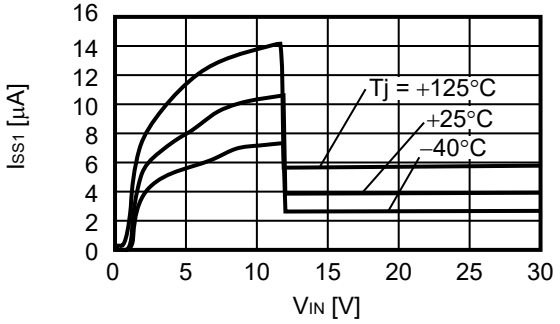
7. 1 V_{OUT} = 2.0 V



7. 2 V_{OUT} = 5.0 V

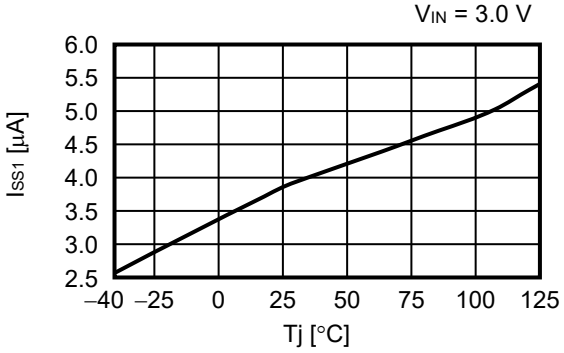


7. 3 V_{OUT} = 12.0 V

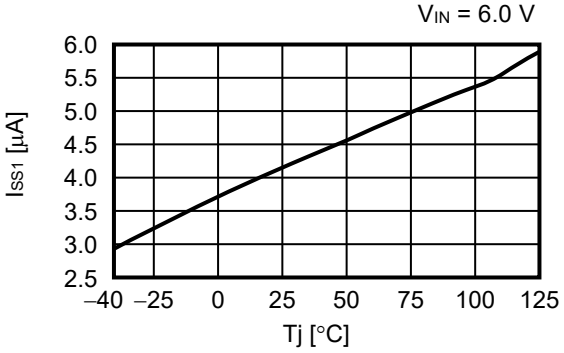


8. Current consumption during operation vs. Temperature

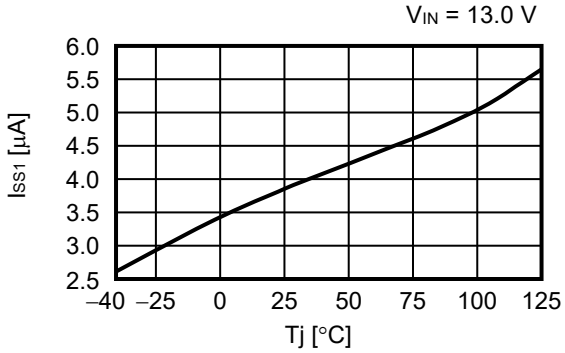
8. 1 V_{OUT} = 2.0 V



8. 2 V_{OUT} = 5.0 V

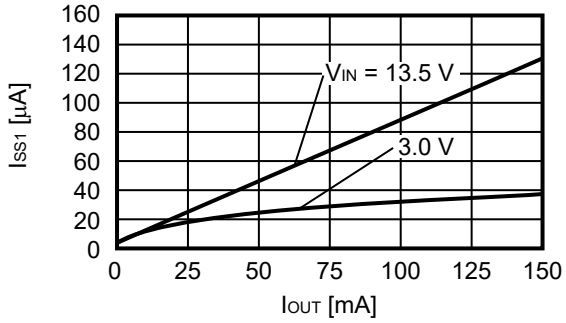


8. 3 V_{OUT} = 12.0 V

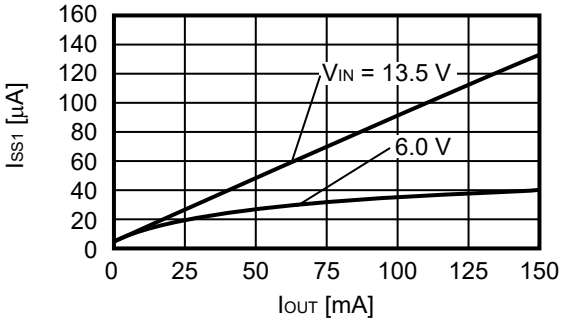


9. Current consumption during operation vs. Output current (Ta = +25°C)

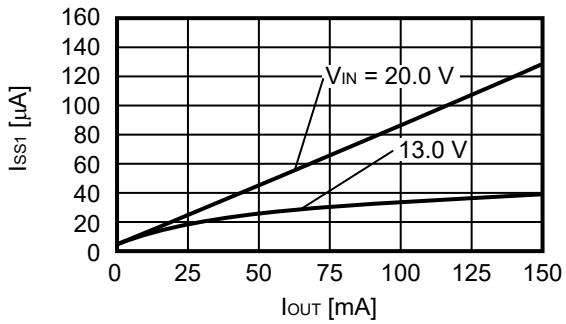
9. 1 V_{OUT} = 2.0 V



9. 2 V_{OUT} = 5.0 V

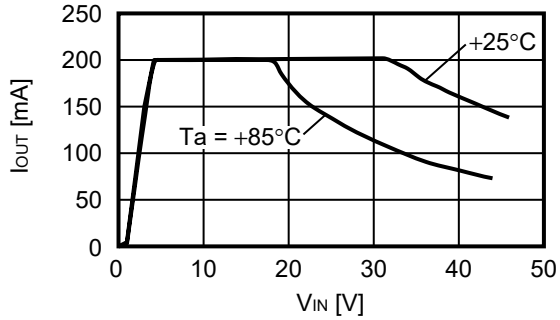


9. 3 V_{OUT} = 12.0 V

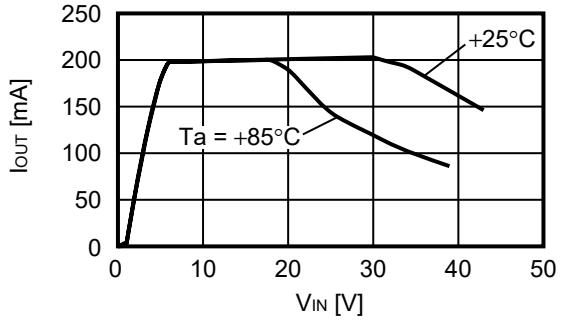


10. Output current vs. Input voltage*1

10. 1 V_{OUT} = 3.3 V



10. 2 V_{OUT} = 5.0 V



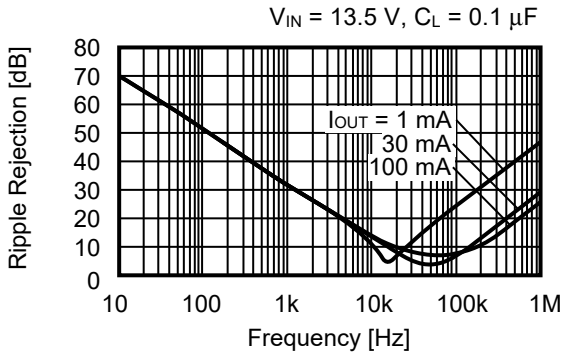
***1.** When mounted on board

[Mounted board]

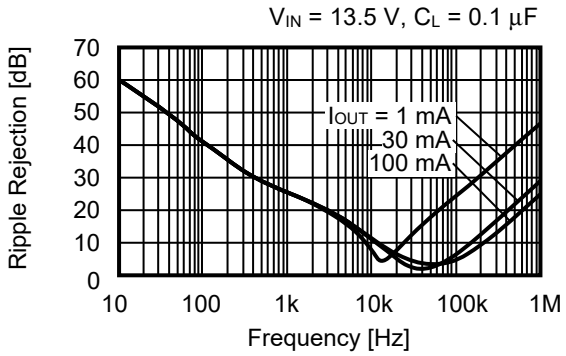
(1) Board size: 50 mm × 50 mm × t1.6 mm
(2) Board material: Glass epoxy resin (two layers)
(3) Wiring ratio: Surface approx. 75%, reverse side approx. 90%
(4) Through hole: Diameter 0.5 mm × 24

11. Ripple rejection (Ta = +25°C)

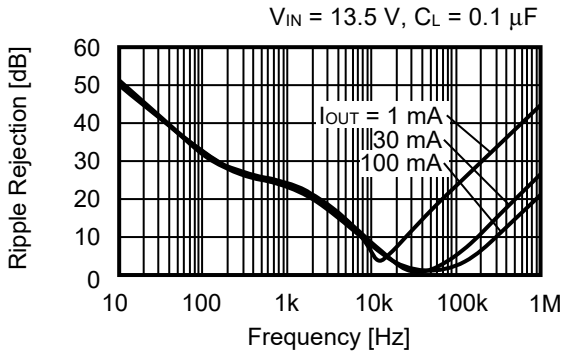
11. 1 V_{OUT} = 2.0 V



11. 2 V_{OUT} = 5.0 V



11. 3 V_{OUT} = 12.0 V

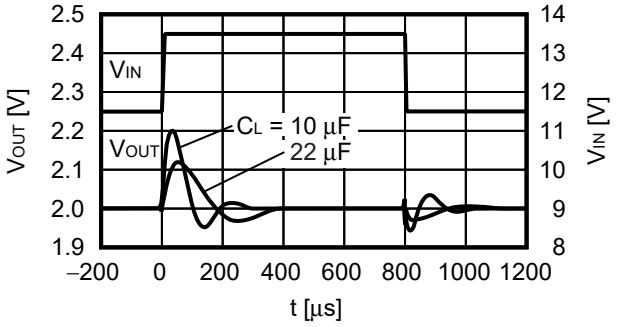


■ Reference Data

1. Characteristics of input transient response (Ta = +25°C)

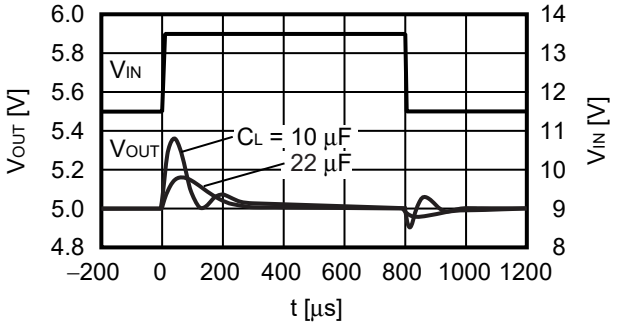
1. 1 V_{OUT} = 2.0 V

I_{OUT} = 30 mA, C_{IN} = 0.1 µF, V_{IN} = 11.5 V ↔ 13.5 V, t_r = t_f = 5.0 µs



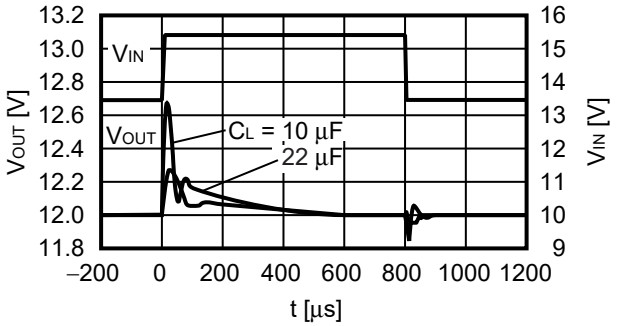
1. 2 V_{OUT} = 5.0 V

I_{OUT} = 30 mA, C_{IN} = 0.1 µF, V_{IN} = 11.5 V ↔ 13.5 V, t_r = t_f = 5.0 µs



1. 3 V_{OUT} = 12.0 V

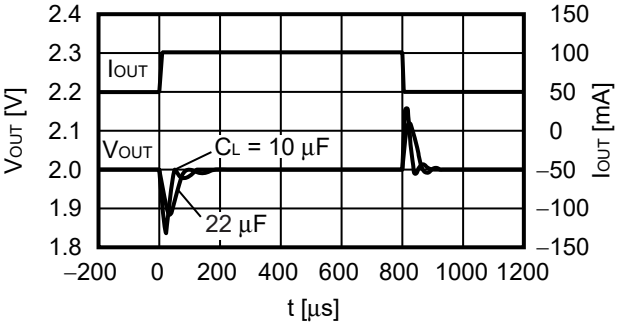
I_{OUT} = 30 mA, C_{IN} = 0.1 µF, V_{IN} = 13.5 V ↔ 15.5 V, t_r = t_f = 5.0 µs



2. Characteristics of load transient response (Ta = +25°C)

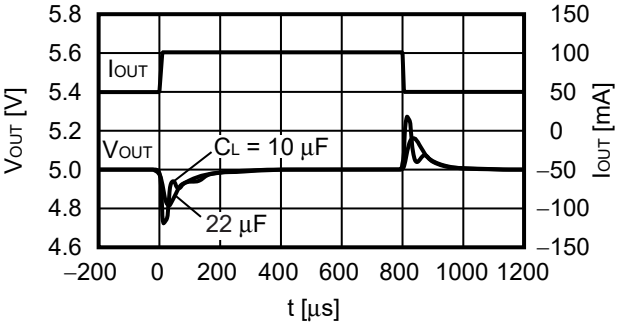
2. 1 V_{OUT} = 2.0 V

V_{IN} = 13.5 V, C_{IN} = 0.1 µF, I_{OUT} = 50 mA ↔ 100 mA



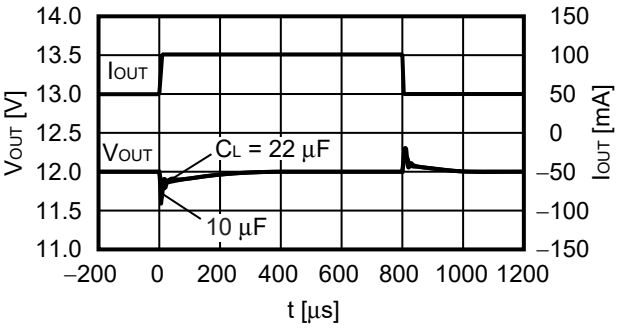
2. 2 V_{OUT} = 5.0 V

V_{IN} = 13.5 V, C_{IN} = 0.1 µF, I_{OUT} = 50 mA ↔ 100 mA



2. 3 V_{OUT} = 12.0 V

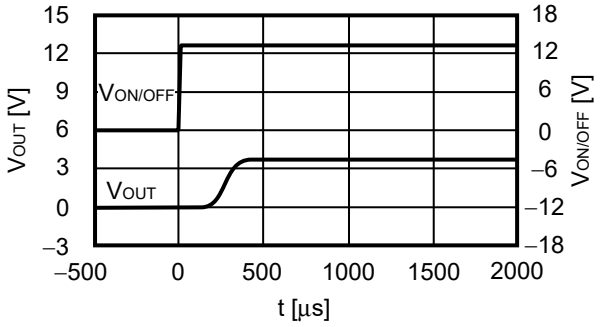
V_{IN} = 13.5 V, C_{IN} = 0.1 µF, I_{OUT} = 50 mA ↔ 100 mA



3. Transient response characteristics of ON / OFF pin (Ta = +25°C)

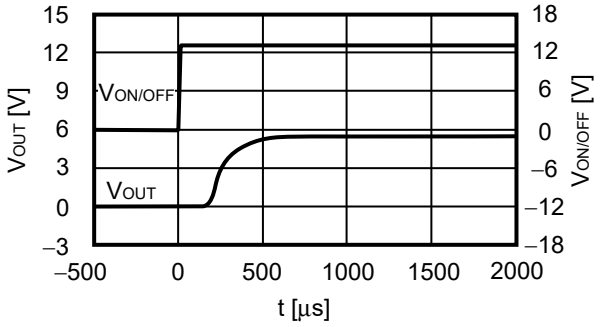
3. 1 V_{OUT} = 3.3 V

V_{IN} = 13.5 V, C_L = 10 µF, C_{IN} = 0.1 µF,
I_{OUT} = 100 mA, $V_{ON / OFF}$ = 0 V → 13.5 V



3. 2 V_{OUT} = 5.0 V

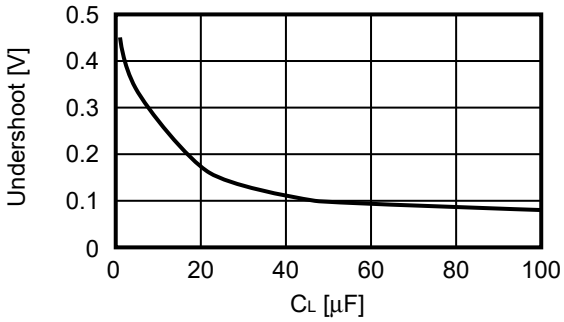
V_{IN} = 13.5 V, C_L = 10 µF, C_{IN} = 0.1 µF,
I_{OUT} = 100 mA, $V_{ON / OFF}$ = 0 V → 13.5 V



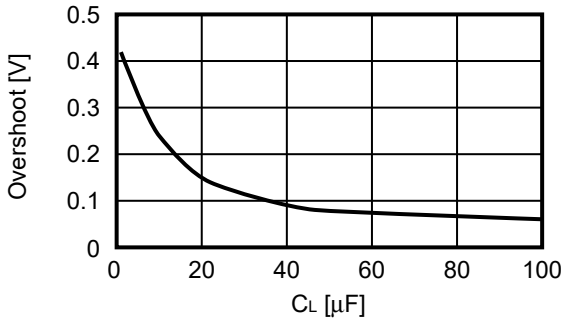
4. Load transient response characteristics dependent on capacitance (Ta = +25°C)

4. 1 V_{OUT} = 5.0 V

V_{IN} = 13.5 V, C_{IN} = 0.1 µF, I_{OUT} = 50 mA → 100 mA



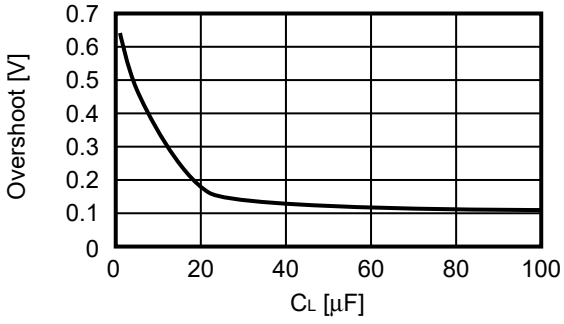
V_{IN} = 13.5 V, C_{IN} = 0.1 µF, I_{OUT} = 100 mA → 50 mA



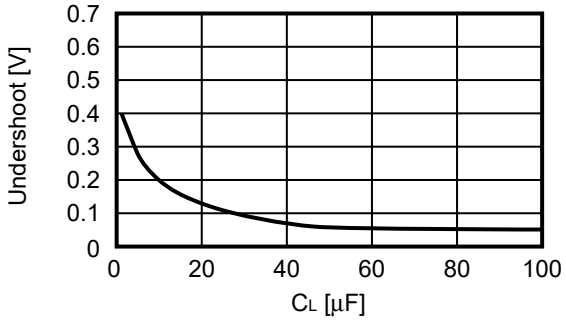
5. Input transient response characteristics dependent on capacitance (Ta = +25°C)

5. 1 V_{OUT} = 5.0 V

V_{IN} = 7.0 V → 12.0 V, t_r = 5.0 µs, C_{IN} = 0.1 µF, I_{OUT} = 30 mA



V_{IN} = 12.0 V → 7.0 V, t_r = 5.0 µs, C_{IN} = 0.1 µF, I_{OUT} = 30 mA



6. Example of equivalent series resistance vs. Output current characteristics (Ta = +25°C)

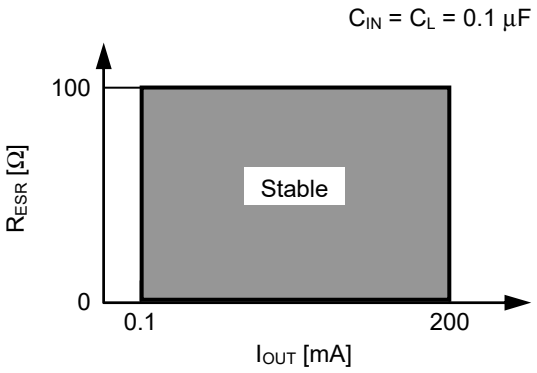
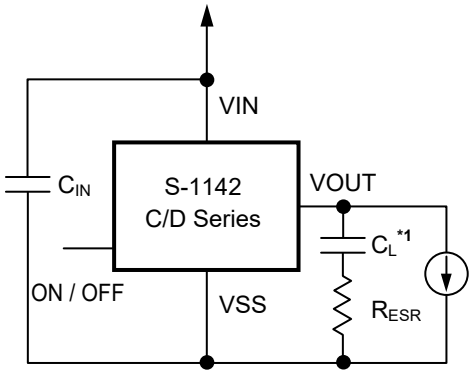


Figure 15



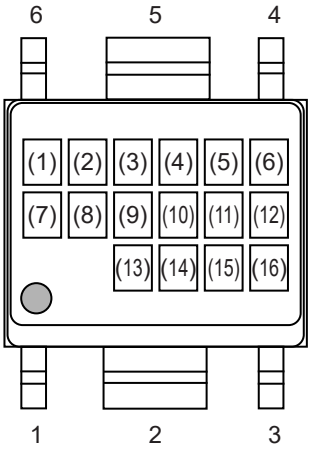
1. C_L: TDK Corporation C3216X8R2A104K (0.1 µF)

Figure 16

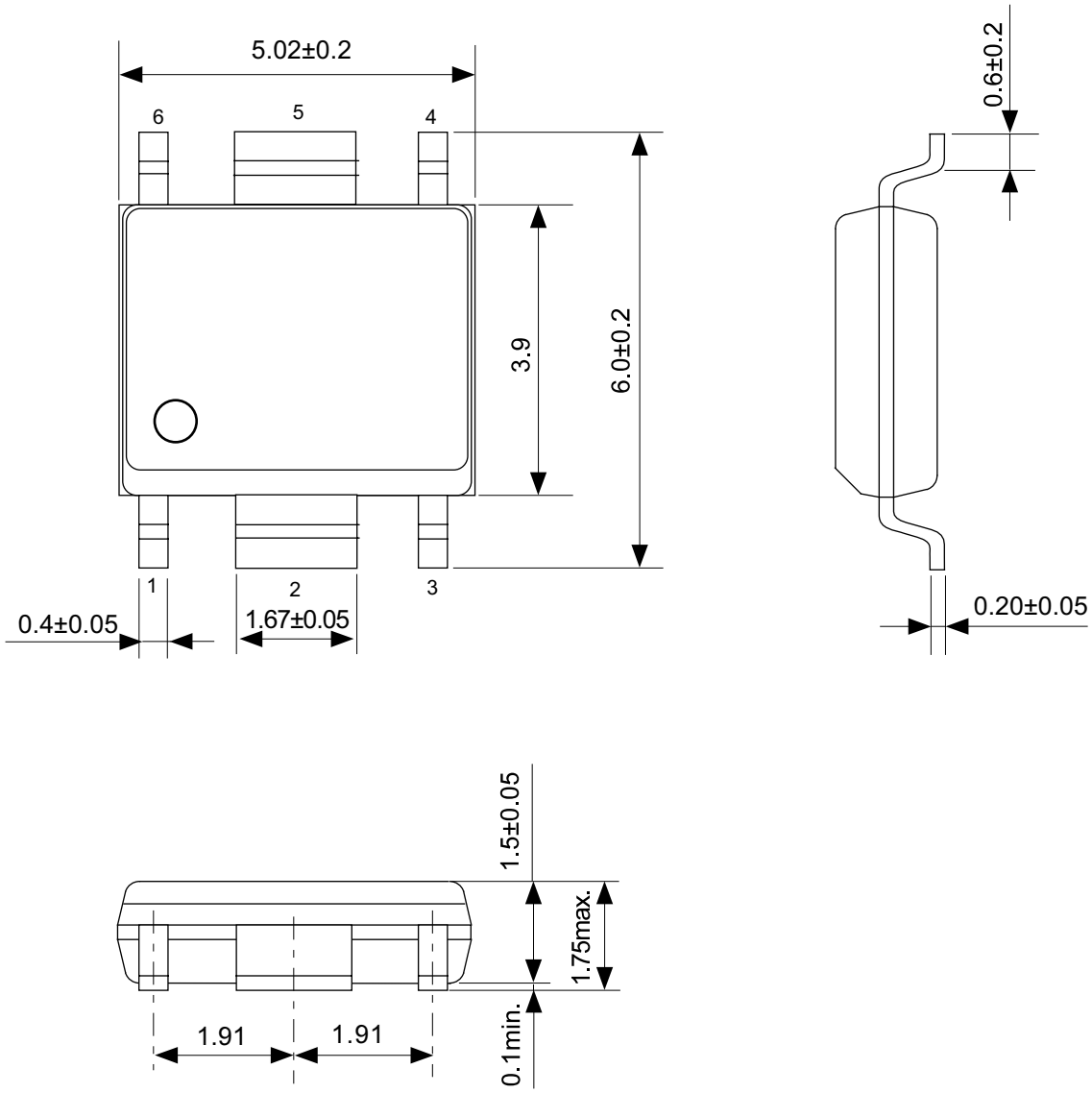
■ Marking Specification

1. HSOP-6

Top view

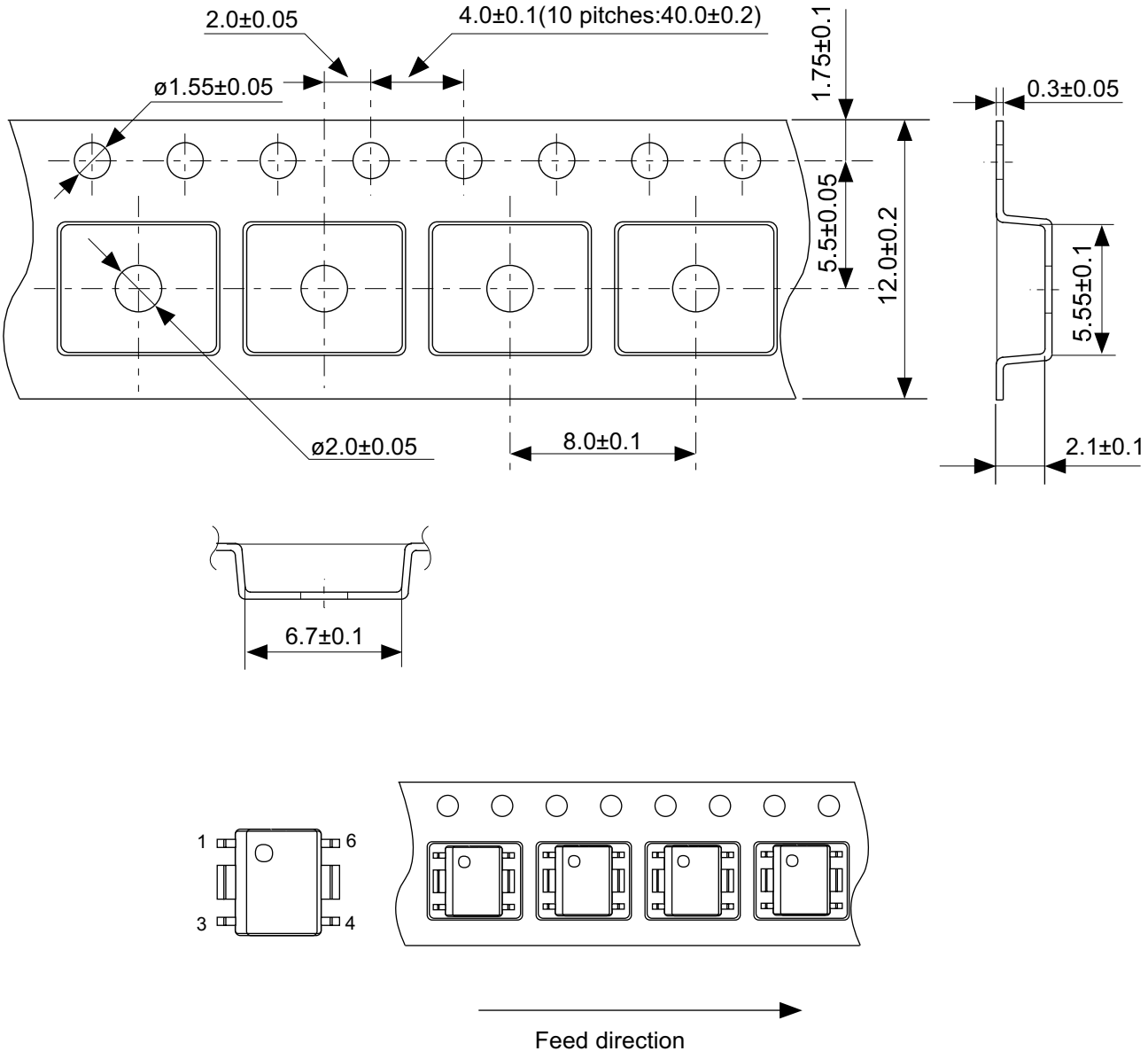


(1) to (5):	Product name: S1142 (Fixed)
(6):	Product type
(7), (8):	Value of output voltage
(9):	Operation temperature
(10) to (16):	Lot number



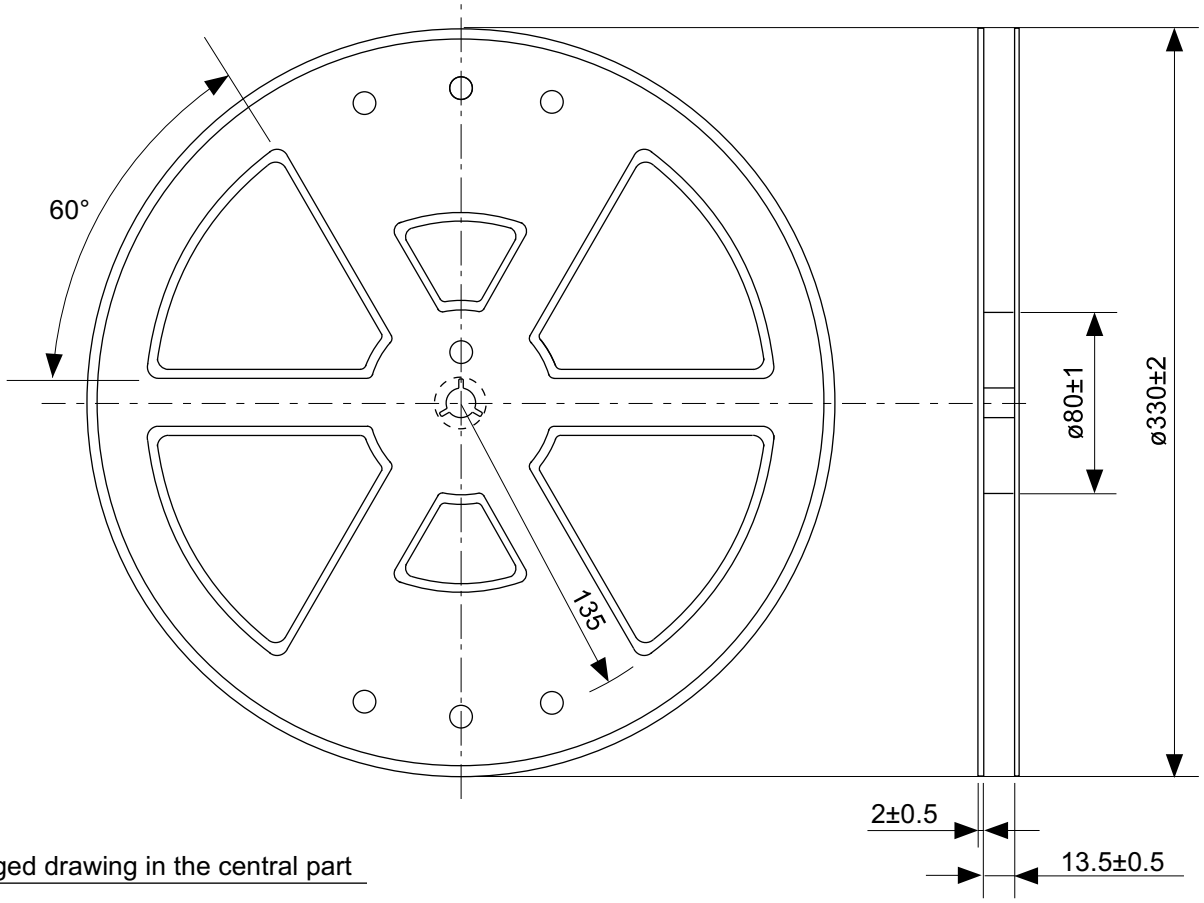
No. FH006-A-P-SD-2.1

TITLE	HSOP6-A-PKG Dimensions
No.	FH006-A-P-SD-2.1
ANGLE	
UNIT	mm
ABLIC Inc.	

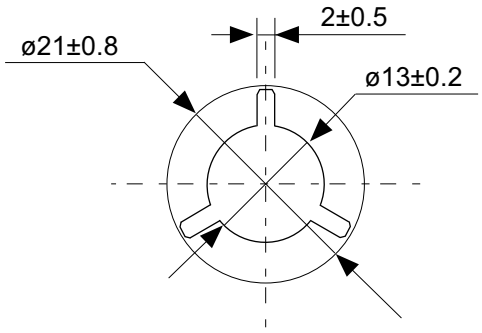


No. FH006-A-C-SD-2.0

TITLE	HSOP6-A-Carrier Tape
No.	FH006-A-C-SD-2.0
ANGLE	
UNIT	mm
ABLIC Inc.	

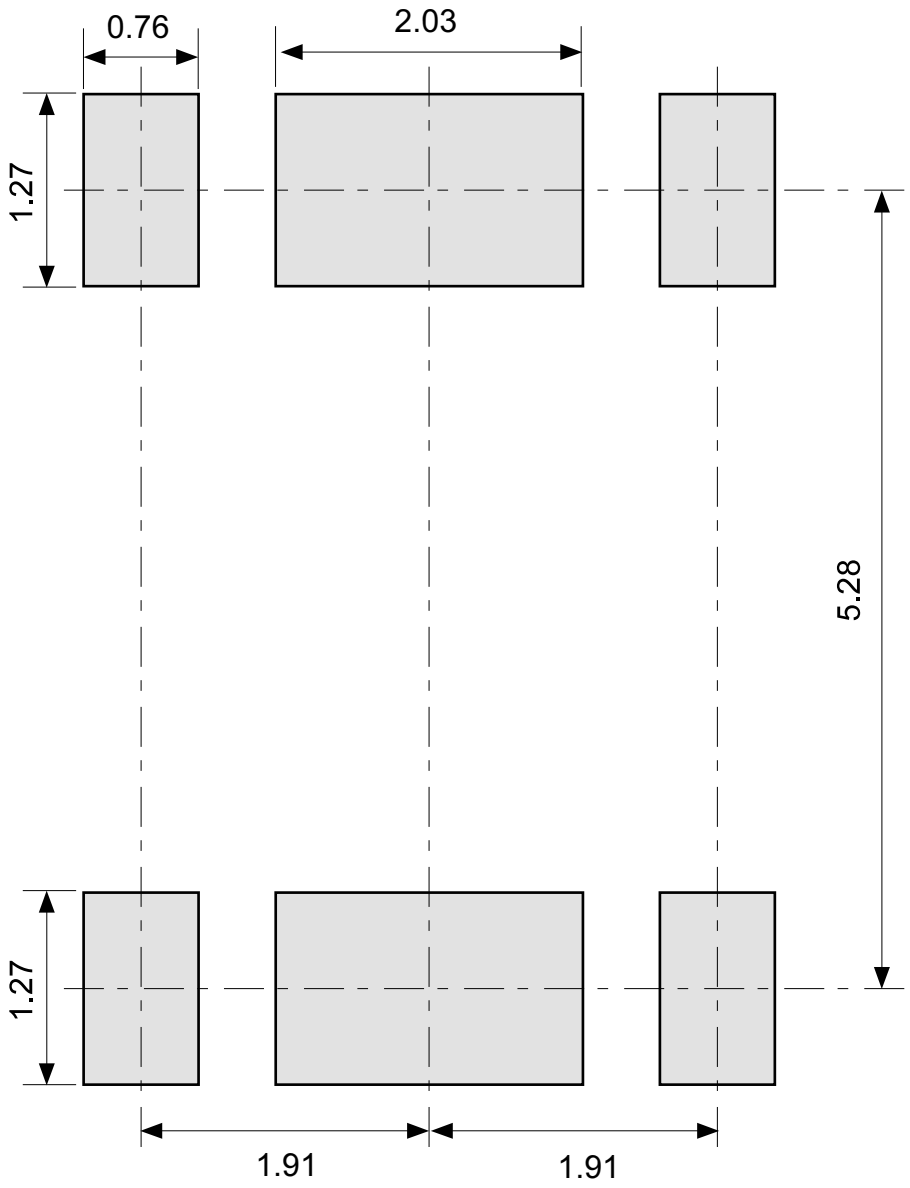


Enlarged drawing in the central part



No. FH006-A-R-SD-1.0

TITLE	HSOP6-A-Reel		
No.	FH006-A-R-SD-1.0		
ANGLE		QTY.	2,000
UNIT	mm		
ABLIC Inc.			



No. FH006-A-L-SD-2.0

TITLE	HSOP6-A -Land Recommendation
No.	FH006-A-L-SD-2.0
ANGLE	
UNIT	mm
ABLIC Inc.	

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2.4-2019.07

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