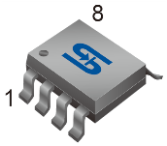
TSM042N03CS

30V N-Channel Power MOSFET

SOP-8



Pin Definition:

1. Source	8. Drain
2. Source	7. Drain
3. Source	6. Drain
4. Gate	5. Drain

Key Parameter Performance

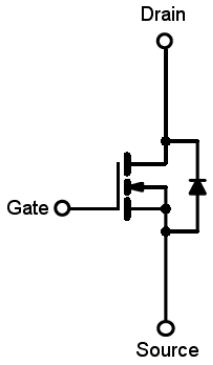
Parameter		Value	Unit
V_{DS}		30	V
$R_{DS(on)}$ (max)	V_{GS} = 10V	4.2	mΩ
	V_{GS} = 4.5V	6	
Q_g		24	nC

Ordering Information

Part No.	Package	Packing
TSM042N03CS RLG	SOP-8	2.5kps / 13'' Reel

Note: "G" denotes for Halogen- and Antimony-free as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds

Block Diagram



N-Channel MOSFET

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	Tc=25ºC	I_D	30	A
	Tc=100ºC		19	A
Pulsed Drain Current (Note 1)		I_{DM}	120	A
Single Pulse Avalanche Energy (Note 2)		E_{AS}	125	mJ
Single Pulse Avalanche Current (Note 2)		I_{AS}	50	A
Power Dissipation @ T_C = 25°C		P_D	7	W
Operating Junction Temperature		T_J	175	ºC
Storage Temperature Range		T_{STG}	-55 to +175	°C

Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance - Junction to Ambient	$R_{\Theta JA}$	62	°C/W

TSM042N03CS

30V N-Channel Power MOSFET

Electrical Specifications (T_J=25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V_{GS} = 0V, I_D = 250μA	BV_{DSS}	30	--	--	V
Drain-Source On-State Resistance	V_{GS} = 10V, I_D = 12A	$R_{DS(ON)}$	--	3.8	4.2	mΩ
	V_{GS} = 4.5V, I_D = 6A			5.2	6	
Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	$V_{GS(TH)}$	1.2	1.6	2.5	V
Zero Gate Voltage Drain Current	V_{DS} = 30V, V_{GS} = 0V	I_{DSS}	--	--	1	μA
	V_{DS} = 24V, T_J = 125ºC		--	--	10	
Gate Body Leakage	V_{GS} = ±20V, V_{DS} = 0V	I_{GSS}	--	--	±100	nA
Forward Transconductance (Note 3)	V_{DS} = 10V, I_D = 6A	g_{fs}	--	12	--	S
Dynamic						
Total Gate Charge (Note 3,4)	V_{DS} = 15V, I_D = 12A, V_{GS} = 4.5V	Q_g	--	24	--	nC
Gate-Source Charge (Note 3,4)		Q_{gs}	--	4.2	--	
Gate-Drain Charge (Note 3,4)		Q_{gd}	--	13	--	
Input Capacitance	V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz	C_{iss}	--	2200	--	pF
Output Capacitance		C_{oss}	--	280	--	
Reverse Transfer Capacitance		C_{rss}	--	177	--	
Switching						
Turn-On Delay Time (Note 3,4)	V_{DD} = 15V, I_D = 15A, V_{GS} = 10V, R_{GEN} =3.3Ω	$t_{d(on)}$	--	12.6	--	ns
Turn-On Rise Time (Note 3,4)		t_r	--	19.5	--	
Turn-Off Delay Time (Note 3,4)		$t_{d(off)}$	--	42.8	--	
Turn-Off Fall Time (Note 3,4)		t_f	--	13.2	--	
Source-Drain Diode Ratings and Characteristic						
Maximum Continuous Drain-Source Diode Forward Current	Integral reverse diode in the MOSFET	I_S	--	--	30	A
Maximum Pulse Drain-Source Diode Forward Current		I_{SM}	--	--	120	A
Diode Forward Voltage	V_{GS} = 0V, I_S = 1A	V_{SD}	--	--	1	V

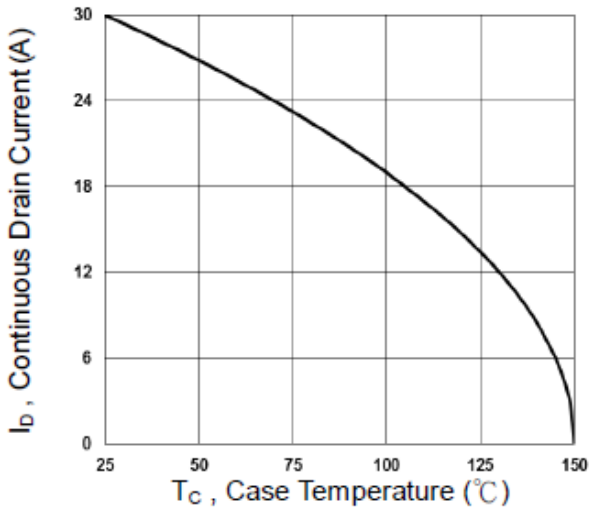
Note:

1. Pulse width limited by safe operating area
2. L=0.1mH, I_{AS} =50A, V_{DD} = 25V, R_G = 25Ω, Starting T_J = 25ºC
3. Pulse test: pulse width ≤300μs, duty cycle ≤2%
4. Switching time is essentially independent of operating temperature.

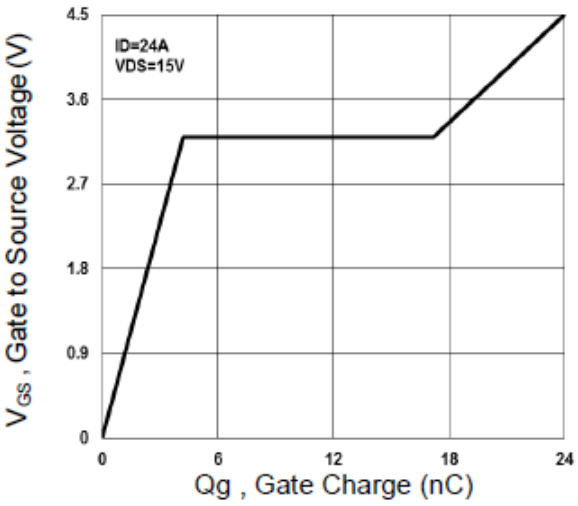
Pb-Free RoHS COMPLIANT

Electrical Characteristics Curve

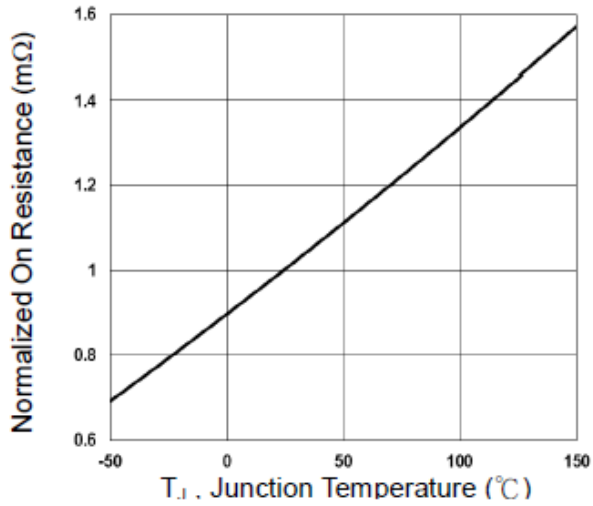
Continuous Drain Current vs. T_C



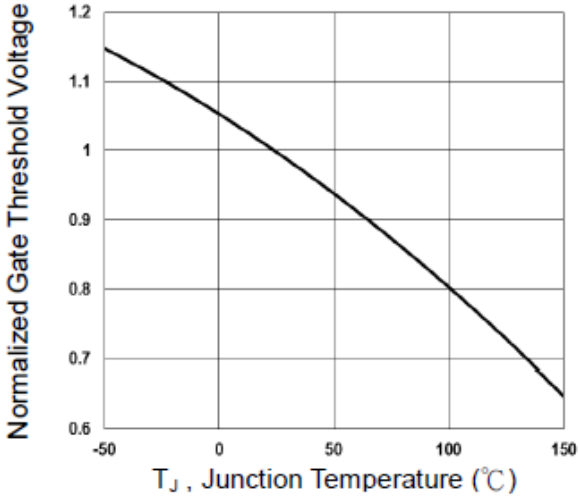
Gate Charge



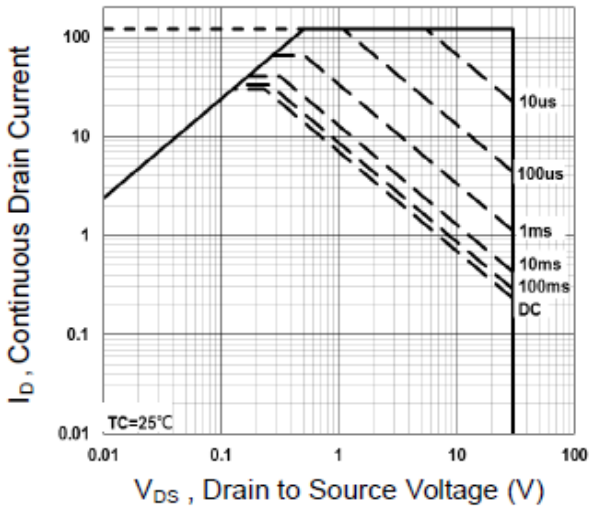
On-Resistance vs. Junction Temperature



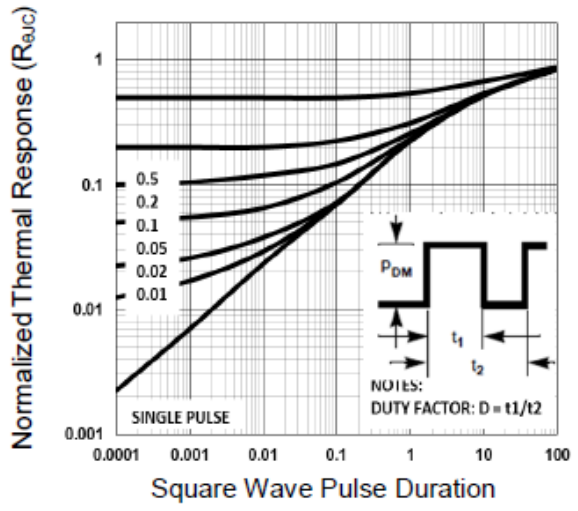
Threshold Voltage vs. Junction Temperature



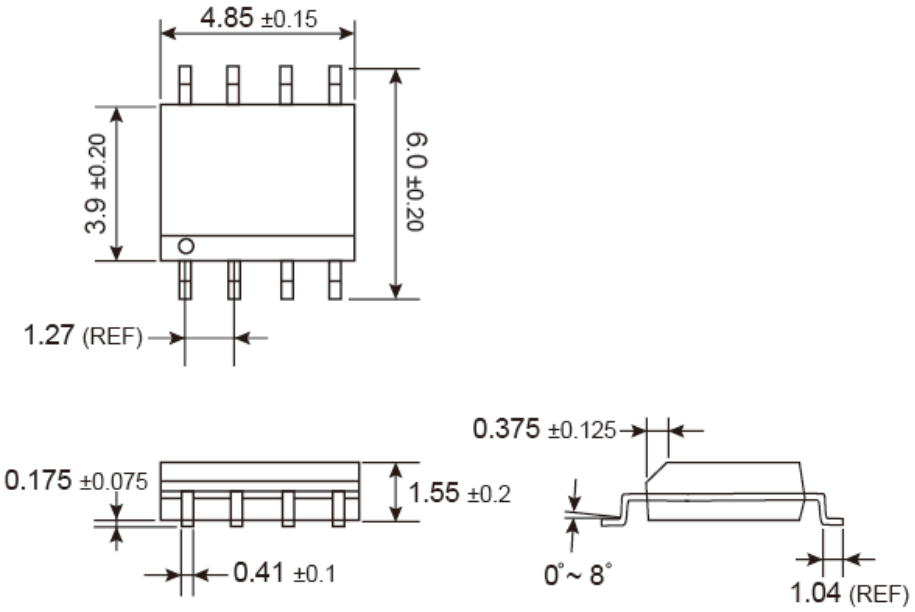
Maximum Safe Operating Area



Normalized Thermal Transient Impedance Curve

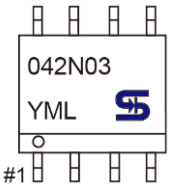


SOP-8 Mechanical Drawing



Unit: Millimeters

Marking Diagram



Y = Year Code

M = Month Code for Halogen Free Product
(**O**=Jan, **P**=Feb, **Q**=Mar, **R**=Apl, **S**=May, **T**=Jun, **U**=Jul, **V**=Aug, **W**=Sep, **X**=Oct, **Y**=Nov, **Z**=Dec)

L = Lot Code

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