Discrete IGBTs Silicon N-Channel IGBT

GT50J341

1. Applications

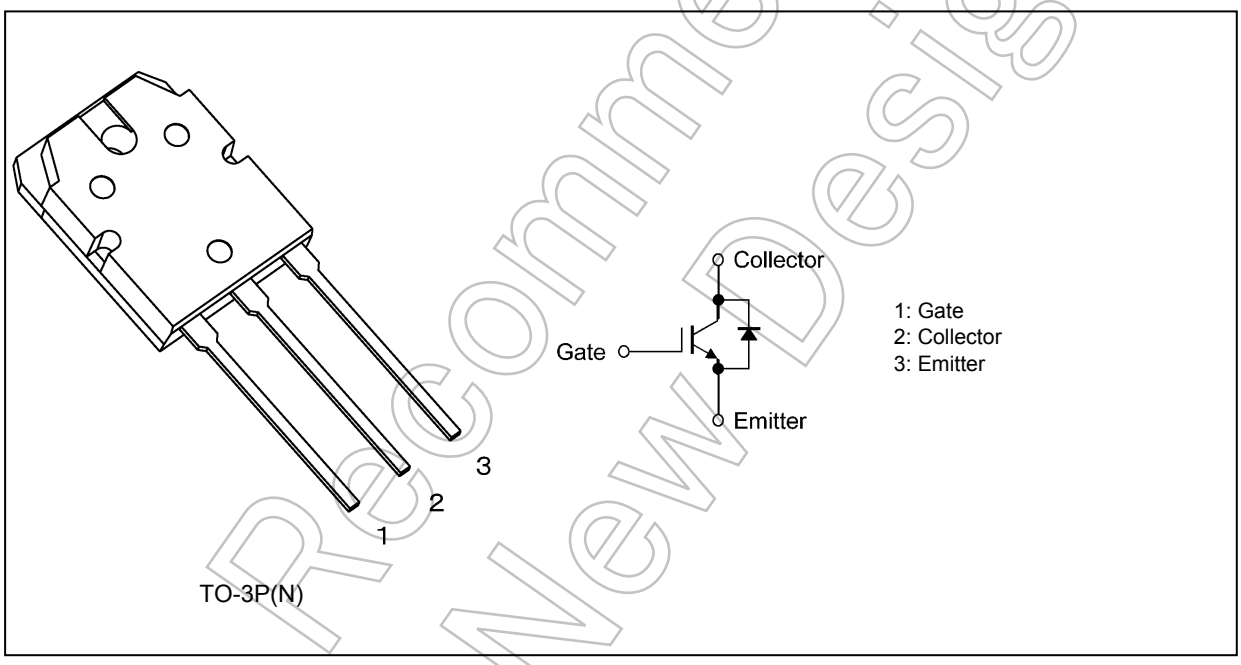
- Dedicated to Current-Resonant Inverter Switching Applications

Note: The product(s) described herein should not be used for any other application.

2. Features

(1) Sixth generation
(2) Enhancement mode
(3) High-speed switching: t_f = 0.15 μs (typ.) (I_C = 50 A)
(4) Low saturation voltage: $V_{CE(sat)}$ = 1.6 V (typ.) (I_C = 50 A)
(5) FRD included between emitter and collector

3. Packaging and Internal Circuit



Not Recommended for New Design

Start of commercial production
2010-06

2014-01-07
Rev.3.0

4. Absolute Maximum Ratings (Note) (T_a = 25°C, unless otherwise specified)

Characteristics			Symbol	Rating	Unit
Collector-emitter voltage			V_{CES}	600	V
Gate-emitter voltage			V_{GES}	±25	
Collector current (DC)			I_C	50	A
Collector current (1 ms)			I_{CP}	100	
Diode forward current (DC)			I_F	28	
Diode forward current (400 μs)			I_{FP}	120	
Collector power dissipation	(T_c = 100°C)		P_C	100	W
Collector power dissipation	(T_c = 25°C)			200	
Junction temperature		(Note 1)	T_j	175	°C
Storage temperature			T_{stg}	-55 to 175	
Mounting torque			TOR	0.8	N · m

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

In general, loss of IGBT increases more when it has positive temperature coefficient and gets higher temperature.
In case that the temperature rise due to loss of IGBT exceeds the heat release capacity of a device, it leads to thermorunaway and results in destruction.
Therefore, please design heat release of a device with due consideration to the temperature rise of IGBT.

Note 1: Ensure that the junction temperature does not exceed 175 °C.

5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Junction-to-case thermal resistance (IGBT)	$R_{th(j-c)}$	0.75	°C/W
Junction-to-case thermal resistance (diode)		1.8	

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6. Electrical Characteristics

6.1. Static Characteristics (T_a = 25°C, unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current	I_{GES}	V_{GE} = ±25 V, V_{CE} = 0 V	—	—	±100	nA
Collector cut-off current	I_{CES}	V_{CE} = 600 V, V_{GE} = 0 V	—	—	1.0	mA
Gate-emitter cut-off voltage	$V_{GE(OFF)}$	I_C = 50 mA, V_{CE} = 5 V	4.5	—	7.5	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	I_C = 50 A, V_{GE} = 15 V	—	1.6	2.2	
Diode forward voltage	V_F	I_F = 15 A, V_{GE} = 0 V	—	—	2.0	

6.2. Dynamic Characteristics (T_a = 25°C, unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Input capacitance	C_{ies}	V_{CE} = 10 V, V_{GE} = 0 V, f = 1 MHz	—	2700	—	pF
Switching time (rise time)	t_r	Resistive load V_{CC} = 300 V, I_C = 50 A, V_{GG} = ±15 V, R_G = 39 Ω See Fig. 6.2.1, 6.2.2.	—	0.18	—	μs
Switching time (turn-on time)	t_{on}		—	0.27	—	
Switching time (fall time)	t_f		—	0.15	0.35	
Switching time (turn-off time)	t_{off}		—	0.45	—	
Reverse recovery time	t_{rr}	I_F = 15 A, V_{GE} = 0 V, di/dt = -100 A/μs	—	0.1	—	

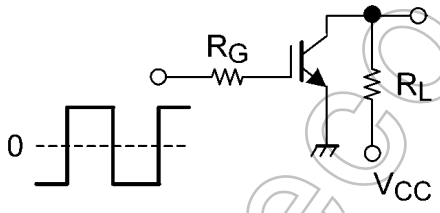


Fig. 6.2.1 Test Circuit of Switching Time

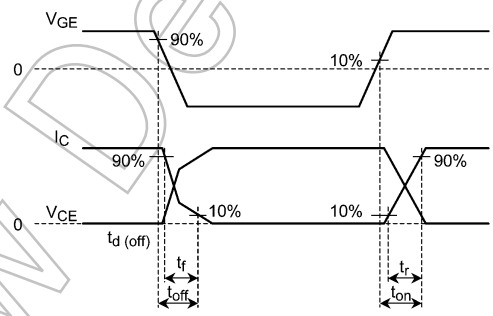


Fig. 6.2.2 Timing Chart of Switching Time

7. Marking (Note)

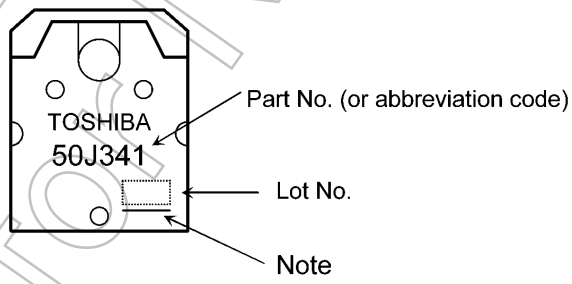


Fig. 7.1 Marking

Note: A line under a Lot No. identifies the indication of product Labels.
[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.
The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

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8. Characteristics Curves (Note)

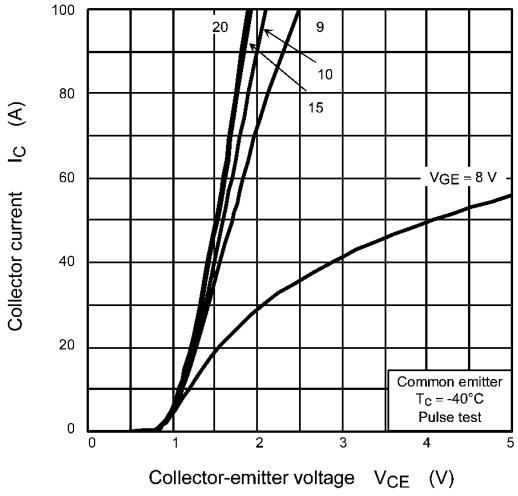


Fig. 8.1 I_C - V_{CE}

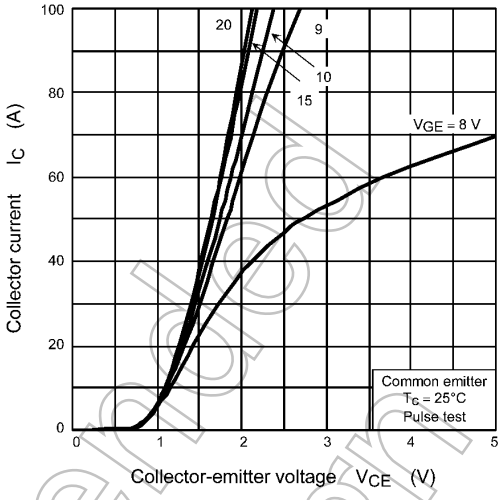


Fig. 8.2 I_C - V_{CE}

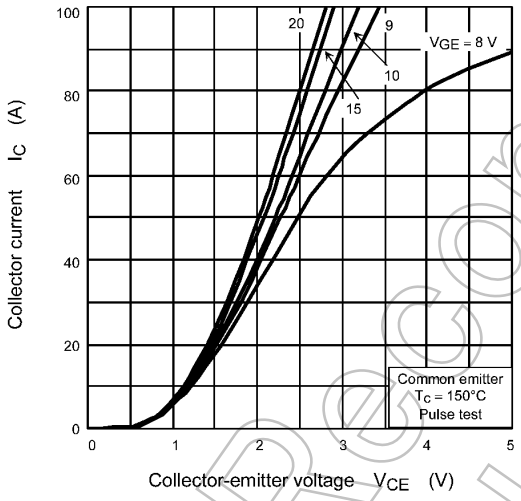


Fig. 8.3 I_C - V_{CE}

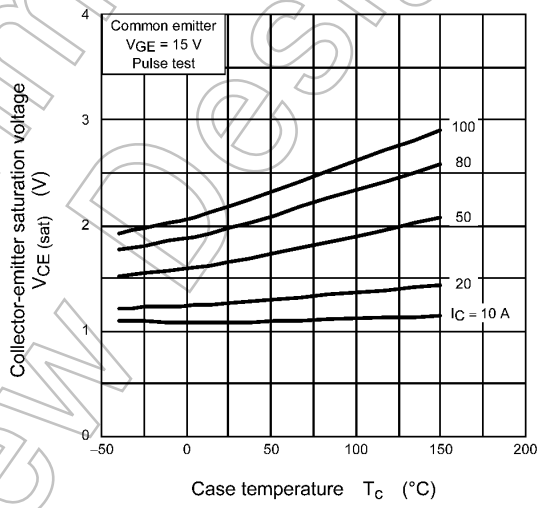


Fig. 8.4 $V_{CE(sat)}$ - T_C

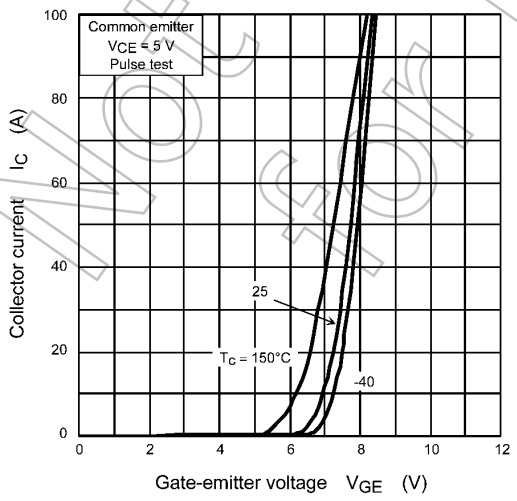


Fig. 8.5 I_C - V_{GE}

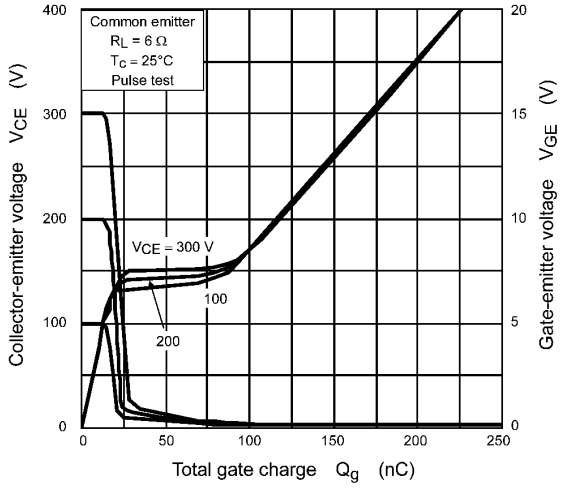


Fig. 8.6 V_{CE}, V_{GE} - Q_g

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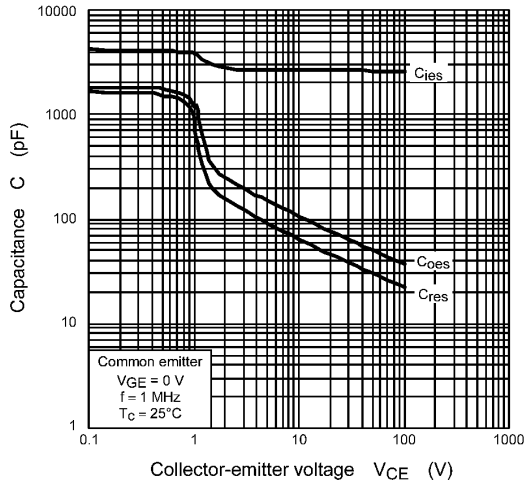


Fig. 8.7 C - V_{CE}

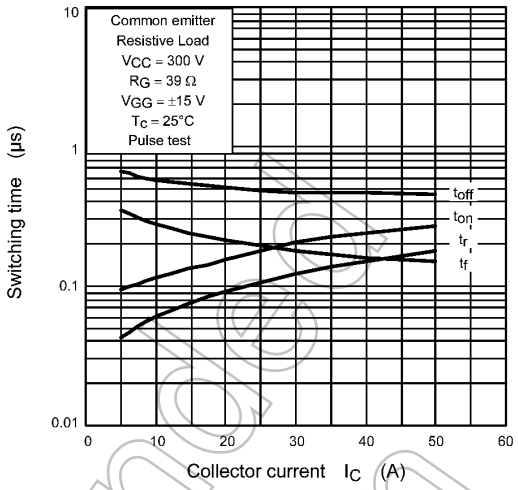


Fig. 8.8 Switching Time - I_C

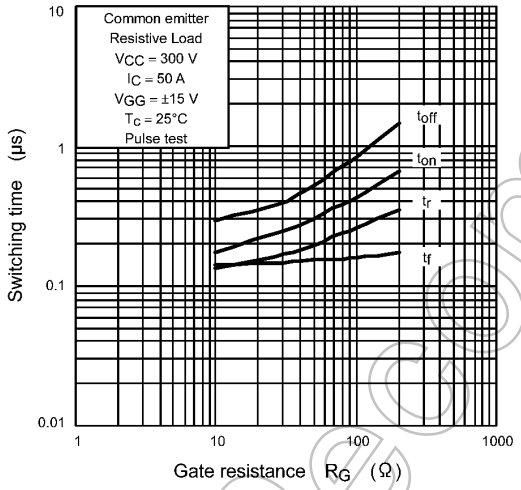


Fig. 8.9 Switching Time - R_G

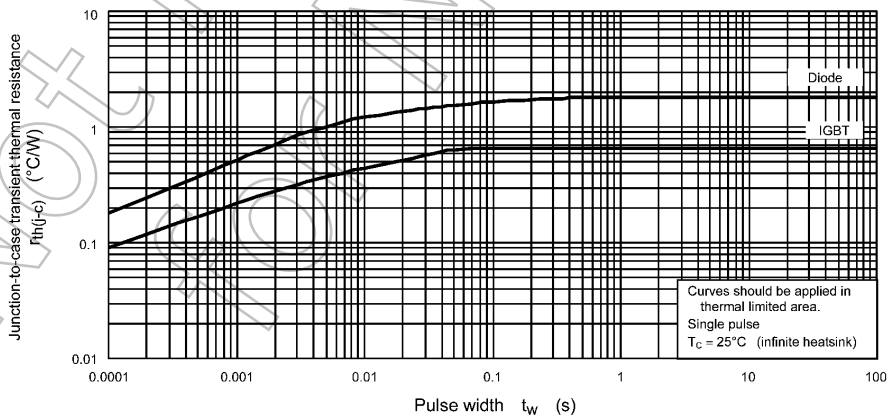


Fig. 8.10 $r_{th(j-c)}$ - t_w
(Guaranteed Maximum)

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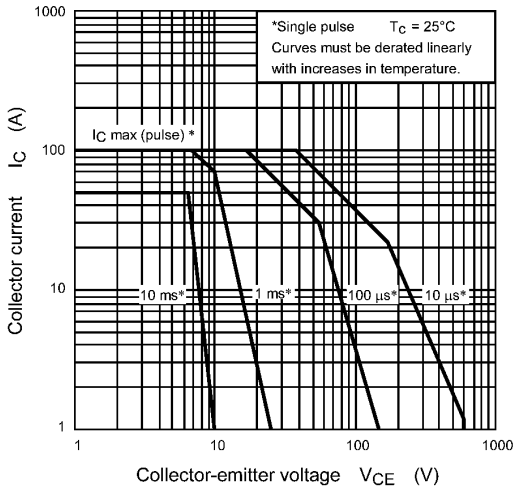


Fig. 8.11 Safe Operating Area (Guaranteed Maximum)

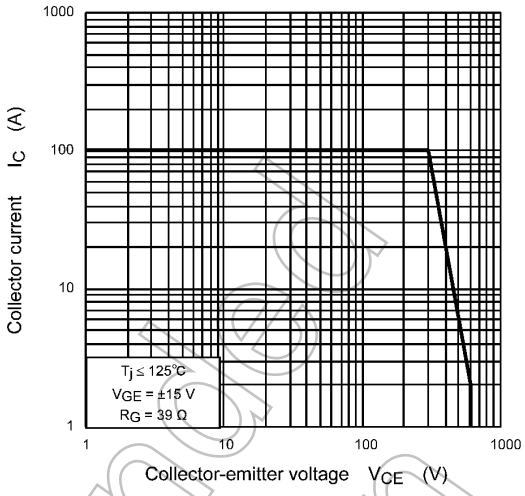


Fig. 8.12 Reverse Bias SOA (Guaranteed Maximum)

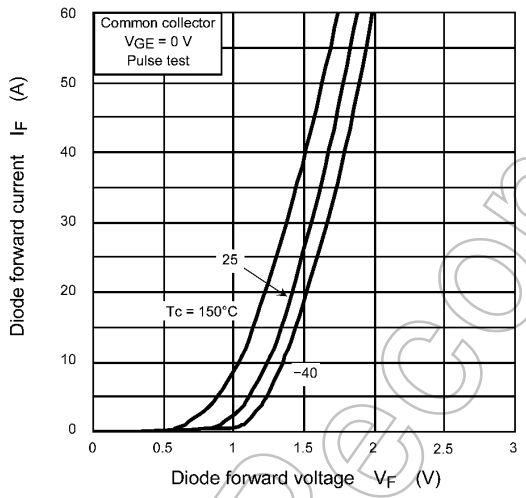


Fig. 8.13 I_F - V_F

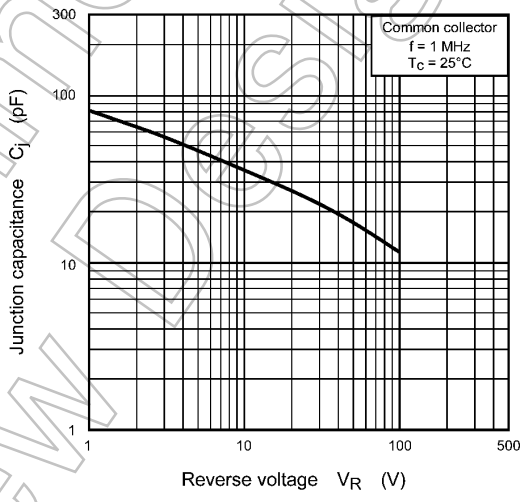


Fig. 8.14 C_j - V_R

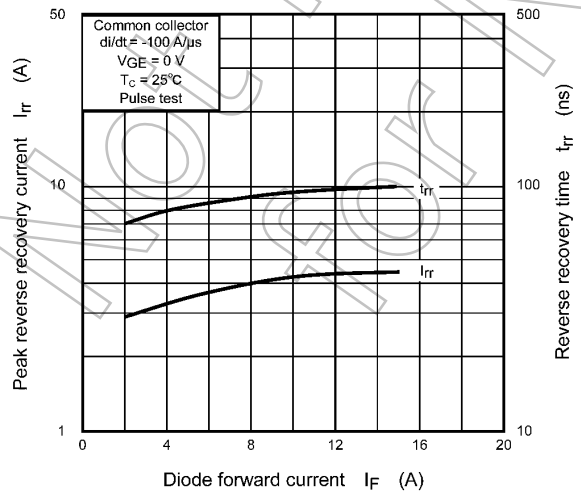


Fig. 8.15 I_{rr},t_{rr} - I_F

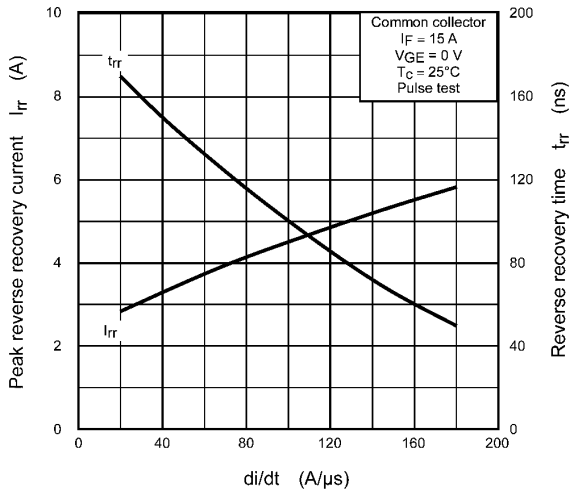


Fig. 8.16 I_{rr}, t_{rr} - di/dt

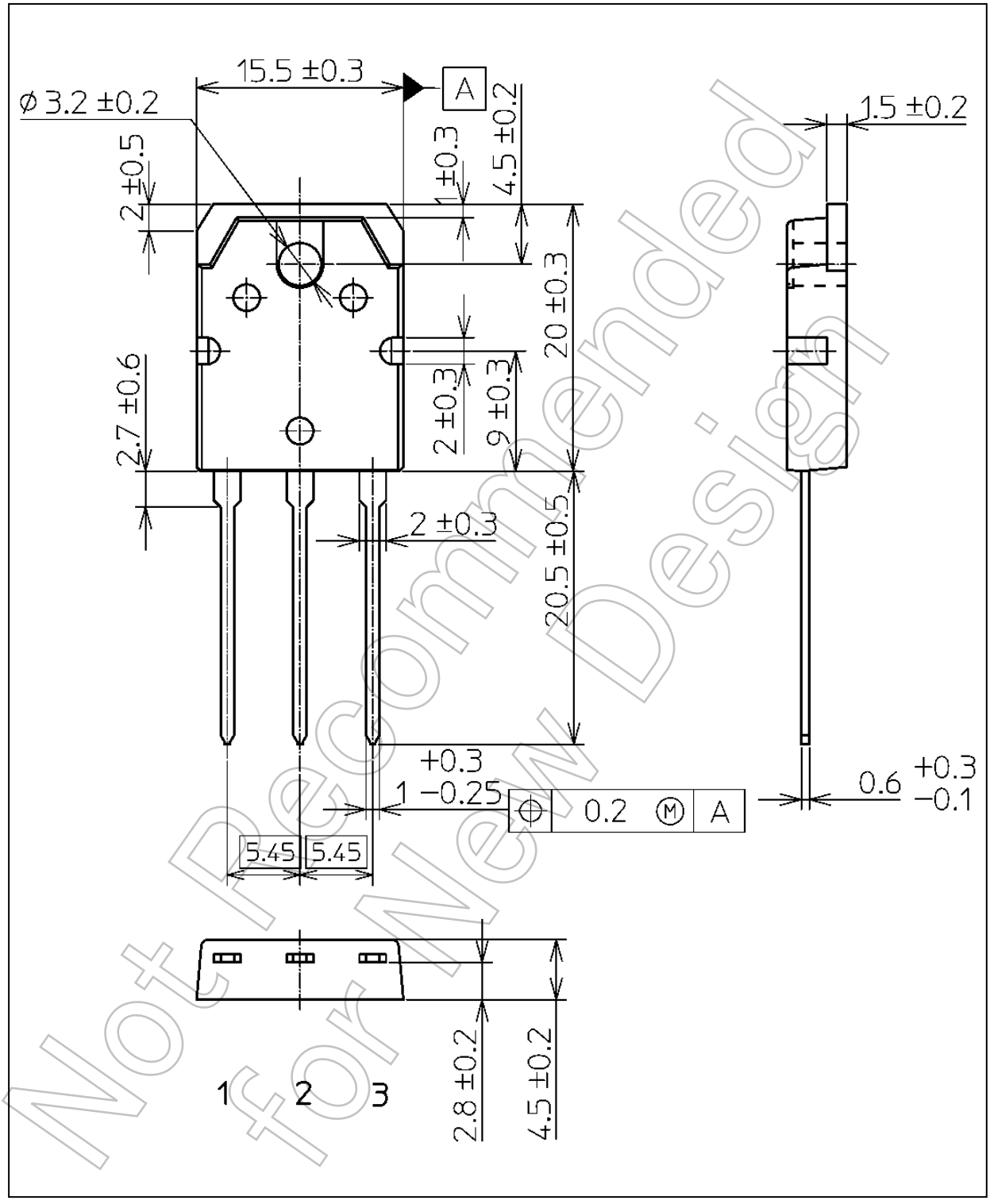
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Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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Package Dimensions

Unit: mm



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Weight: 4.6 g (typ.)

Package Name(s)
TOSHIBA: 2-16C1S
Nickname: TO-3P(N)

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