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NVATS4A103PZ

Power MOSFET
–30 V, 13 mΩ, –60 A, P-Channel

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The NVATS4A103PZ is a power MOSFET designed for compact size and high efficiency which can achieve high thermal performance.
AEC-Q101 qualified MOSFET and PPAP capable suitable for automotive applications.

Features

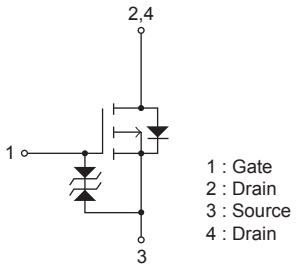
- Low On-Resistance
- High Current Capability
- 100% Avalanche Tested
- AEC-Q101 qualified and PPAP capable
- ATPAK package is pin-compatible with DPAK (TO-252)
- Pb-Free, Halogen Free and RoHS compliance

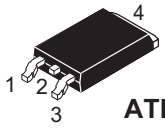
Typical Applications

- Reverse Battery Protection
- Load Switch
- Automotive Front Lighting
- Automotive Body Controllers

V_{DSS}	R_{DS}(on) Max	I_D Max
–30 V	13 mΩ @ –10 V	–60A
	20.5 mΩ @ –4.5 V	

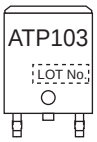
ELECTRICAL CONNECTION
P-Channel





ATPAK

MARKING



SPECIFICATIONS

ABSOLUTE MAXIMUM RATING at Ta = 25°C (Note 1)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	V_{DSS}	–30	V
Gate to Source Voltage	V_{GSS}	±20	V
Drain Current (DC)	I_D	–60	A
Drain Current (Pulse) PW ≤ 10 μs, duty cycle ≤ 1%	I_{DP}	–180	A
Power Dissipation Tc = 25°C	P_D	60	W
Operating Junction and Storage Temperature	Tj, Tstg	–55 to +175	°C
Avalanche Energy (Single Pulse) (Note 2)	E_{AS}	57	mJ
Avalanche Current (Note 3)	I_{AV}	–28	A

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

2 : V_{DD} = –10 V, L = 100 μH, I_{AV} = –28 A

3 : L ≤ 100 μH, Single pulse

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction to Case Steady State (Tc = 25°C)	$R_{\theta JC}$	2.5	°C/W
Junction to Ambient (Note 4)	$R_{\theta JA}$	80.1	°C/W

Note 4 : Surface mounted on FR4 board using a 130 mm^2, 1 oz. Cu pad.

ORDERING INFORMATION

See detailed ordering and shipping information on page 6 of this data sheet.

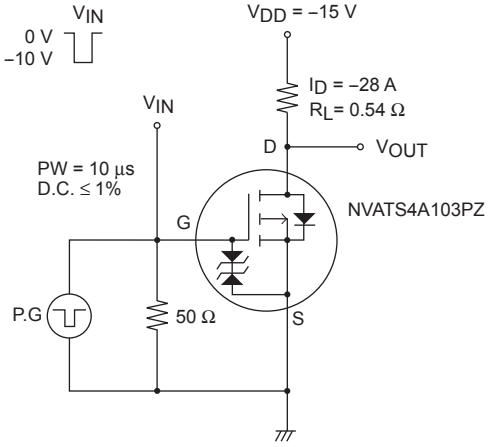
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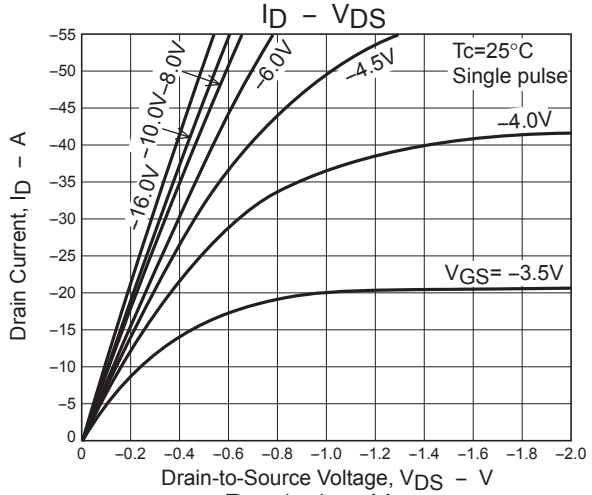
ELECTRICAL CHARACTERISTICS at Ta = 25°C (Note 5)

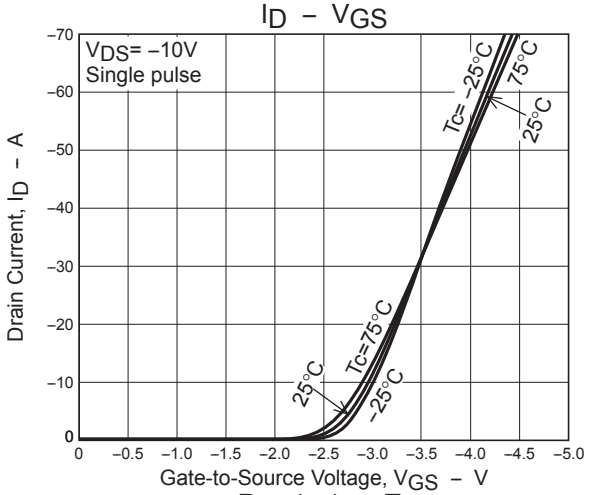
Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	I_D = −1 mA, V_{GS} = 0 V	−30			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS} = −30 V, V_{GS} = 0 V			−1	μA
Gate to Source Leakage Current	I_{GSS}	V_{GS} = ±16 V, V_{DS} = 0 V			±10	μA
Gate Threshold Voltage	$V_{GS}(th)$	V_{DS} = −10 V, I_D = −1 mA	−1.2		−2.6	V
Forward Transconductance	g_{FS}	V_{DS} = −10 V, I_D = −28 A		45		S
Static Drain to Source On-State Resistance	$R_{DS}(on)1$	I_D = −28 A, V_{GS} = −10 V		10	13	mΩ
	$R_{DS}(on)2$	I_D = −14 A, V_{GS} = −4.5 V		14.5	20.5	mΩ
Input Capacitance	Ciss	V_{DS} = −10 V, f = 1 MHz		2,430		pF
Output Capacitance	Coss			555		pF
Reverse Transfer Capacitance	Crss			395		pF
Turn-ON Delay Time	$t_d(on)$	See Fig.1		19		ns
Rise Time	t_r			400		ns
Turn-OFF Delay Time	$t_d(off)$			150		ns
Fall Time	t_f			145		ns
Total Gate Charge	Qg	V_{DS} = −15 V, V_{GS} = −10 V, I_D = −55 A		47		nC
Gate to Source Charge	Qgs			10		nC
Gate to Drain “Miller” Charge	Qgd			8.7		nC
Forward Diode Voltage	V_{SD}	I_S = −55 A, V_{GS} = 0 V		−1.03	−1.5	V

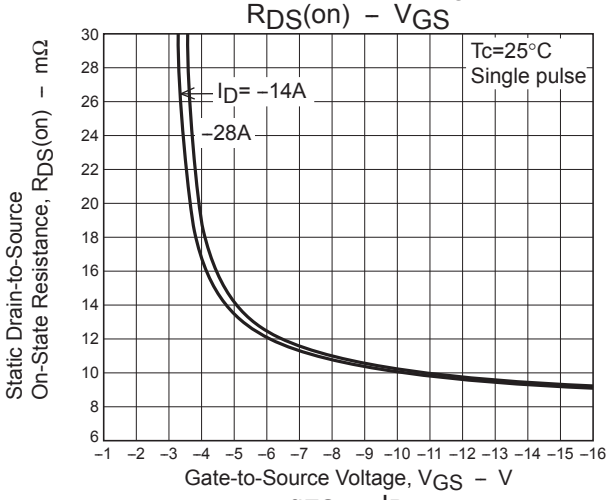
Note 5 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

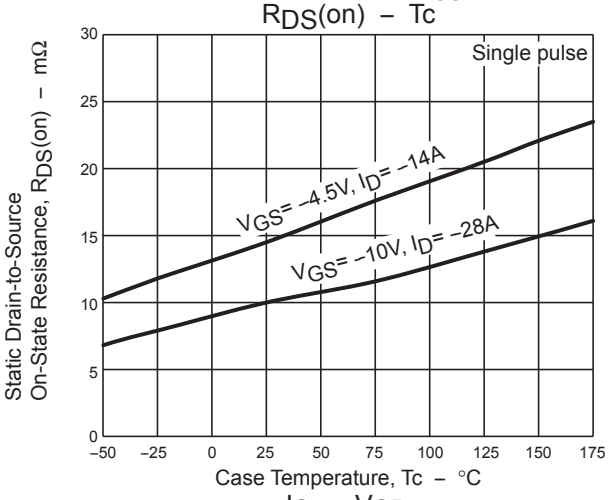
Fig.1 Switching Time Test Circuit

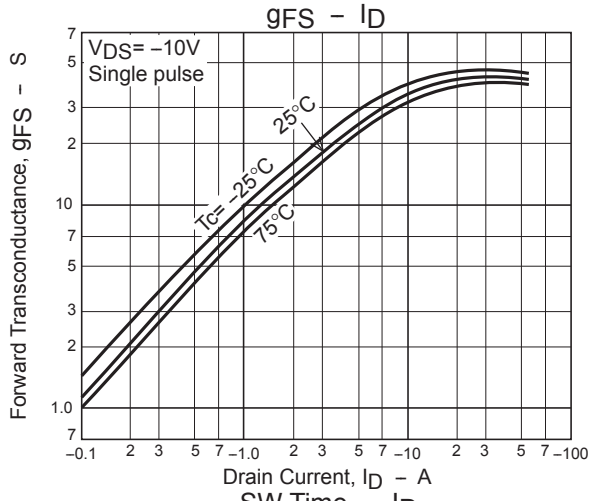


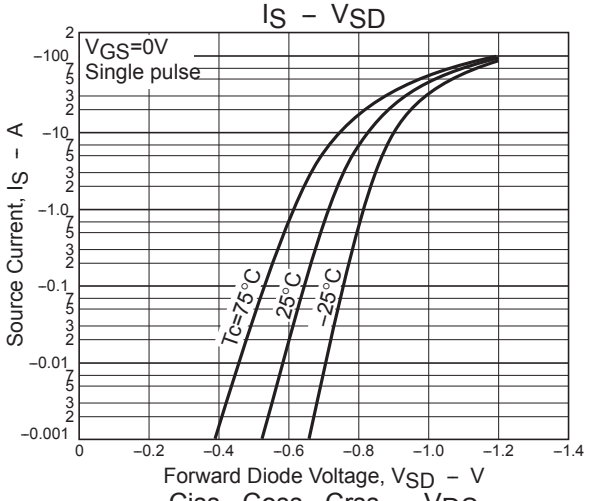

I_D – V_{DS}
Tc=25°C
Single pulse
–4.5V
–4.0V
–6.0V
–8.0V
–10.0V
–16.0V
V_{GS}= –3.5V
Drain Current, I_D – A
Drain-to-Source Voltage, V_{DS} – V

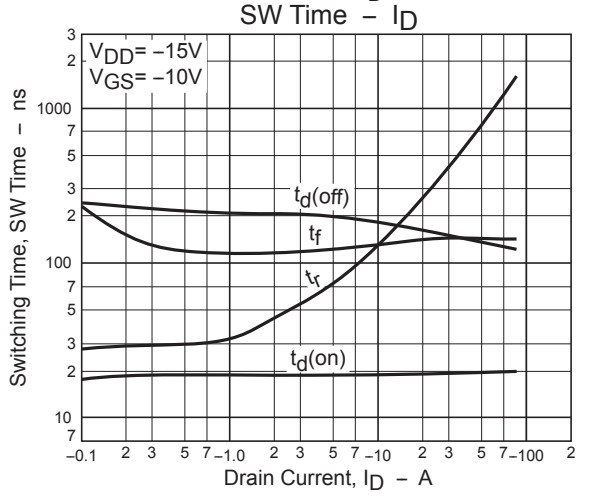

I_D – V_{GS}
V_{DS}= –10V
Single pulse
Tc= –25°C
75°C
25°C
Tc=75°C
–25°C
Drain Current, I_D – A
Gate-to-Source Voltage, V_{GS} – V

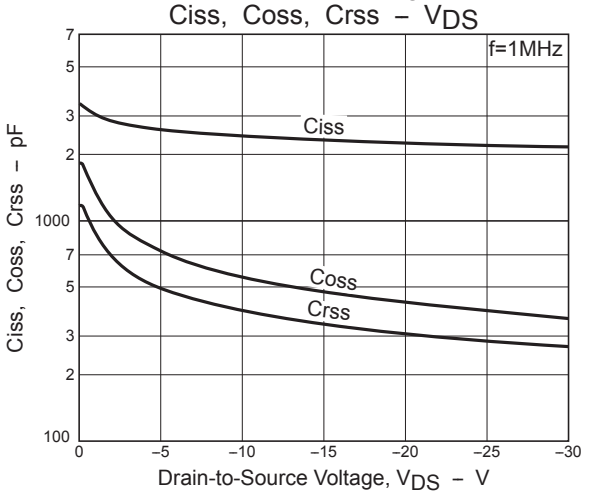

$R_{DS}(on)$ – V_{GS}
Tc=25°C
Single pulse
I_D= –14A
–28A
Static Drain-to-Source
On-State Resistance, $R_{DS}(on)$ – mΩ
Gate-to-Source Voltage, V_{GS} – V

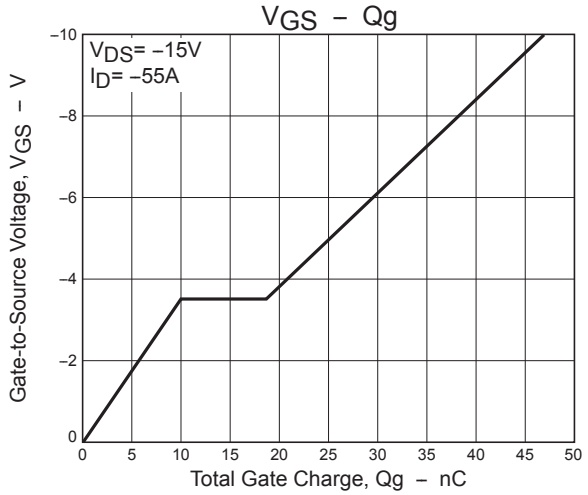

$R_{DS}(on)$ – Tc
Single pulse
V_{GS}= –4.5V, I_D= –14A
V_{GS}= –10V, I_D= –28A
Static Drain-to-Source
On-State Resistance, $R_{DS}(on)$ – mΩ
Case Temperature, Tc – °C

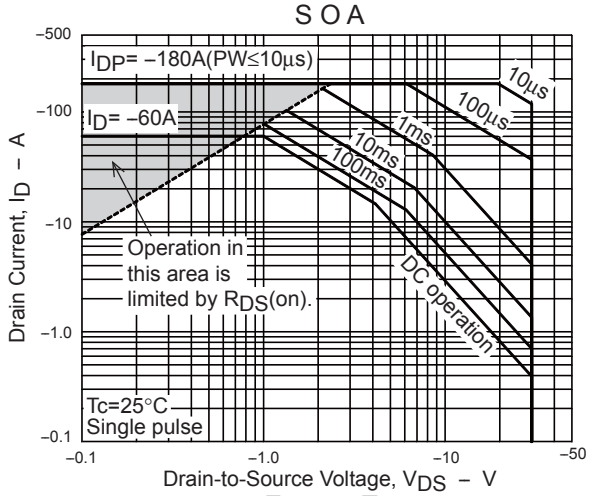

g_{FS} – I_D
V_{DS}= –10V
Single pulse
25°C
Tc= –25°C
75°C
Forward Transconductance, g_{FS} – S
Drain Current, I_D – A

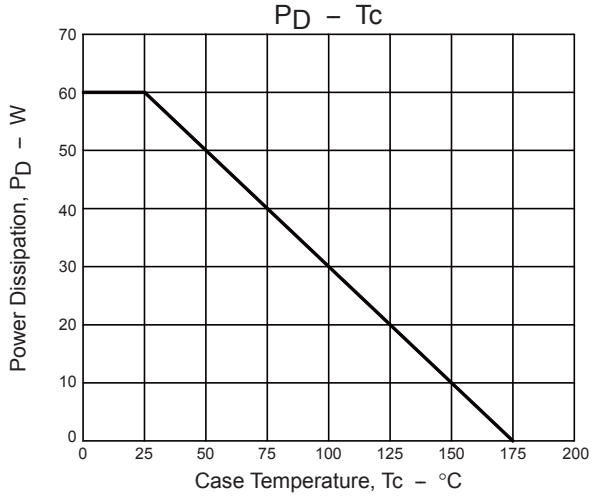

I_S – V_{SD}
V_{GS}=0V
Single pulse
Tc=75°C
25°C
–25°C
Source Current, I_S – A
Forward Diode Voltage, V_{SD} – V

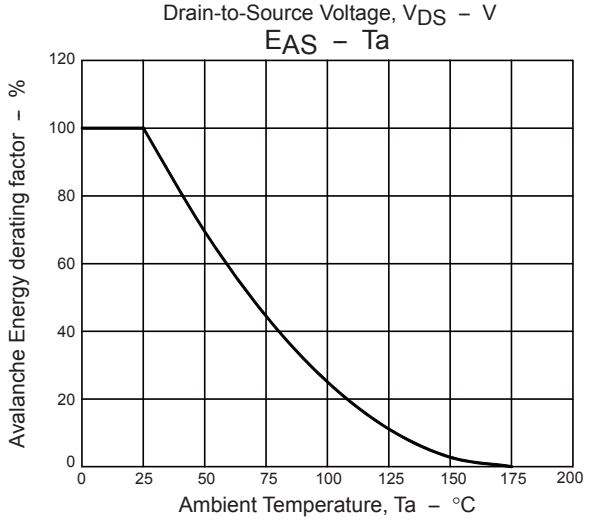

SW Time – I_D
V_{DD}= –15V
V_{GS}= –10V
$t_{d(off)}$
t_f
t_r
$t_{d(on)}$
Switching Time, SW Time – ns
Drain Current, I_D – A

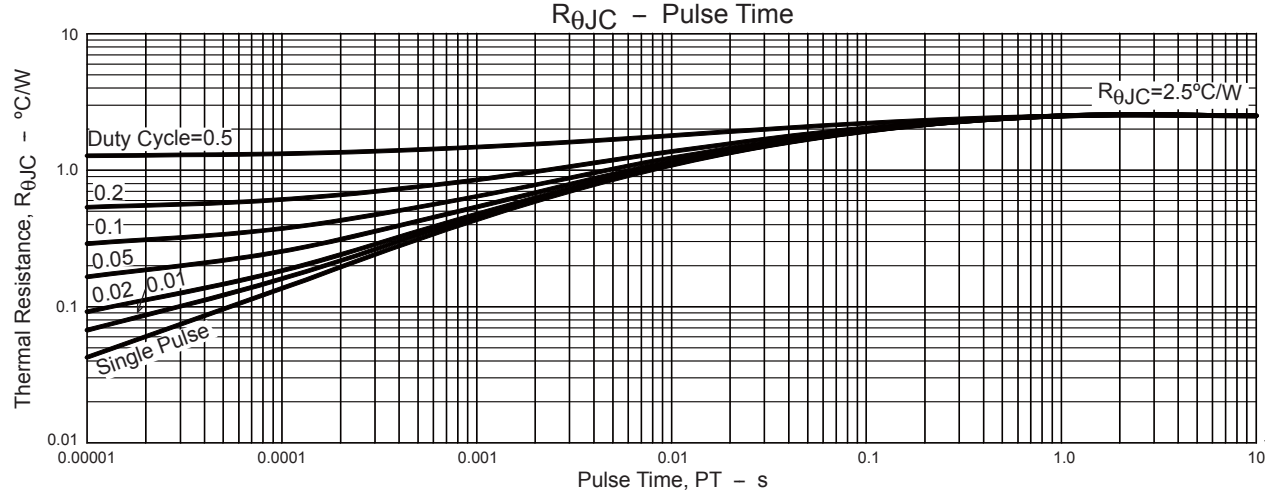

Ciss, Coss, Crss – V_{DS}
f=1MHz
Ciss
Coss
Crss
Ciss, Coss, Crss – pF
Drain-to-Source Voltage, V_{DS} – V


VGS – Qg
VDS= –15V
ID= –55A
Gate-to-Source Voltage, VGS – V
Total Gate Charge, Qg – nC


S O A
IDP= –180A(PW≤10μs)
ID= –60A
10μs
100μs
1ms
10ms
100ms
DC operation
Operation in this area is limited by RDS(on).
Tc=25°C
Single pulse
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V

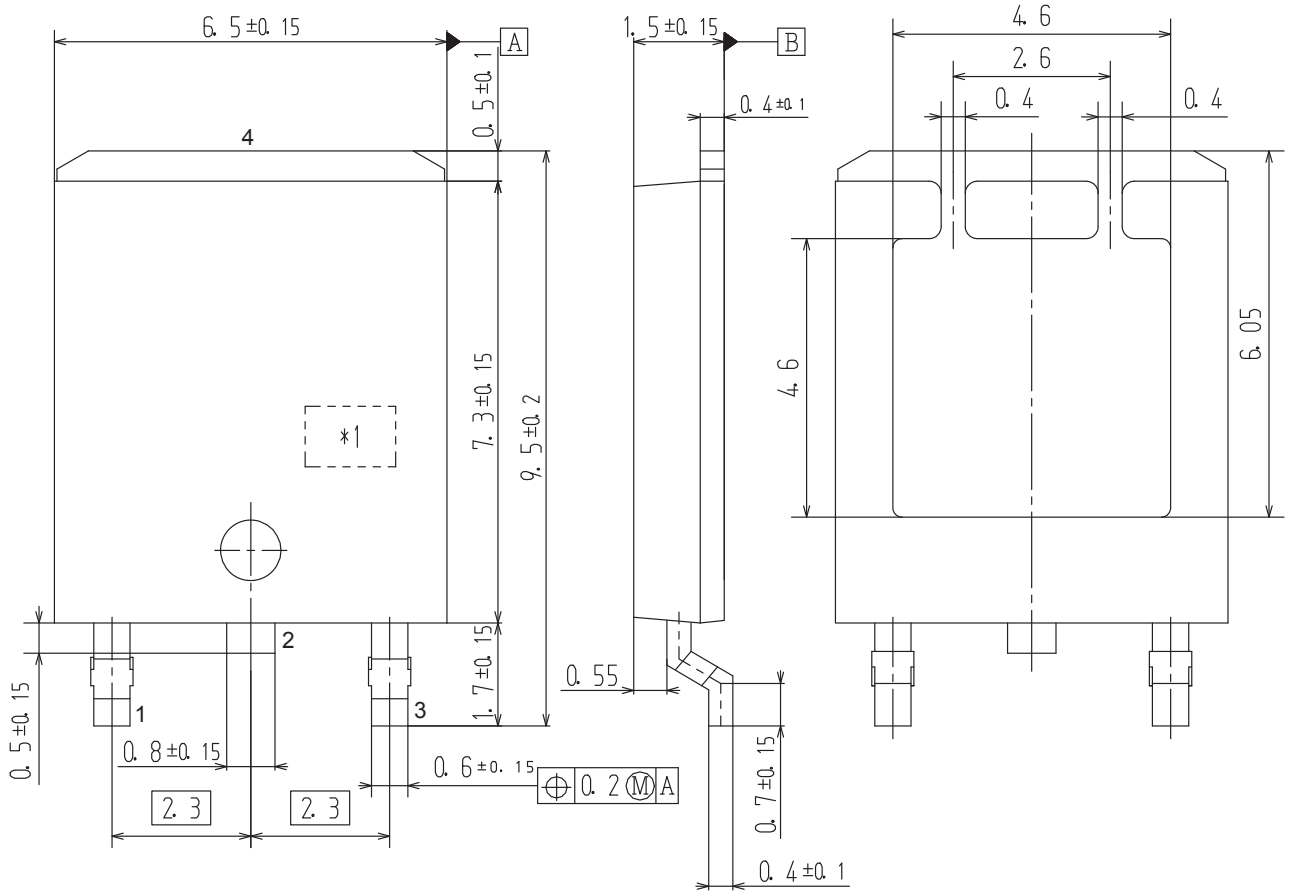

PD – Tc
Power Dissipation, PD – W
Case Temperature, Tc – °C

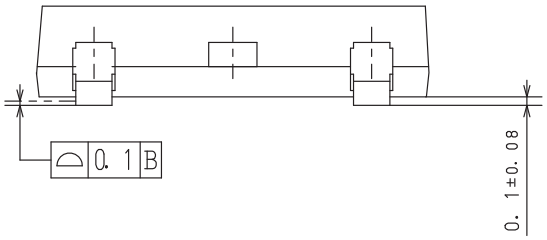

EAS – Ta
Avalanche Energy derating factor – %
Ambient Temperature, Ta – °C


RθJC – Pulse Time
RθJC=2.5°C/W
Duty Cycle=0.5
0.2
0.1
0.05
0.02
0.01
Single Pulse
Thermal Resistance, RθJC – °C/W
Pulse Time, PT – s

PACKAGE DIMENSIONS
unit : mm

DPAK (Single Gauge) / ATPAK
CASE 369AM
ISSUE O



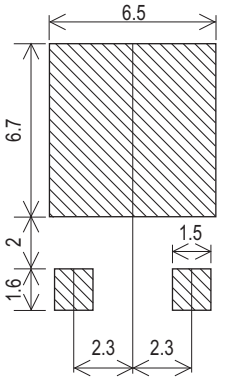


Pin2 is idle pin with electrical designation only carried

*1:Lot indication

1 : Gate
2 : Drain
3 : Source
4 : Drain

RECOMMENDED SOLDERING FOOTPRINT



ORDERING INFORMATION

Device	Marking	Package	Shipping (Qty / Packing)
NVATS4A103PZT4G	ATP103	DPAK(Single Gauge) / ATPAK (Pb-Free / Halogen Free)	3,000 / Tape & Reel

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

Note on usage : Since the NVATS4A103PZ is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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