RJK2076DPA

200V - 20A - MOS FET
High Speed Power Switching

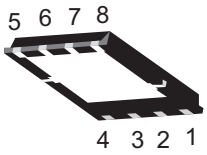
R07DS0859EJ0200
Rev.2.00
Jan 08, 2013

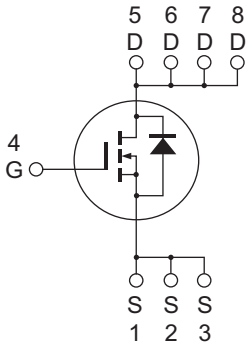
Features

- Low on-resistance
  $R_{DS(on)}$ = 0.068 Ω typ. (at I_D = 10 A, V_{GS} = 10 V, Ta = 25°C)
- Very low gate charge
  Qg = 19 nC typ. (at V_{DD} = 160 V, V_{GS} = 10 V, I_D = 20 A, Ta = 25°C)
- Low leakage current
- High speed switching

Outline

RENESAS Package code: PWSN0008DE-A
(Package name: WPAK(3F))





1, 2, 3	Source
4	Gate
5, 6, 7, 8	Drain

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	200	V
Gate to source voltage	V_{GSS}	±30	V
Drain current	I_D Note4	20	A
Drain peak current	$I_{D\ (pulse)}$ Note1	40	A
Body-drain diode reverse drain current	I_{DR}	20	A
Body-drain diode reverse drain peak current	$I_{DR\ (pulse)}$ Note1	40	A
Avalanche current	I_{AP} Note2	9	A
Avalanche energy	E_{AR} Note2	5.4	mJ
Channel dissipation	Pch Note3	65	W
Channel to case thermal impedance	θch-c	1.93	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%
2. STch = 25°C, Tch ≤ 150°C
3. Value at Tc = 25°C
4. Limited by maximum safe operation area

Electrical Characteristics

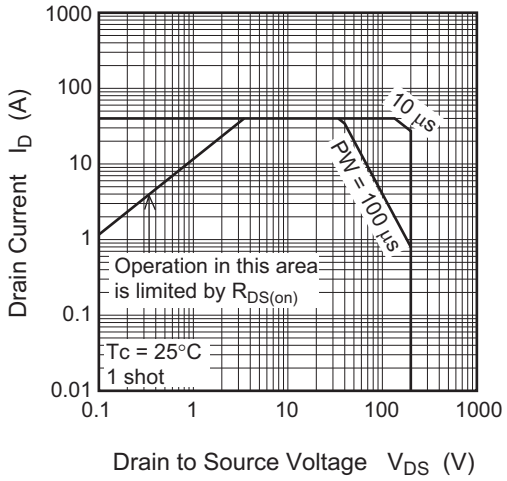
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	200	—	—	V	I_D = 10 mA, V_{GS} = 0
Zero gate voltage drain current	I_{DSS}	—	—	1	μA	V_{DS} = 200 V, V_{GS} = 0
Gate to source leak current	I_{GSS}	—	—	±1	μA	V_{GS} = ±30 V, V_{DS} = 0
Gate to source cutoff voltage	$V_{GS(off)}$	2.5	—	4.5	V	V_{DS} = 10 V, I_D = 1 mA
Static drain to source on state resistance	$R_{DS(on)}$	—	0.068	0.085	Ω	I_D = 10 A, V_{GS} = 10 V Note5
Input capacitance	Ciss	—	1200	—	pF	V_{DS} = 25 V V_{GS} = 0 f = 1 MHz
Output capacitance	Coss	—	200	—	pF	
Reverse transfer capacitance	Crss	—	30	—	pF	
Turn-on delay time	$t_{d(on)}$	—	17	—	ns	I_D = 10 A V_{GS} = 10 V R_L = 10 Ω Rg = 10 Ω
Rise time	t_r	—	18	—	ns	
Turn-off delay time	$t_{d(off)}$	—	25	—	ns	
Fall time	t_f	—	12	—	ns	
Total gate charge	Qg	—	19	—	nC	V_{DD} = 160 V V_{GS} = 10 V I_D = 20 A
Gate to source charge	Qgs	—	6	—	nC	
Gate to drain charge	Qgd	—	6	—	nC	
Body-drain diode forward voltage	V_{DF}	—	0.82	1.35	V	I_F = 20 A, V_{GS} = 0 Note5
Body-drain diode reverse recovery time	t_{rr}	—	123	—	ns	I_F = 20 A, V_{GS} = 0 di_F/dt = 100 A/μs

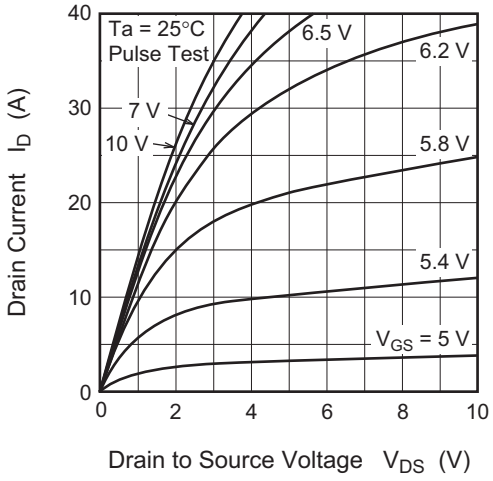
Notes: 5. Pulse test

Main Characteristics

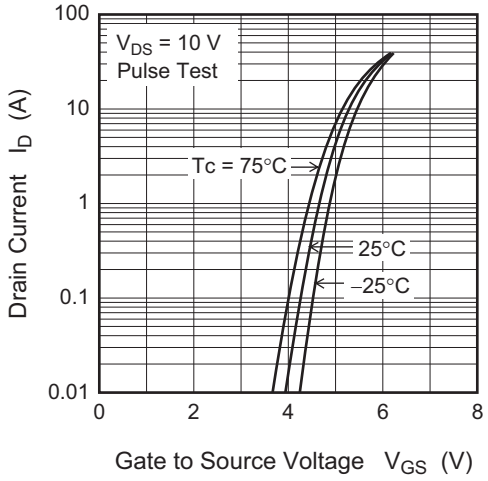
Maximum Safe Operation Area



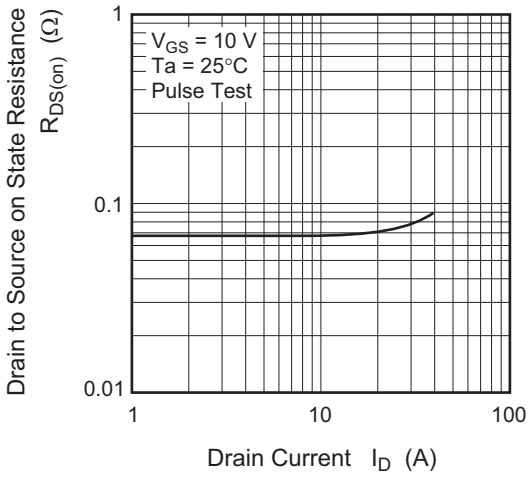
Typical Output Characteristics



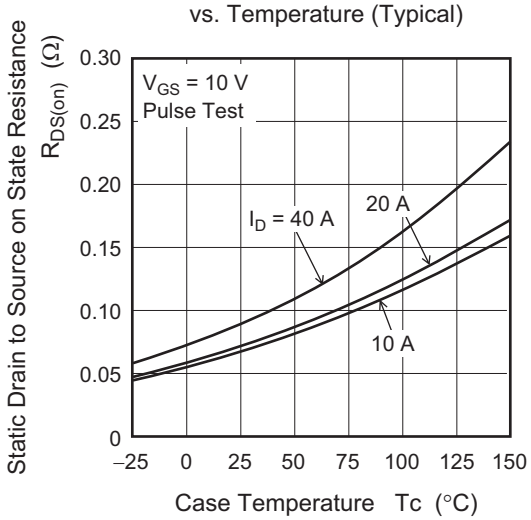
Typical Transfer Characteristics



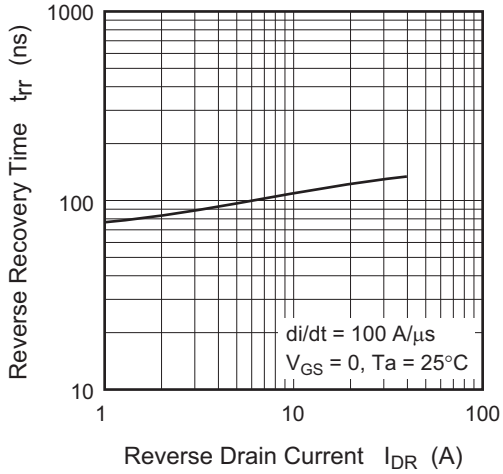
Static Drain to Source on State Resistance vs. Drain Current (Typical)



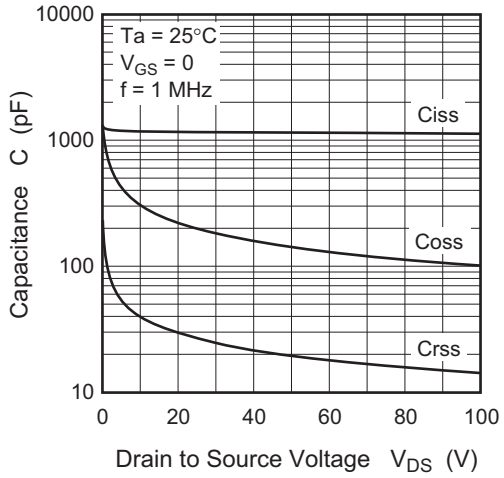
Static Drain to Source on State Resistance vs. Temperature (Typical)



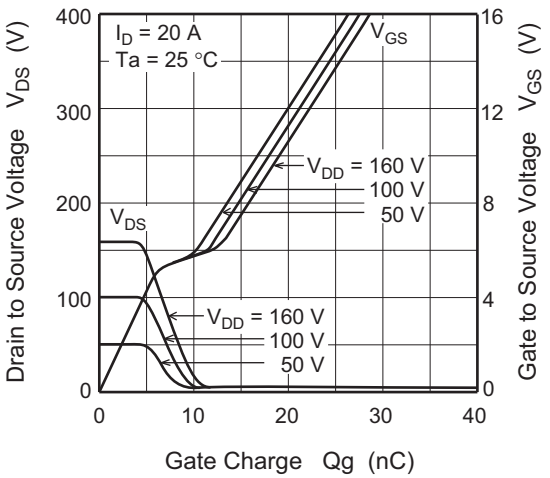
Body-Drain Diode Reverse Recovery Time (Typical)



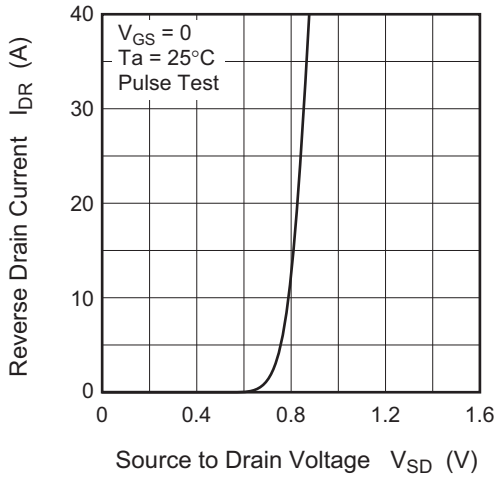
Typical Capacitance vs.
Drain to Source Voltage (Typical)



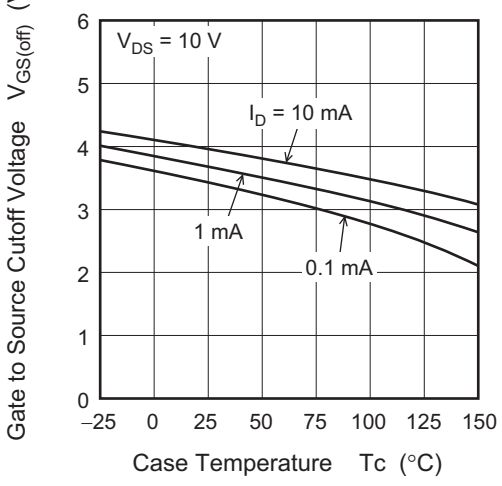
Dynamic Input Characteristics (Typical)



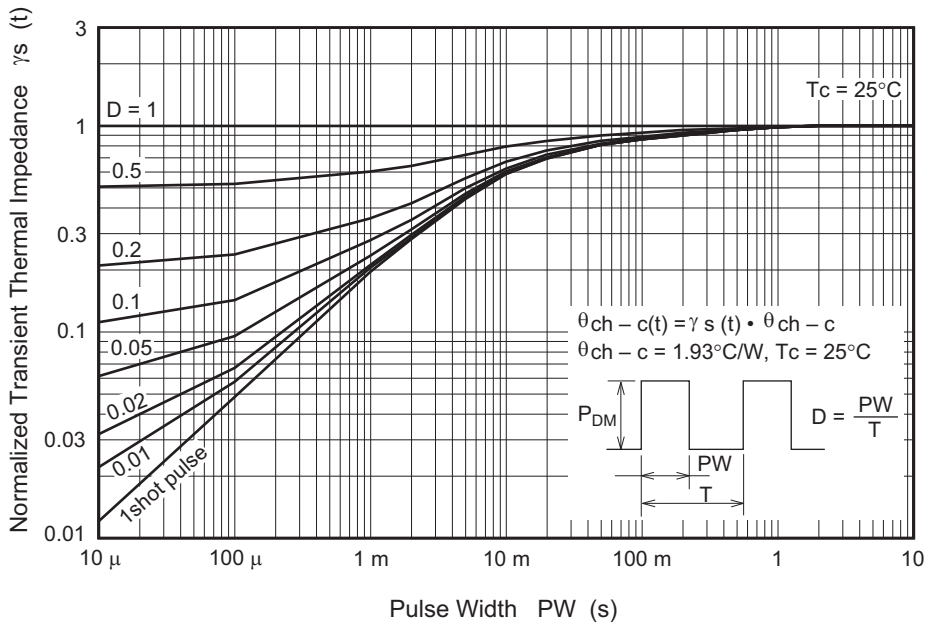
Reverse Drain Current vs.
Source to Drain Voltage (Typical)



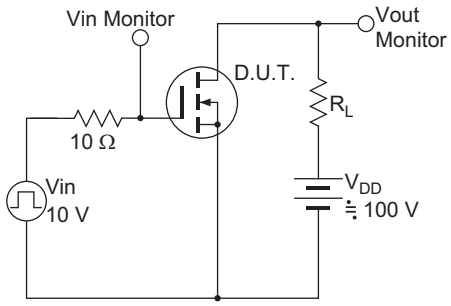
Gate to Source Cutoff Voltage
vs. Case Temperature (Typical)



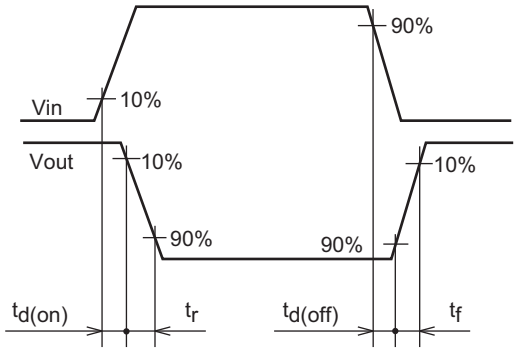
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



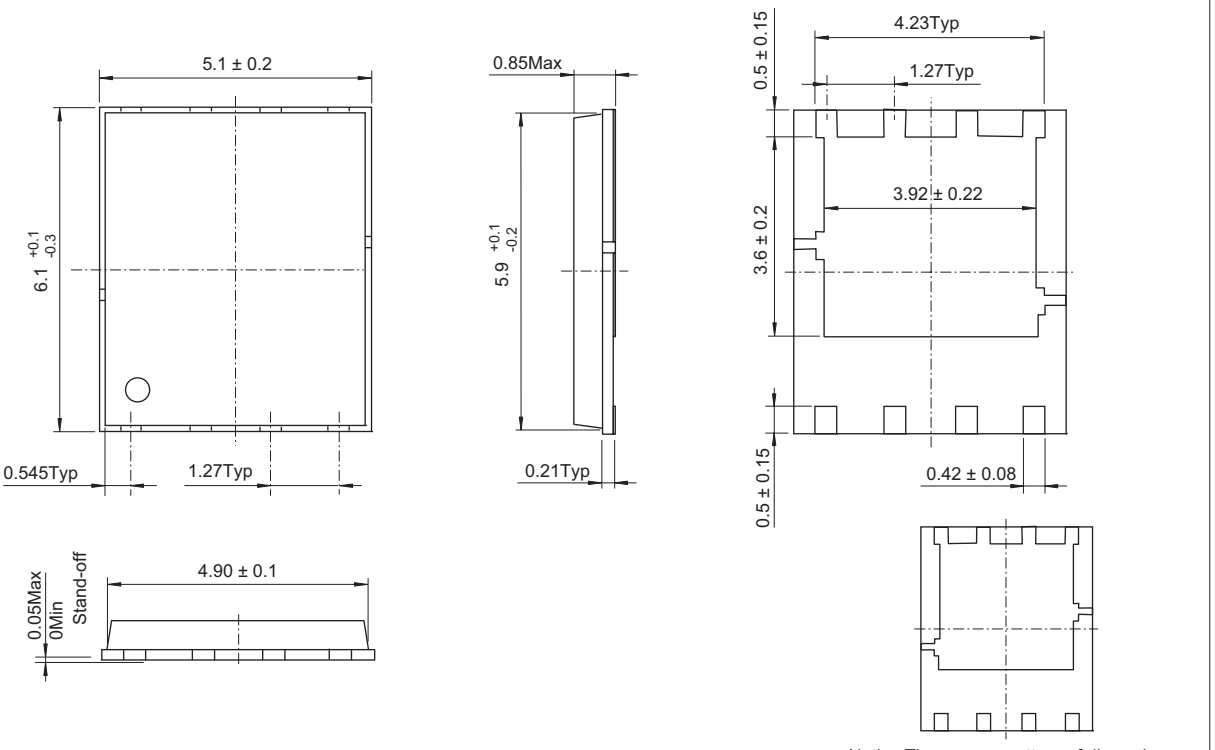
Waveform



Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
WPAK(3F)	—	PWSN0008DE-A	WPAK(3F)V	0.075g

Unit: mm



Notice:The reverse pattern of die-pad support lead described above exists.

Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJK2076DPA-00#J5A	3000 pcs	Taping

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