RJK5030DPP-M0

Silicon N Channel MOS FET
High Speed Power Switching

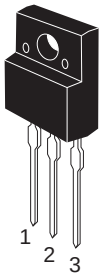
R07DS0227EJ0100
Rev.1.00
Dec 14, 2010

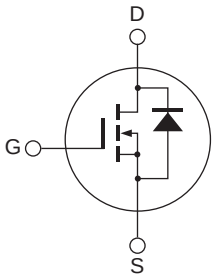
Features

- Low on-state resistance
  $R_{DS(on)}$ = 1.3 Ω typ. (at I_D = 2 A, V_{GS} = 10 V, Ta = 25°C)
- High speed switching

Outline

RENESAS Package code: PRSS0003AF-A
(Package name: TO-220FL)





1. Gate
2. Drain
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Drain to source voltage	V_{DSS}	500	V
Gate to source voltage	V_{GSS}	±30	V
Drain current	I_D	5	A
Drain peak current	$I_{D\ (pulse)}$ Note1	20	A
Avalanche current	I_{AP} Note3	5	A
Channel dissipation	Pch Note 2	28.5	W
Channel to case thermal Impedance	θch-c	4.38	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes: 1. Pulse width limited by safe operating area.
2. Value at Tc = 25°C
3. STch = 25°C, Tch ≤ 150°C

Electrical Characteristics

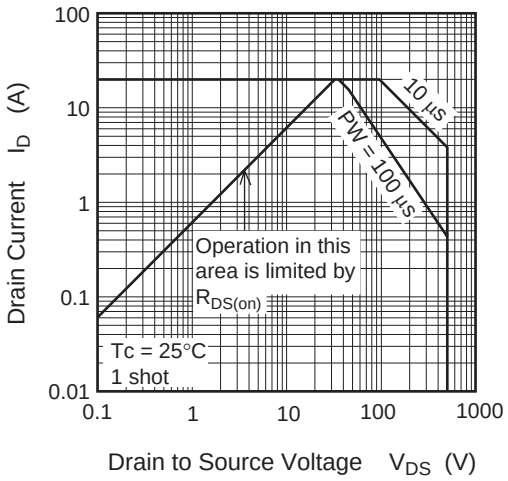
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)\ DSS}$	500	—	—	V	I_D = 1 mA, V_{GS} = 0
Zero gate voltage drain current	I_{DSS}	—	—	10	μA	V_{DS} = 500 V, V_{GS} = 0
Gate to source leak current	I_{GSS}	—	—	±0.1	μA	V_{GS} = ±30 V, V_{DS} = 0
Gate to source cutoff voltage	$V_{GS\ (off)}$	3.5	—	4.5	V	V_{DS} = 10 V, I_D = 1 mA
Static drain to source on state resistance	$R_{DS\ (on)}$	—	1.3	1.6	Ω	I_D = 2 A, V_{GS} = 10 V Note 4
Input capacitance	Ciss	—	550	—	pF	V_{DS} = 25 V
Output capacitance	Coss	—	60	—	pF	V_{GS} = 0
Reverse transfer capacitance	Crss	—	10	—	pF	f = 1 MHz
Turn-on delay time	$t_{d\ (on)}$	—	15	—	ns	V_{DD} = 200 V
Rise time	t_r	—	20	—	ns	I_D = 2 A
Turn-off delay time	$t_{d\ (off)}$	—	90	—	ns	V_{GS} = 10 V
Fall time	t_f	—	30	—	ns	Rg = 25 Ω
Total gate charge	Qg	—	13	—	nC	V_{DD} = 400 V
Gate to source charge	Qgs	—	3.3	—	nC	V_{GS} = 10 V
Gate to drain charge	Qgd	—	6.6	—	nC	I_D = 5 A
Body-drain diode forward voltage	V_{DF}	—	0.9	1.5	V	I_F = 5 A, V_{GS} = 0 Note 4
Body-drain diode reverse recovery time	t_{rr}	—	250	—	ns	I_F = 5 A, V_{GS} = 0 V_{DD} = 250 V di_F/dt = 100 A/μs

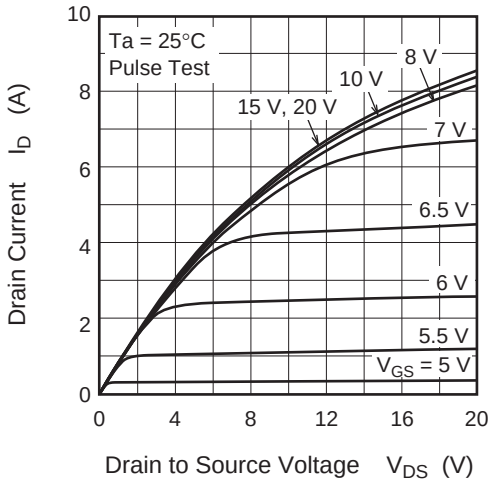
Note: 4. Pulse test

Main Characteristics

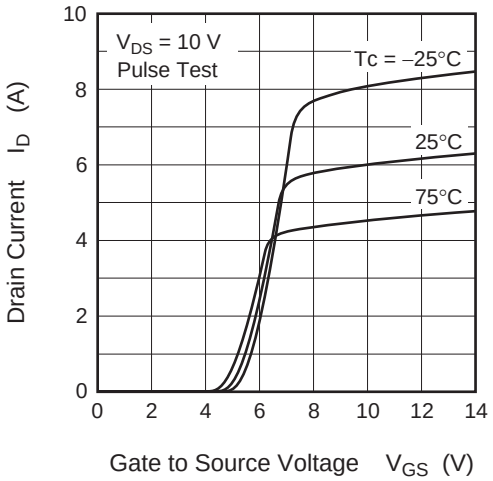
Maximum Safe Operation Area



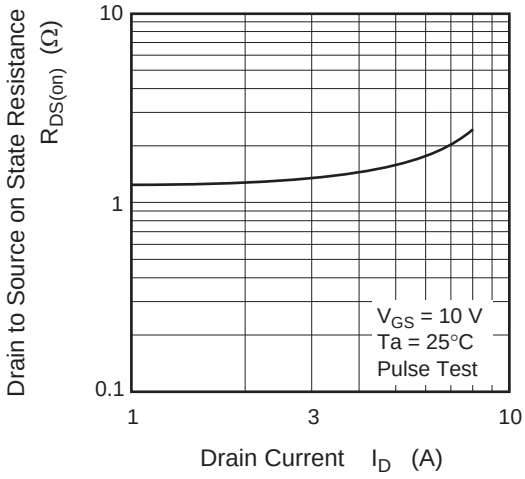
Typical Output Characteristics



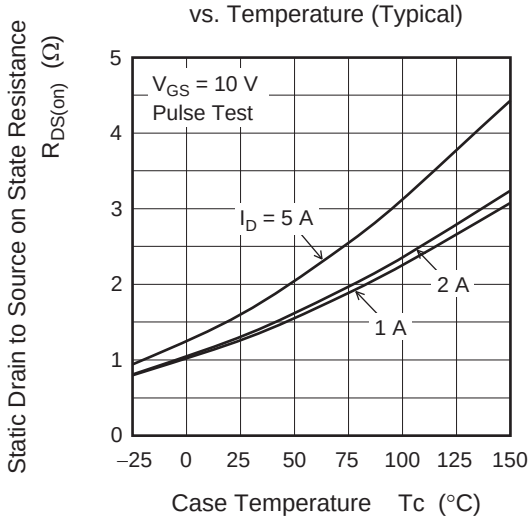
Typical Transfer Characteristics



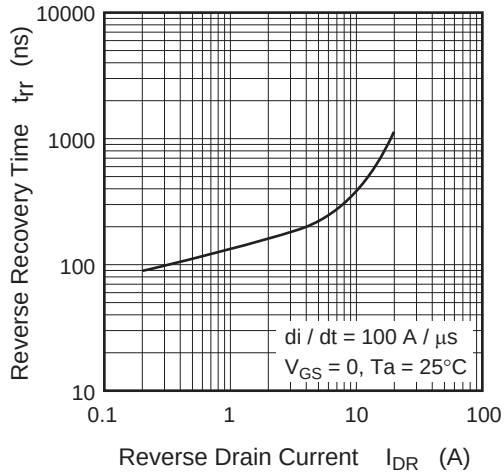
Static Drain to Source on State Resistance vs. Drain Current (Typical)



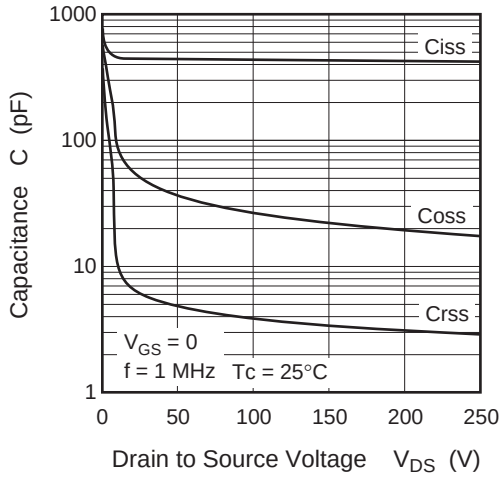
Static Drain to Source on State Resistance vs. Temperature (Typical)



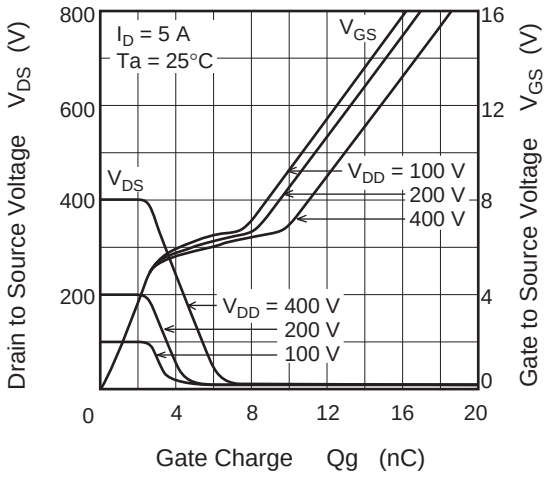
Body-Drain Diode Reverse Recovery Time (Typical)



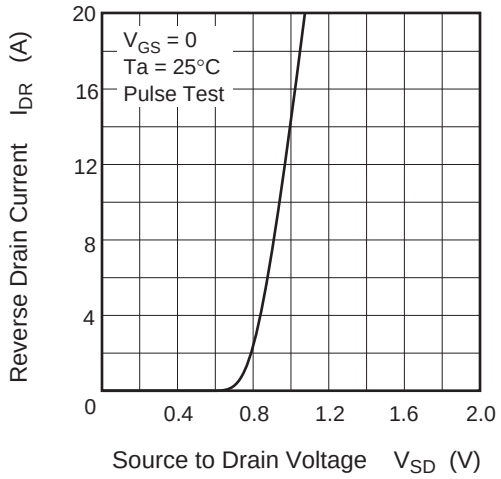
Typical Capacitance vs.
Drain to Source Voltage



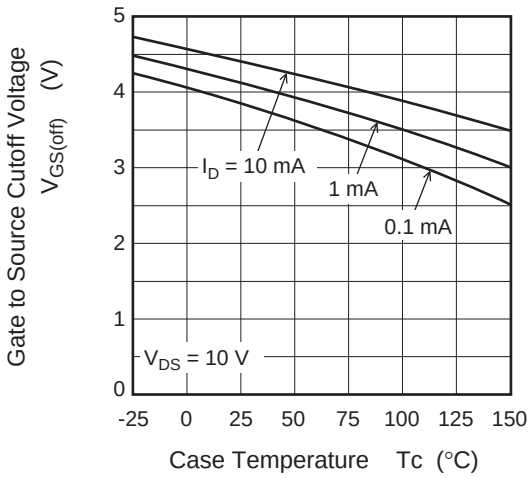
Dynamic Input Characteristics (Typical)



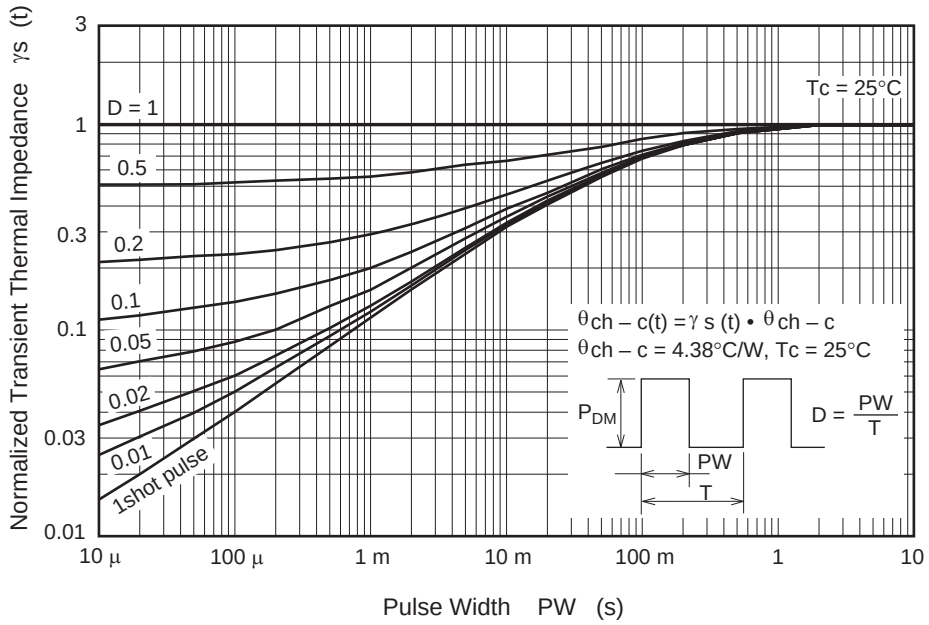
Reverse Drain Current vs.
Source to Drain Voltage (Typical)



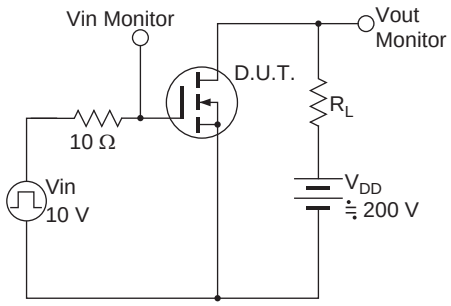
Gate to Source Cutoff Voltage
vs. Case Temperature (Typical)



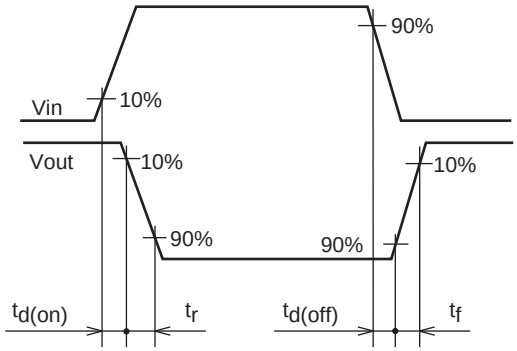
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



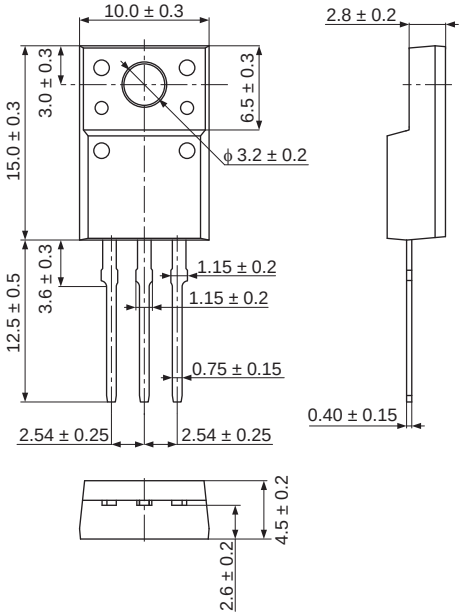
Waveform



Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
TO-220FL	—	PRSS0003AF-A	TO-220FL	1.5g

Unit: mm



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJK5030DPP-M0-T2	1050 pcs	Box (Tube)

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