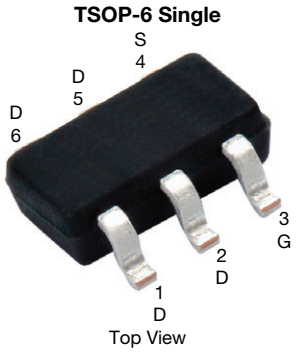
Automotive P-Channel 12 V (D-S) 175 °C MOSFET



Marking Code: 8UY

FEATURES

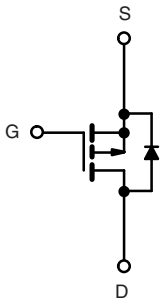
- TrenchFET® power MOSFET
- AEC-Q101 qualified [c]
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

AUTOMOTIVE GRADE

Pb-free

RoHS COMPLIANT

HALOGEN FREE



P-Channel MOSFET

PRODUCT SUMMARY	
V_{DS} (V)	-12
$R_{DS(on)}$ (Ω) at V_{GS} = -4.5 V	0.025
$R_{DS(on)}$ (Ω) at V_{GS} = -2.5 V	0.032
$R_{DS(on)}$ (Ω) at V_{GS} = -1.8 V	0.043
I_D (A)	-8
Configuration	Single

ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free and halogen-free	SQ3461EV (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V_{DS}	-12	V
Gate-source voltage		V_{GS}	± 8	
Continuous drain current [c]	T_C = 25 °C	I_D	-8	A
	T_C = 125 °C		-6.6	
Continuous source current (diode conduction)		I_S	-6.3	
Pulsed drain current [a]		I_{DM}	-30	
Single pulse avalanche current	L = 0.1 mH	I_{AS}	-17	
Single pulse avalanche energy		E_{AS}	14	mJ
Maximum power dissipation [a]	T_C = 25 °C	P_D	5	W
	T_C = 125 °C		1.67	
Operating junction and storage temperature range		T_J, T_{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS

PARAMETER		SYMBOL	LIMIT	UNIT
Junction to ambient	PCB mount [b]	R_{thJA}	110	°C/W
Junction to case (drain)		R_{thJF}	30	

Notes

a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %
b. When mounted on 1" square PCB (FR4 material)
c. Package limited

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SPECIFICATIONS (T_C = 25 °C, unless otherwise noted)							
PARAMETER	**SYMBOL**	**TEST CONDITIONS**		**MIN.**	**TYP.**	**MAX.**	**UNIT**
Static							
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = -250 µA		-12	-	-	V
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = -250 µA		-0.4	-0.6	-1	
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 8 V		-	-	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{GS} = 0 V	V_{DS} = -12 V	-	-	-1	µA
		V_{GS} = 0 V	V_{DS} = -12 V, T_J = 125 °C	-	-	-50	
		V_{GS} = 0 V	V_{DS} = -12 V, T_J = 175 °C	-	-	-150	
On-State Drain Current [a]	$I_{D(on)}$	V_{GS} = -4.5 V	V_{DS} = -5 V	-11	-	-	A
Drain-Source On-State Resistance [a]	$R_{DS(on)}$	V_{GS} = -4.5 V	I_D = -7.9 A	-	0.021	0.025	Ω
		V_{GS} = -4.5 V	I_D = -6.6 A, T_J = 125 °C	-	-	0.033	
		V_{GS} = -4.5 V	I_D = -3.5 A, T_J = 175 °C	-	-	0.037	
		V_{GS} = -2.5 V	I_D = -7 A	-	0.026	0.032	
		V_{GS} = -1.8 V	I_D = -3 A	-	0.036	0.043	
Forward Transconductance [b]	g_{fs}	V_{DS} = -5 V, I_D = -7.9 A		-	21	-	S
Dynamic [b]							
Input Capacitance	C_{iss}	V_{GS} = 0 V	V_{DS} = -6 V, f = 1 MHz	-	1600	2000	pF
Output Capacitance	C_{oss}			-	620	770	
Reverse Transfer Capacitance	C_{rss}			-	490	620	
Total Gate Charge [c]	Q_g	V_{GS} = -4.5 V	V_{DS} = -6 V, I_D = -7.9 A	-	21	28	nC
Gate-Source Charge [c]	Q_{gs}			-	2.5	-	
Gate-Drain Charge [c]	Q_{gd}			-	7	-	
Gate Resistance	R_g	f = 1 MHz		2.8	5.7	8.6	Ω
Turn-On Delay Time [c]	$t_{d(on)}$	V_{DD} = -6 V, R_L = 1.6 Ω $I_D \cong$ -7.9 A, V_{GEN} = -4.5 V, R_g = 1 Ω		-	12	17	ns
Rise Time [c]	t_r			-	52	68	
Turn-Off Delay Time [c]	$t_{d(off)}$			-	92	120	
Fall Time [c]	t_f			-	71	93	
Source-Drain Diode Ratings and Characteristics [b]							
Pulsed Current [a]	I_{SM}			-	-	-20	A
Forward Voltage	V_{SD}	I_F = -2 A, V_{GS} = 0 V		-	-0.8	-1.2	V

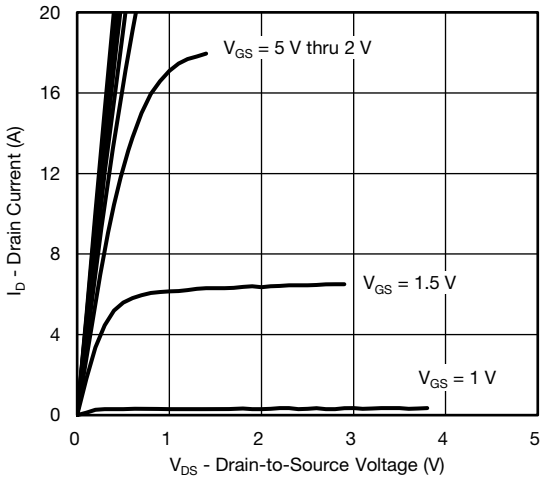
Notes

a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

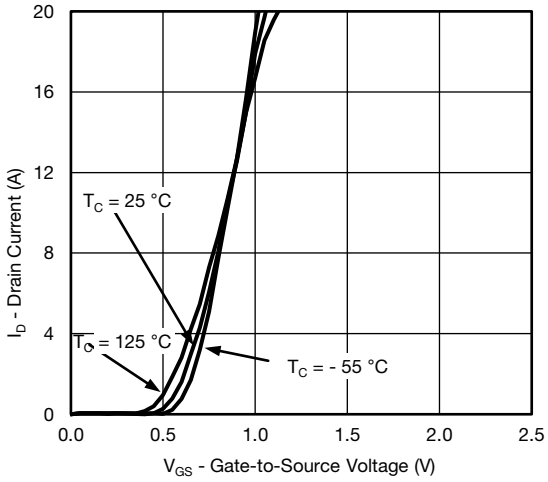
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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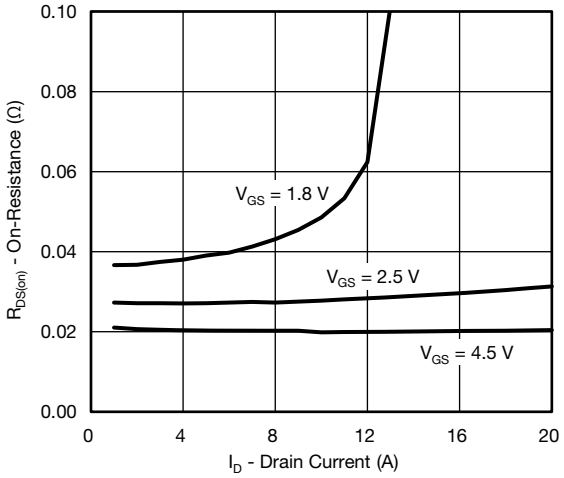
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



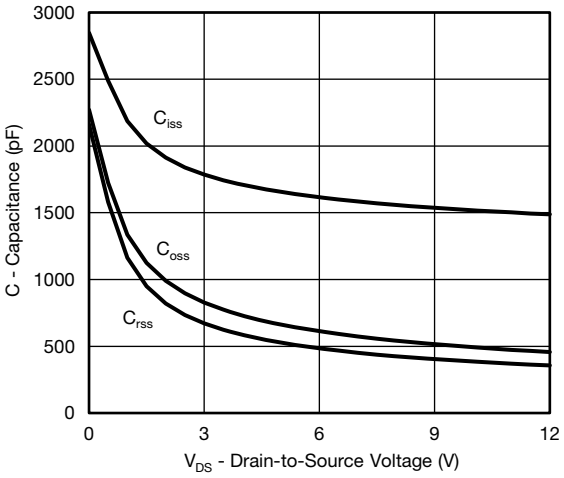
Output Characteristics



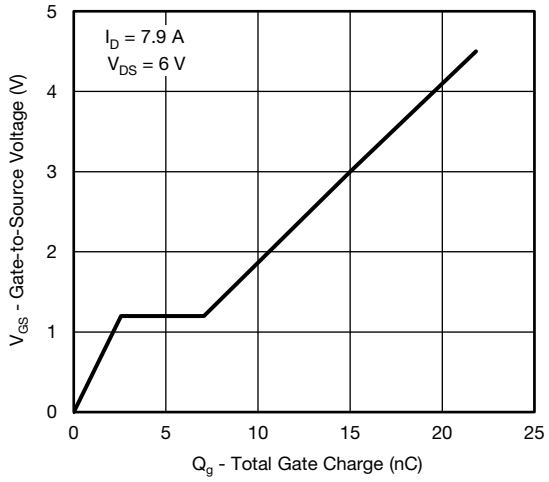
Transfer Characteristics



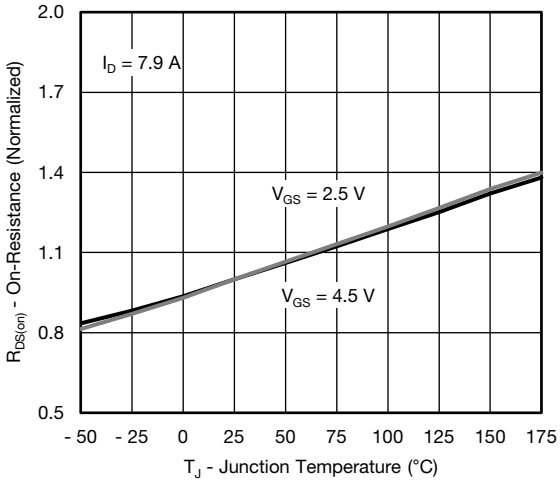
On-Resistance vs. Drain Current



Capacitance



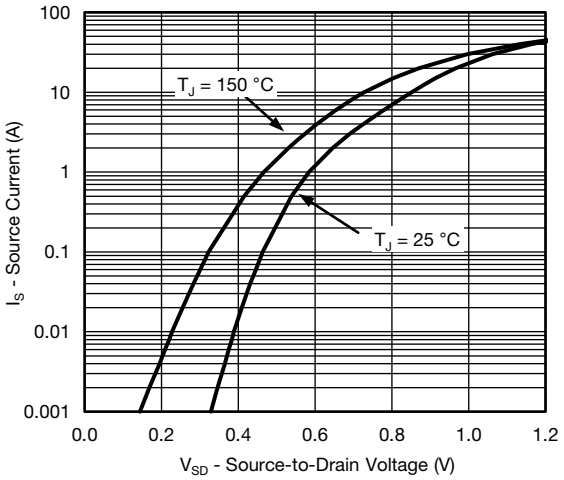
Gate Charge



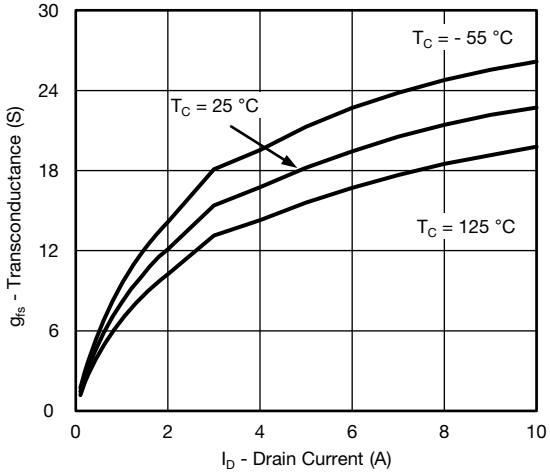
On-Resistance vs. Junction Temperature

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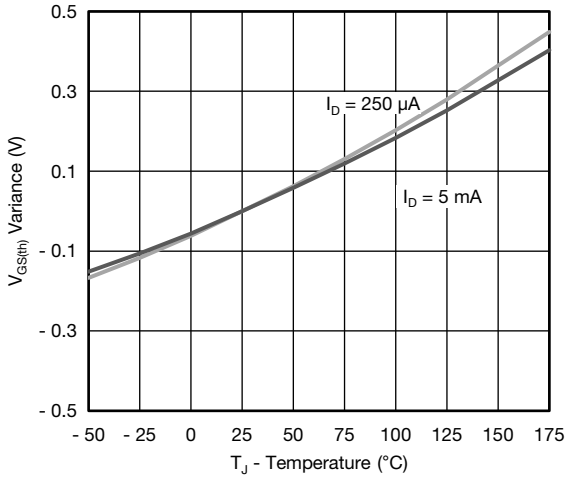
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



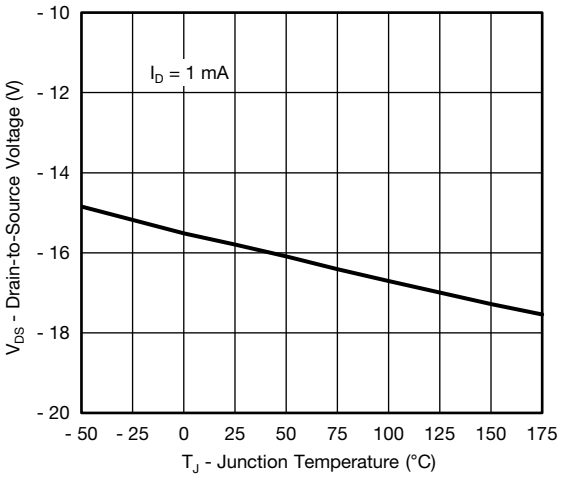
Source-Drain Diode Forward Voltage



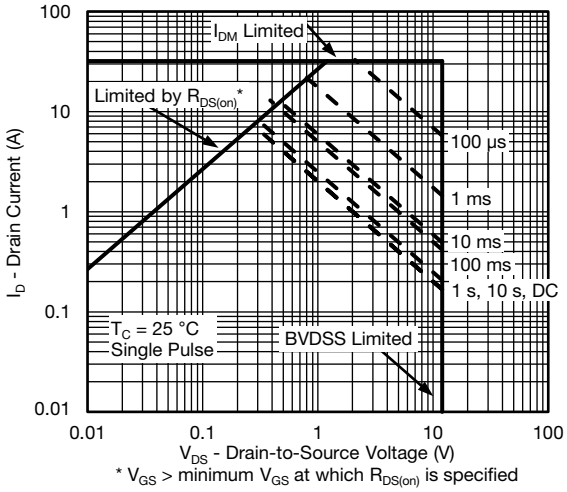
Transconductance



Threshold Voltage



Drain-to- Source Voltage vs. Junction Temperature



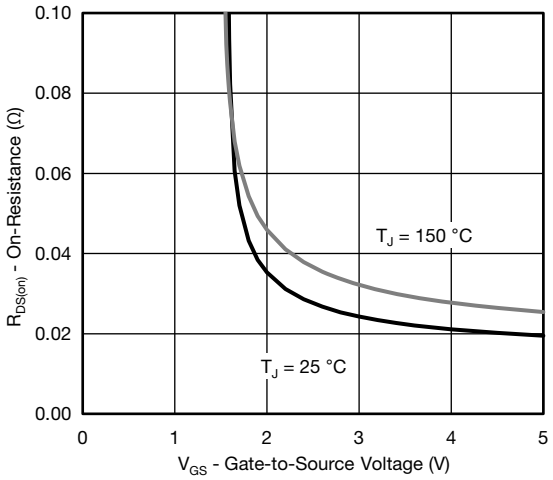
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

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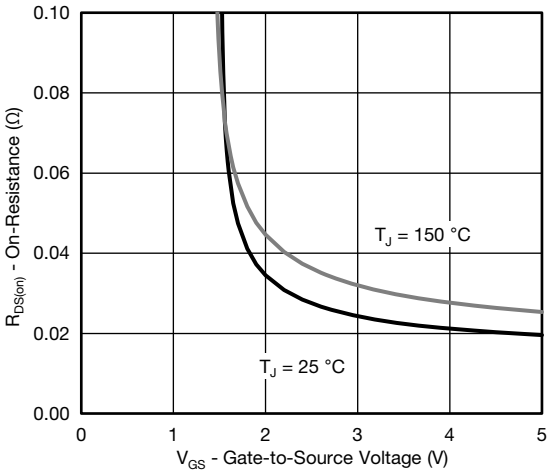
Safe Operating Area

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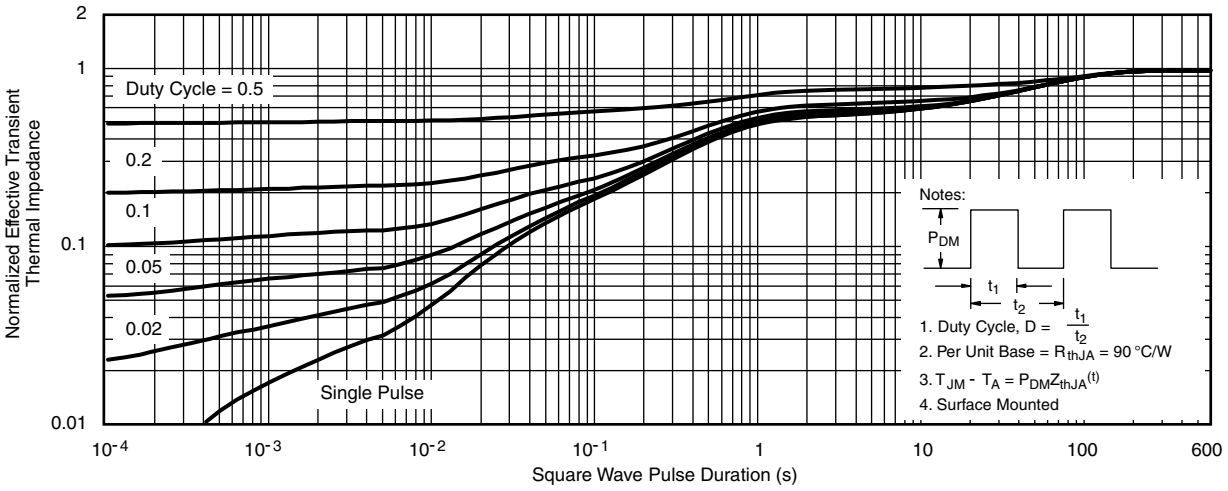
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



On-Resistance vs. Gate-to-Source Voltage (7.9 A)



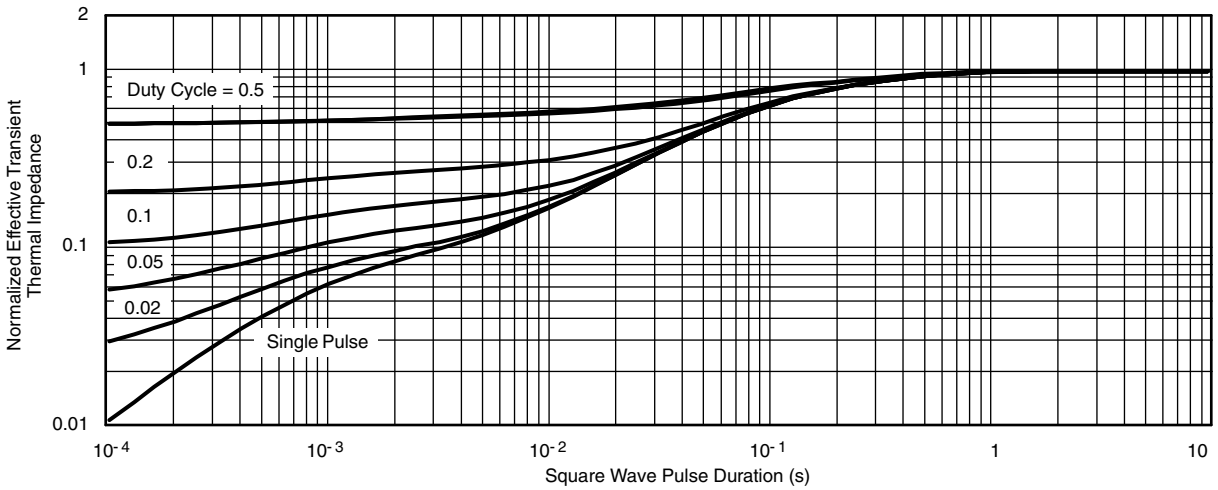
On-Resistance vs. Gate-to-Source Voltage (6.6 A)



Normalized Thermal Transient Impedance, Junction-to-Ambient

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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

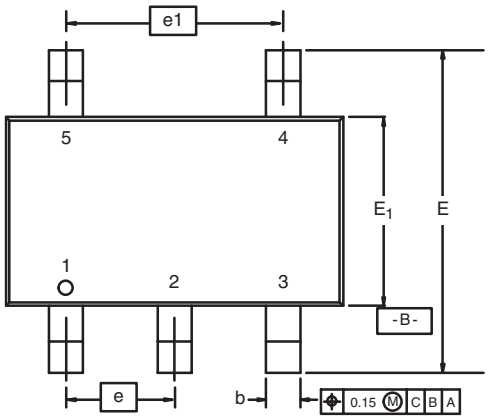
- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

  are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

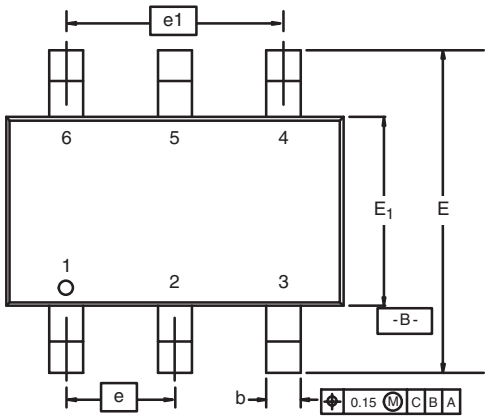
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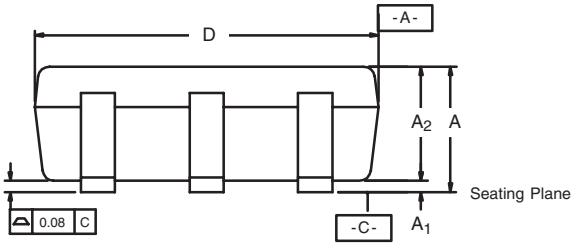
TSOP: 5/6–LEAD
JEDEC Part Number: MO-193C

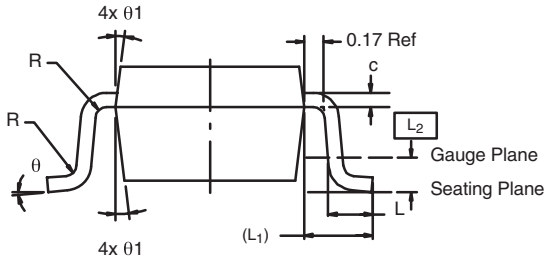


5-LEAD TSOP



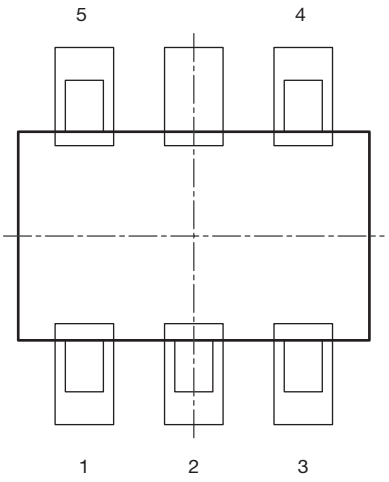
6-LEAD TSOP



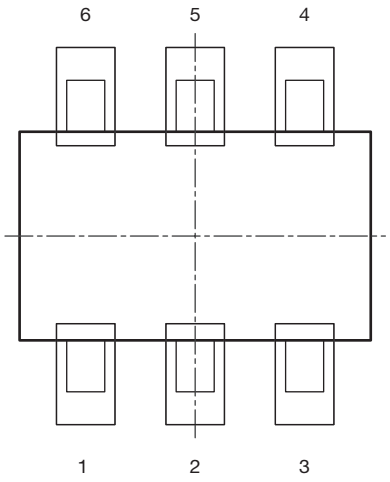


Dim	MILLIMETERS			INCHES		
	Min	Nom	Max	Min	Nom	Max
A	0.91	-	1.10	0.036	-	0.043
A_1	0.01	-	0.10	0.0004	-	0.004
A_2	0.90	-	1.00	0.035	0.038	0.039
b	0.30	0.32	0.45	0.012	0.013	0.018
c	0.10	0.15	0.20	0.004	0.006	0.008
D	2.95	3.05	3.10	0.116	0.120	0.122
E	2.70	2.85	2.98	0.106	0.112	0.117
E_1	1.55	1.65	1.70	0.061	0.065	0.067
e	0.95 BSC			0.0374 BSC		
e_1	1.80	1.90	2.00	0.071	0.075	0.079
L	0.32	-	0.50	0.012	-	0.020
L_1	0.60 Ref			0.024 Ref		
L_2	0.25 BSC			0.010 BSC		
R	0.10	-	-	0.004	-	-
θ	0°	4°	8°	0°	4°	8°
θ_1	7° Nom			7° Nom		
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540						

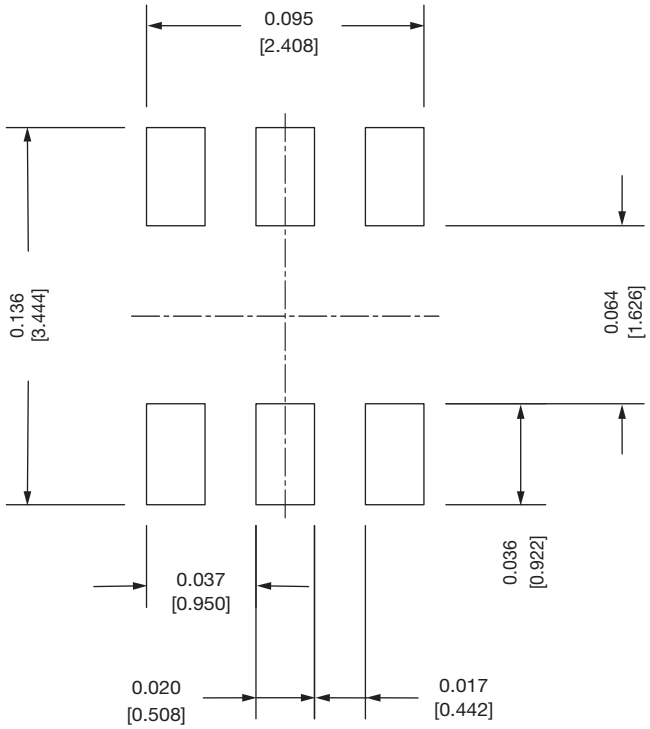
Recommended Land Pattern For TSOP-5L / TSOP-6L



TSOP 5L



TSOP 6L



Note

- All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022 DWG: 3010

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